









TOP Slot1 TOP Slot2



	J2	
5V <u>1</u>		2_3V3_M1
GND_3		4 GND
SDA_1_5_		6_SCL_1
MOSI_1_7_	L	<u>8</u> MISO_1
SPICLK_1_9_	L	<u>10</u> SS_1
RXD_1_11		12_TXD_1
DioNext_1 <u>13</u>		14~
AioNext_1 <u>15</u>		160
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BOTTOM Slot1 BOTTOM Slot2

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	J4	
5V_1_1_		<u>2</u> 3V3_1
GND <u>3</u>	_	4_GND
SDA <u>5</u>	L	<u>6</u> SCL
MOSI <u>7</u>	L	<u>8</u> MIS0
SPI_CLK_9		<u>10</u> SS
RXD_MCU_11		12 TXD_MCU
DioCrnt_13		14 DioNext
AioCrnt <u>15</u>	L	<u>16</u> AioNext
		l

	J1	
5V <u>1</u>		2_3V3_M1
GND_3		4_GND
SDA_1_5_		6_SCL_1
MOSI_1_7_		8_MISO_1
SPICLK_1 9		10 SS_1
RXD_1 11		12 TXD_1
DioCrnt_1 13		14 DioNext_1
AioCrnt_1 15		16_AioNext_1

BC95



