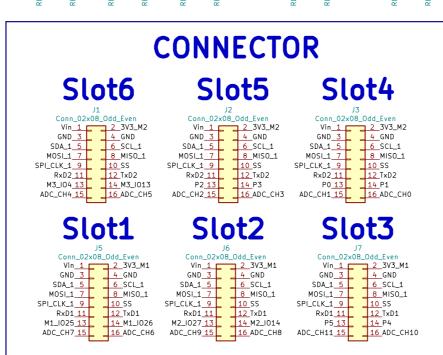


SPI\_CLK\_1 MOSI\_1 MISO\_1



ADC\_CH7

ADC\_CH9

ADC\_CH11

ADC\_CH8

ADC\_CH10

R39 0R P7\_OUT R40 C56 C58 NA NA NA

D35 D37

D35 D37

D37

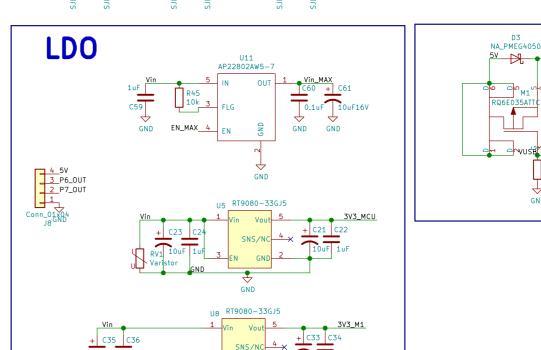
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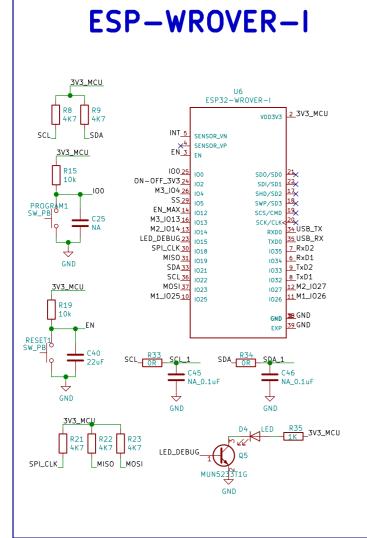
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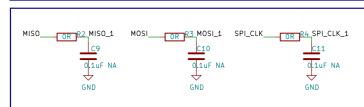
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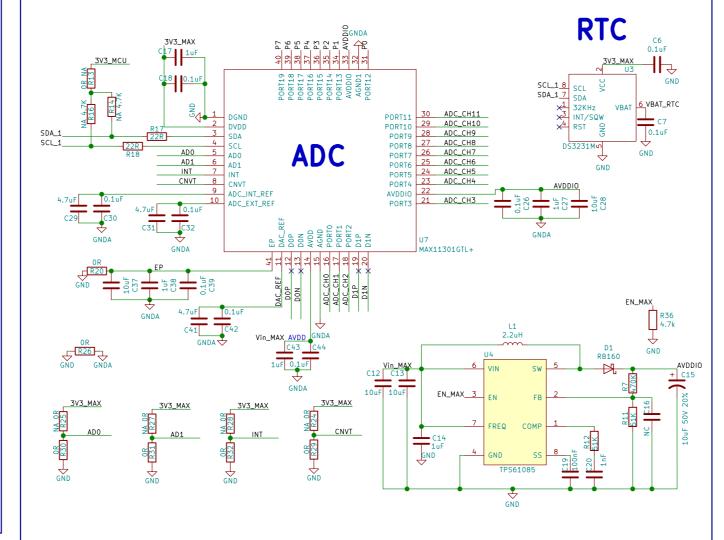
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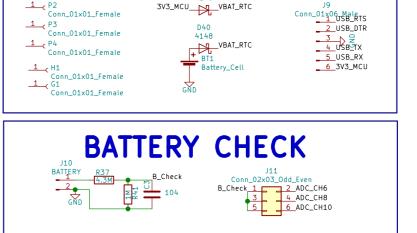
D12A10LC











D39 4148 3V3\_MCU\_\_\_\_\_VBAT\_RTC

