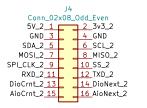


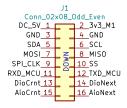
## TOP Slot1 TOP Slot2 TOP Slot5 TOP Slot6

J3				J2				
Conn_02x08_Odd_Even				Conn_02x08_Odd_Even				
5V_2_1_		<u>2</u> 3v3_2		$\Box$	DC_5V_1_		2_3v3_M1	
GND_ <u>3</u>		4_GND		V	GND <u>3</u>		4_GND	
SDA_2_ <u>5</u>	L _	6_SCL_2			SDA <u>5</u>	_	<u>6</u> SCL	
MOSI_2 <u>7</u>	L .	8_MISO_2			MOSI <u>7</u>		<u>8</u> MISO	
SPI_CLK_2_9	L .	<u>10</u> SS_2			SPI_CLK_9	ᆸᄟ	<u>10</u> SS	
RXD_2 <u>11</u>	L .	12_TXD_2		F	RXD_MCU_11		12_TXD_MCU	
DioNext_2 <u>13</u>	L _	14			DioNext_13	_	14	
AioNext_2 <u>15</u>	L .	16			AioNext <u>15</u>		16	
							· · ·	

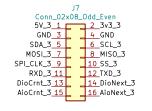
J8			J6				
Conn_02x08_Odd_Even			Conn_02x08_Odd_Even				
5V_4 <u>1</u>		<u>2</u> 3v3_4	5V_3 <u>1</u>		<u>2</u> 3v3_3		
GND_3		4_GND	GND <u>3</u>		4_GND		
SDA_4_5_	L _	6_SCL_4	SDA_3 <u>5</u>		<u>6</u> _SCL_3		
MOSI_4_7_	L _	8_MISO_4	MOSI_3_7_		<u>8</u> MIS0_3		
1_CLK_4 <u>9</u>	L _	<u>10</u> SS_4	SPI_CLK_3_9		<u>10</u> SS_3		
RXD_4 <u>11</u>	_	12_TXD_4	RXD_3 <u>11</u>		12_TXD_3		
oNext_4 <u>13</u>		14~	DioNext_3 <u>13</u>		14		
oNext_4 <u>15</u>	L _	16	AioNext_3 <u>15</u>		16		
		^					

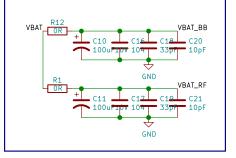
## BOTTOM Slot1 BOTTOM Slot2 BOTTOM Slot5 BOTTOM Slot6

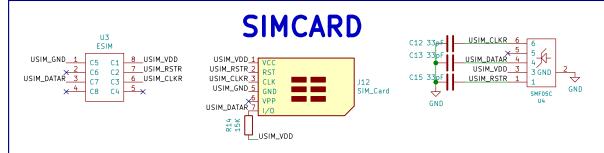




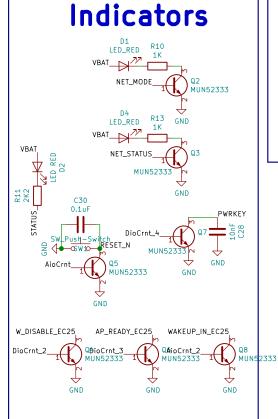








## **Antenna** ANT\_MAIN ANT\_MAIN C5 = == <u>VBAT</u> ANT\_GNSS GND ANT\_GNSS\_ C24 GND GND ANT\_DIV ANT\_DIV\_ C23



## Level Shifter C29 0.1uF 3v3\_M1vDD\_EXT 3v3\_M1 U5 74LVC2G07-ABIIZEUS GND

