

pkt

FR pkt

(1)

(4)

Stateful switch

(2)

m1:

f1: { c_1^1 , t_1^1 , FR_1^1 }

f2: { c_1^2 , t_1^2 , FR_1^2 }

...

fn: { c_1^n , t_1^n , FR_1^n }

...

mk:

f1: { c_k^1 , t_k^1 , FR_k^1 }

f2: { c_k^2 , t_k^2 , FR_k^2 }

...

fn: { c_k^n , t_k^n , FR_k^n }

(5)

- **Remove FR from FR pkt and pkt reads/writes states**

(3)

- **Tag FR to pkt**
- **Pkt reads/writes states**