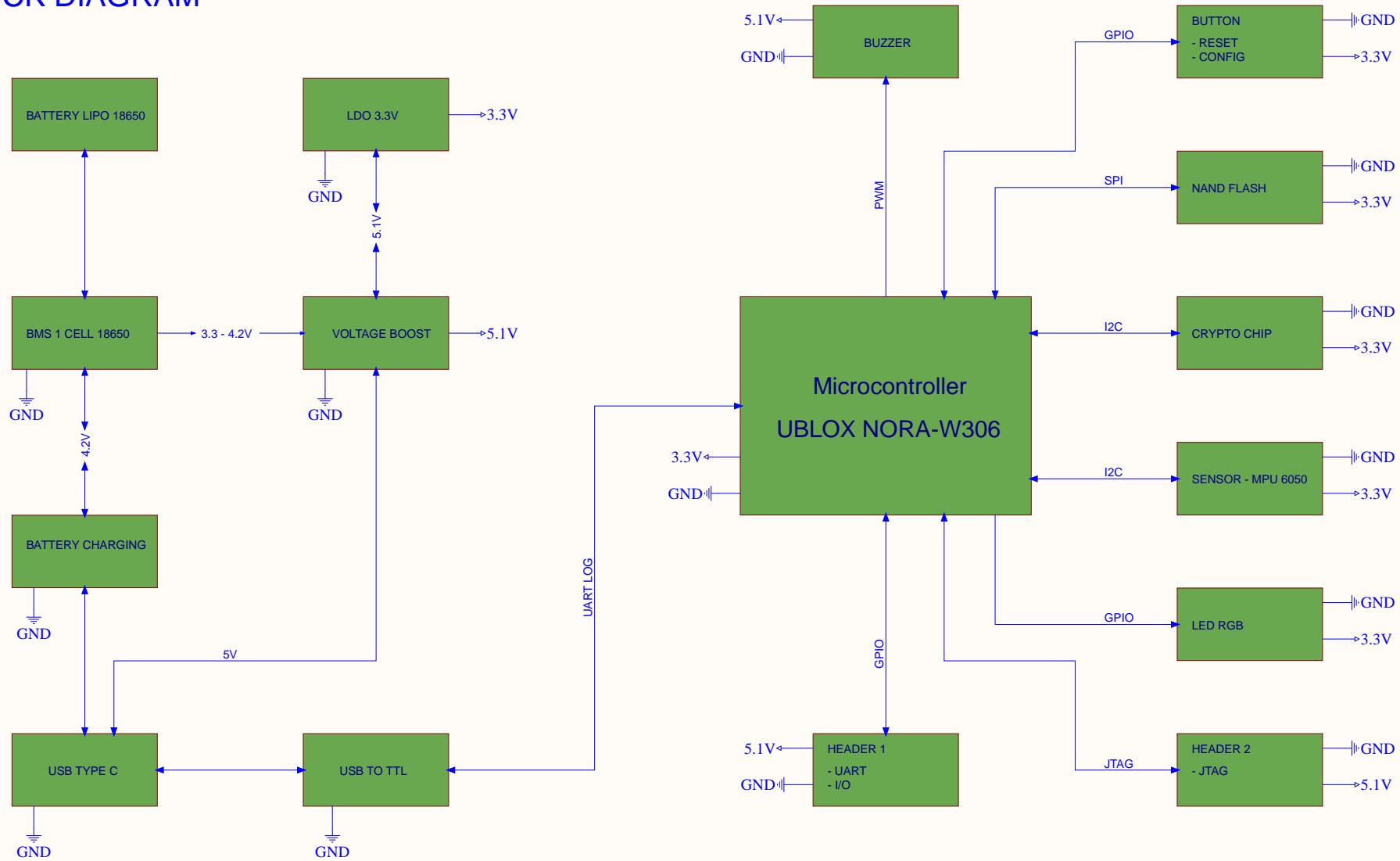


BLOCK DIAGRAM



Title BLOCK DIAGRAM		
Size	Number	Revision
A2		1.0
Date:	Tue-05-12-2023	Sheet 1 of 4
File:	D:\15.FL\BLOCK_DIAGRAM.SchDk	Drawn By:

GPIO TABLE

MCU Pin	MCU Function	DESCRIBE
A5	UART LOG TX	Uart download. Connect to usb to ttl (cp2102)
A6	UART LOG RX	
J2	SWD_CLK	Emulator, connect to debug header
H2	SWD_DATA	
J3	nRESET	Connect to Reset button
G8	GPIO	Connect to LED RGB
G9	GPIO	
E9	GPIO	
F8	HS_USI_I2C_SDA	Connect to crypto chip and mpu 6050
F9	HS_USI_I2C_SCL	
E8	LP_UART_RXD	Connect to log header
D8	LP_UART_TXD	
J8	SPI1_MISO	Connect to extend flash
H8	SPI1_CS	
H9	SPI1_CLK	
J9	SPI1_MOSI	
C8	LP_PWM3	Connect to Buzzer
B1	GPIO	Connect to log header
C2	GPIO	Connect to Button 1
C1	GPIO	Connect to pin INT of mpu 6050

HISTORY TABLE

No.	Date	Content	Note
1	05/12/2023	New release drawing version 1.0	

Title		HIISTORY TABLE		
Size	Number			Revision
A2				1.0
Date:	Tue-05-12-2023	Sheet	2	of 4
File:	D:\15_FL\HIISTORY TABLE\SchDoc	Drawn By:		

