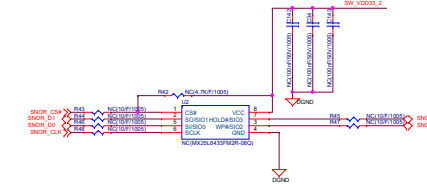


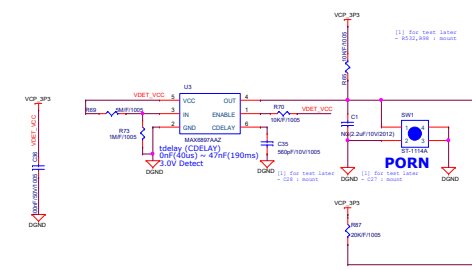
CLOCK



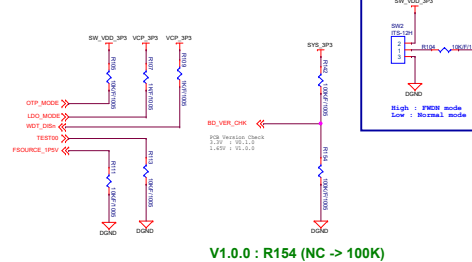
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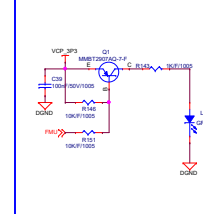
PORN / RST



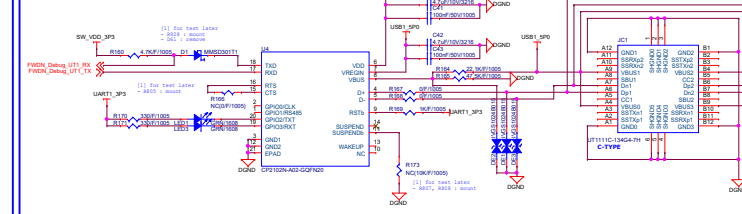
SETTING & Ver. Check



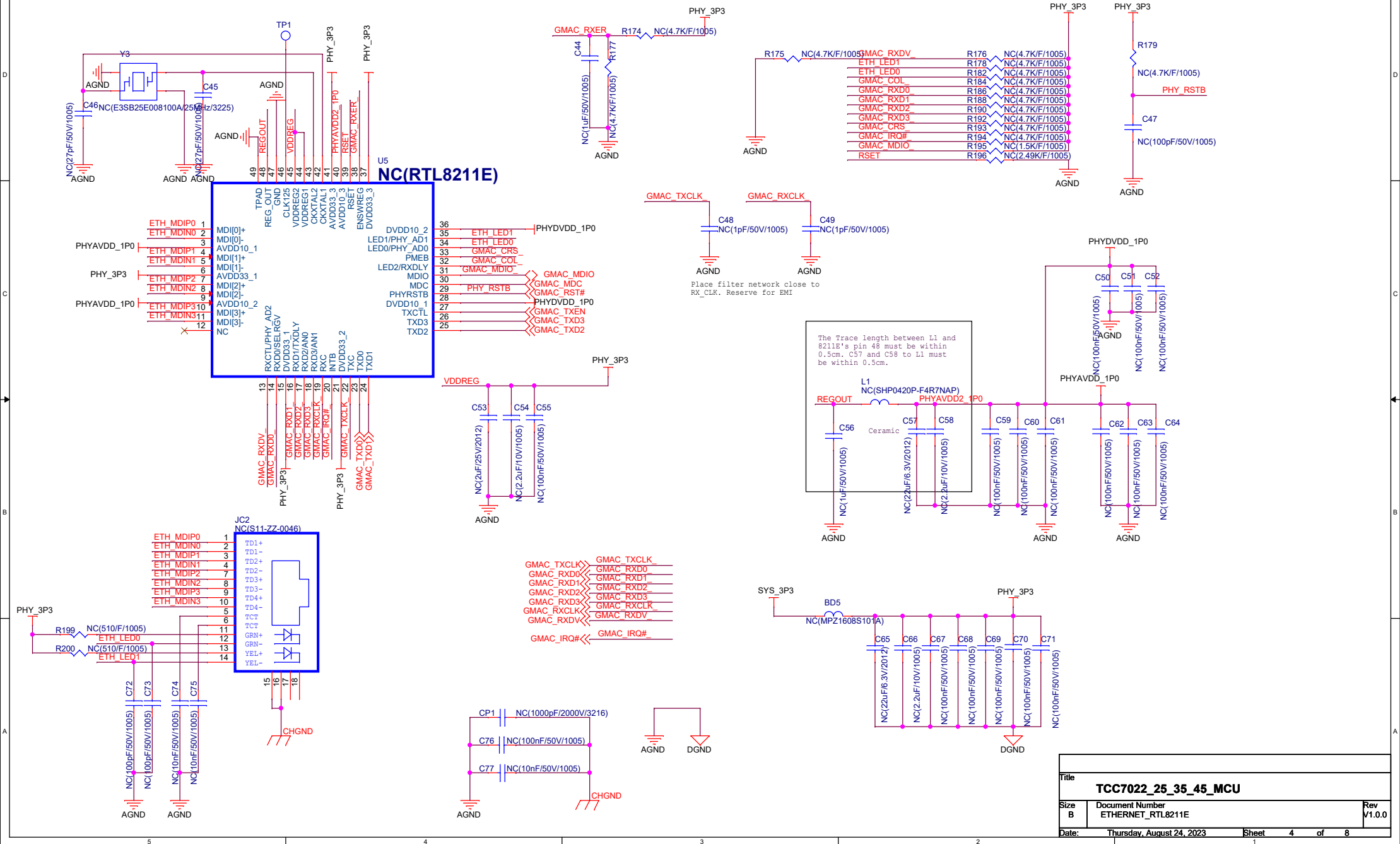
FMU



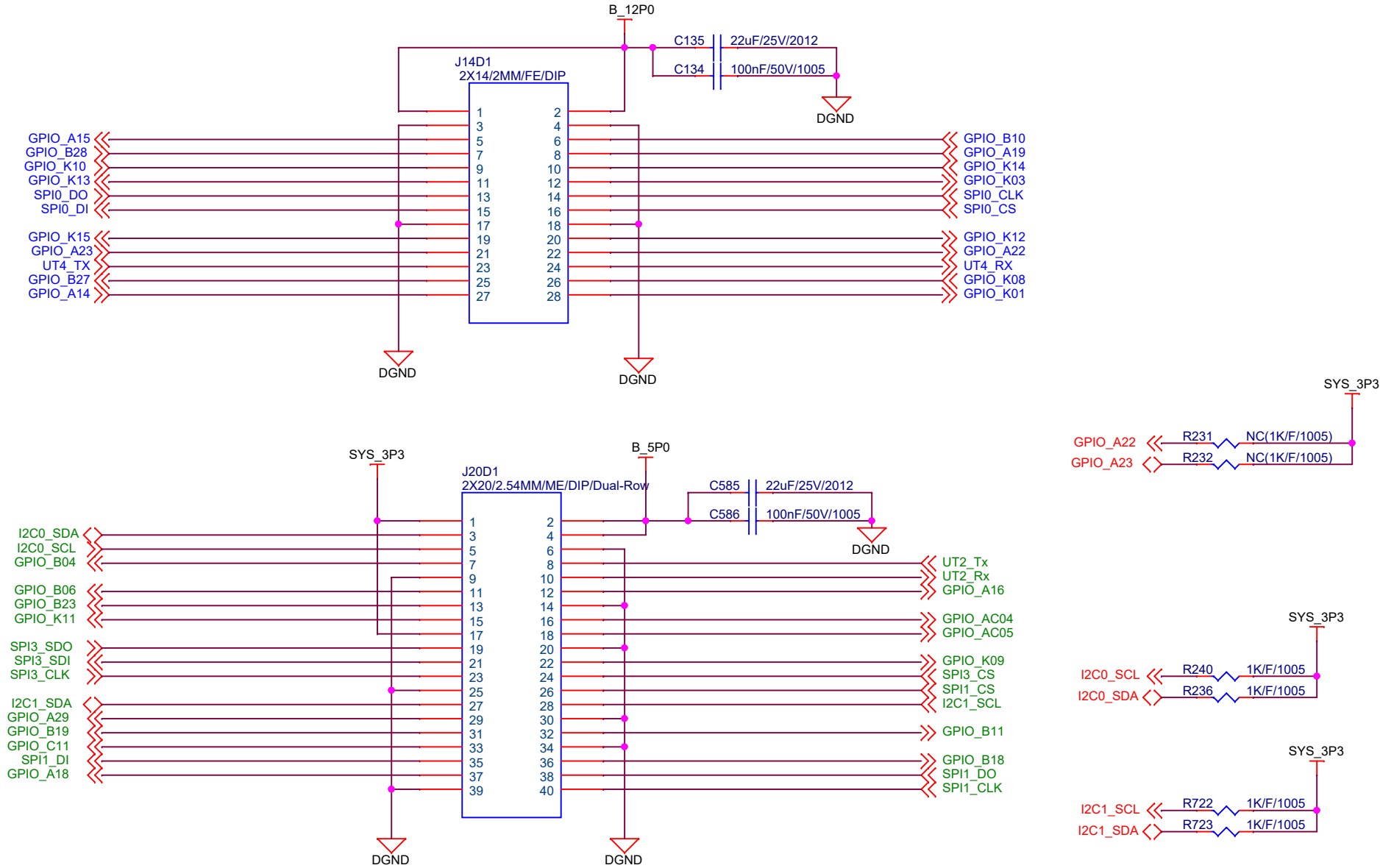
FWDN



ETHERNET PHY



External CON



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Voltage Detector	
Detection Voltage	3.89V
Release Voltage	4.16V



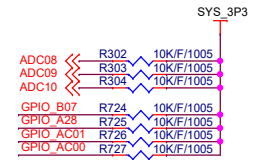
Voltage Detector	
Detection Voltage	3.99V
Release Voltage	4.25V



V1.0.0 : SW7 소자 변경

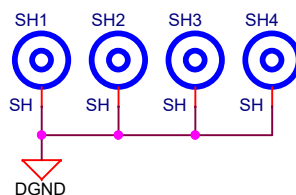
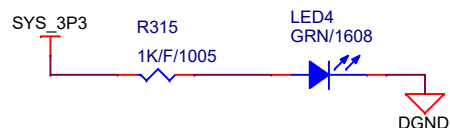
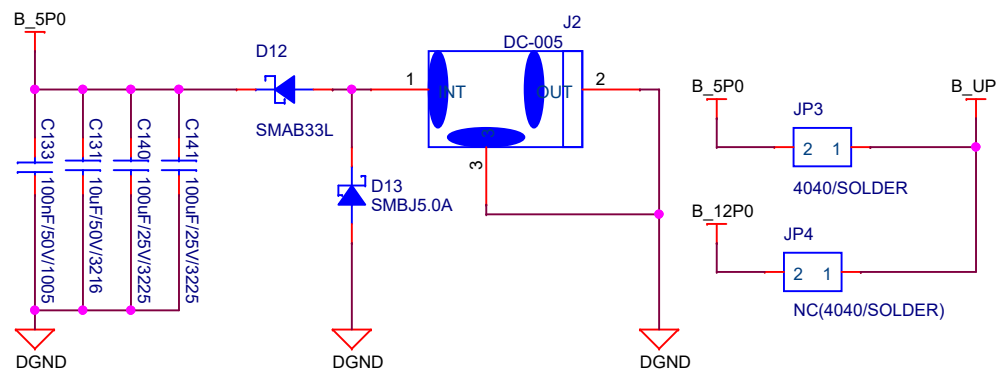
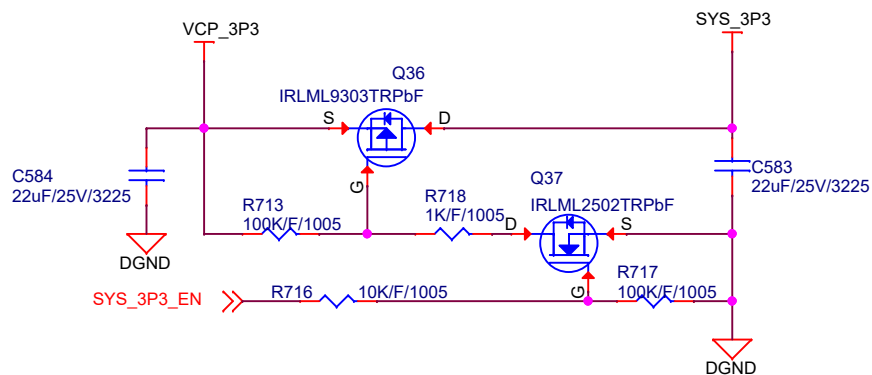
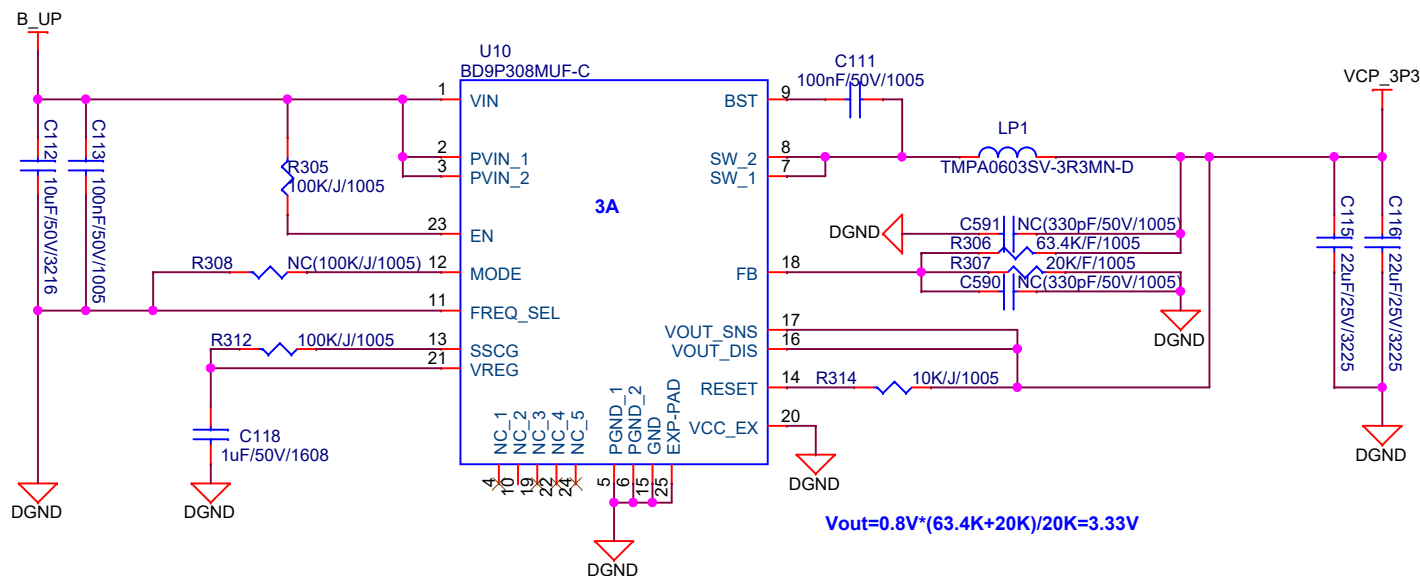
V1.0.0 : SW8 소자 변경, D14 추가

V1.0.0 : R728 저항 추가



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VCP Power



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