Solar panels configuration, evaluation.

Attendees: NJB, Henrik Madsen, Søren Holdesen, Lars Vesterager, Jacob, Jørgen V and Karsten M.

Purpose of the meeting:

Try to secure the changes not has any drawback and are usable in all existing solar panel configurations.

We discussed about the "short" that is handmade on the first DualCell panel in a series: It is today a small piece off Kovar tabs that is modified and hand soldered on the printed circuit board. SKH stated that if the design is changed we need to make an evaluation about stability over time.

Under the development of the DualCell board it is discovers an unwanted "feature": The screw holes is connecting the feedback line directly to the aluminum mounting plate. Not a problem in the configurations we have done until now but might be an unwanted "feature" when new designs are coming up.

The design is on the PCB changed to have an opportunity to mount either a resistor or a capacitor between feedback line and mounting hole.

It is concluded that a 0 ohm resistor (foot print for) wil be mounted on P2C and S2C to make it possible to make the connection from/to feedback line on this board.

It is validated that the new positions for P2C/I2C and GSSB can be positioned on the satellite (DualCell PCB) with no complains / problems.

Remark: If GSSB is changed we might need a new testfixture.

/Karsten