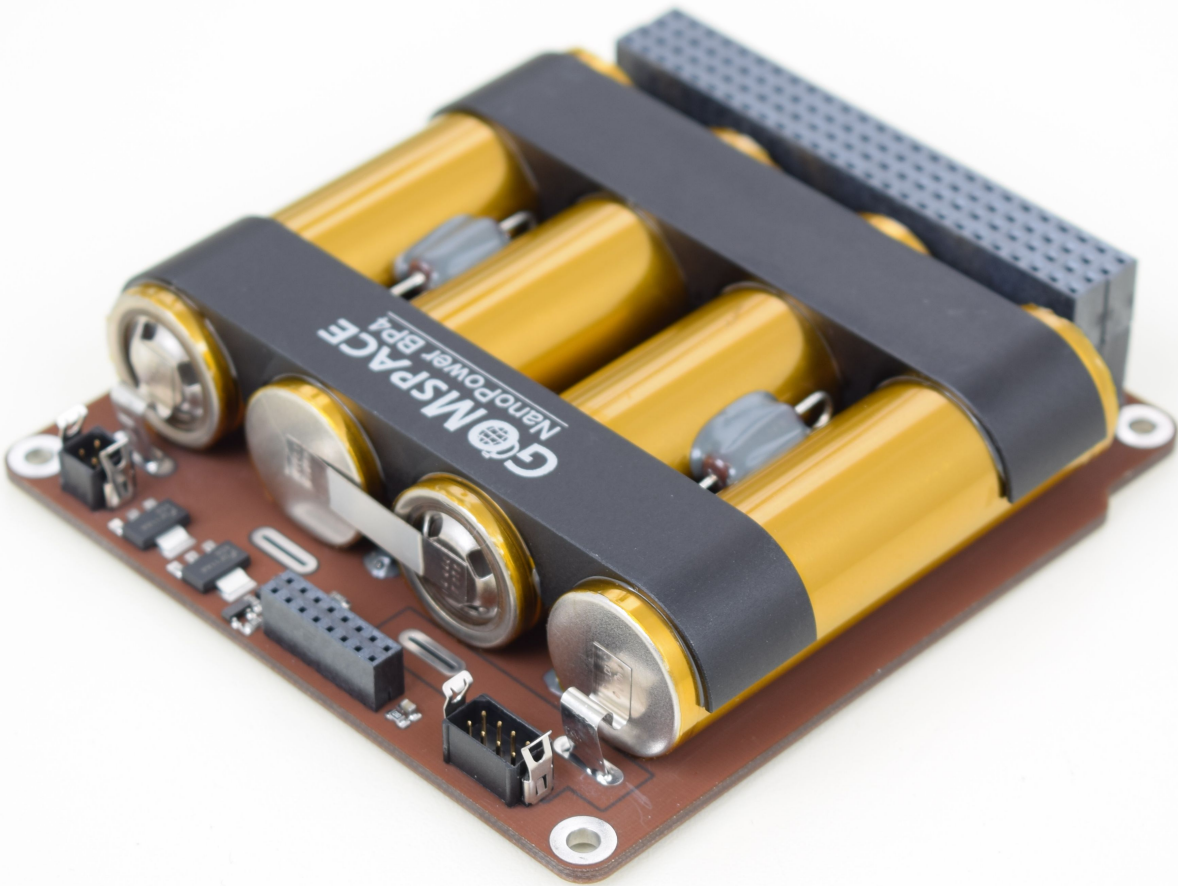


NanoPower BP4



DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

DESIGN NOTE:
Example text for debug notes.

Confidential - DO NOT DISTRIBUTE
Copyright © 2016 by GomSpace ApS
Alfred Nobels Vej 21C, 1.
9220 Aalborg East
Denmark

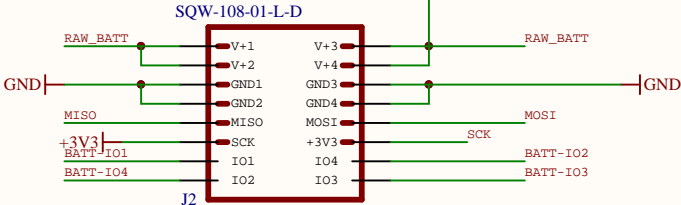
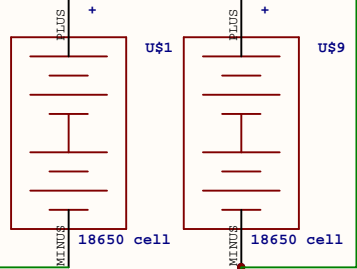
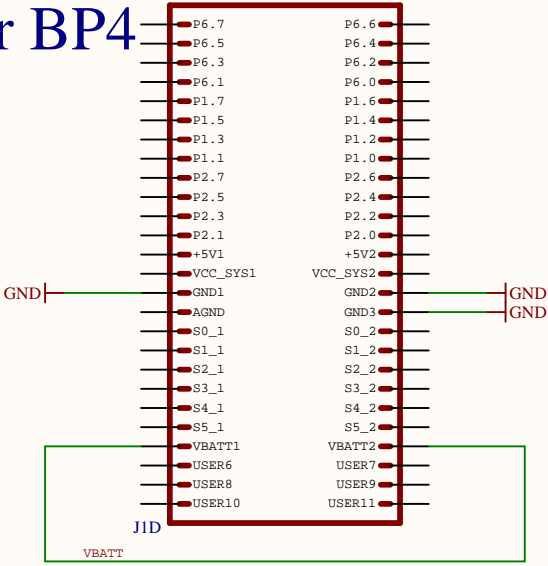
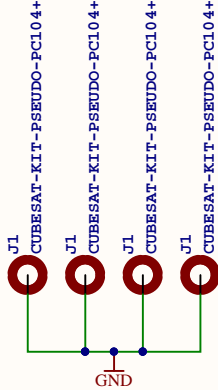
NanoPower BP4	
Author: *	
BOM Variant: [No Variations]	
Date: 25-09-2018 08:38:48	
Project: nanopower-bp4-v.1.6.PrjPcb	
EDA build number: 1.1.9.86	Revision: 1.6
File: coverpage.SchDoc	Sheet: */*



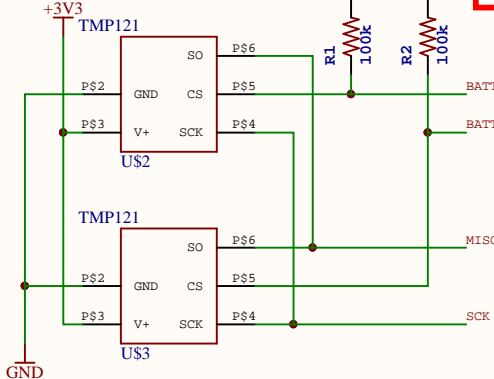
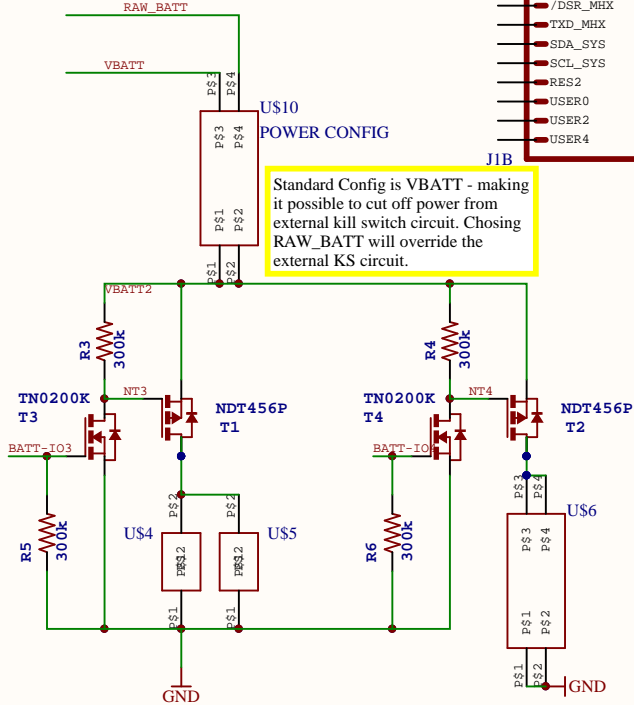
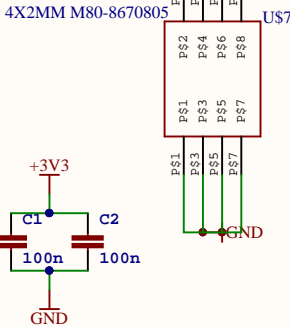
NanoPower BP4

STACKCONNECTOR

STACKCONNECTOR



Marking on symbol wrong: SCK is +3V3 and +3V3 is SCK - i.e. net labels are correct



Confidential - DO NOT DISTRIBUTE
Copyright © 2016 by GomSpace ApS
Alfred Nobels Vej 21C, 1.
9220 Aalborg East
Denmark

NanoPower BP4	
Author: *	
BOM Variant: [No Variations]	
Date: 25-09-2018 08:38:49	
Project: nanopower-bp4-v.1.6.PrjPcb	
EDA build number: 1.1.9.86	Revision: 1.6
File: nanopower-bp4-v.1.6.SchDoc	Sheet: */*

