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CHECKED IN		
	Date:	02-02-2018
	Program & PCB Name:	NanoPower PDU400
	CID:	100696
	Rev:	1
	PCB Designer:	Thomas Tarp Hansen
	PCB Specifications:	
	Base Specification:	IPC-A-6012 cl. 3
	Material:	Glass/polyimide (GI) IPC-4101/40 Arlon 85N or Arlon 35N
	Construction:	High Density Interconnect (1+n+1)
	Layer count:	12
	Stackup detailes:	See Stack-up sheet
	Special processes:	
☑	Notation Top	White low-outgassing epoxy
☑	Notation Bottom	White low-outgassing epoxy
☑	Nickel/Hard Gold edge plating:	See Gold plated mechanical layer in files included below.
☐	Vias in pad has to be filled and capped	All drilled vias
☑	All Microvia has to be with copper filling.	Unless otherwise agreed
☑	Surface finish:	Hot Oil reflow SnPb - unless otherwise agreed
☐	Nickel/Hard gold contacts:	See Gold plated mechanical layer in files included below.
☑	Panelization	Use Gomspace standard cluster template - unless otherwise agreed
☑	Minimum isolation distance:	150um
☐	Countersunk holes	All 2.5 mm holes countersunk by 90 degrees to 5.5mm opening from the buttom
☑	PCB manufacturer logo - **Not allowed**	unless otherwise agreed
☑	Stencil data required	Stencil data must be based on compensated production files
☑	Electrically test to be done.	
☐	Peelable Solder Mask:	
☐	Impedance controlled nets	

Files included in data package

	File Description	File Name	Format	Comments
☑	Read-Me File	ReadMe.pdf	ACROBAT	This Document
☑	Outline (Mechanical 4)	P80-PDU_HV_default.GM4	OBD++	Board Outline
☑	Hard gold top	P80-PDU_HV_default.GM6	ODB++	Hard gold
☑	Hard gold bottom	P80-PDU_HV_default.GM7	OBD++	Hard gold
☑	Silkscreen Top	P80-PDU_HV_default.GM5	ODB++	Notation Top - white low-outgassing epoxy
☑	Top Paste	P80-PDU_HV_default.GTP	OBD++	Top Paste
☑	Top Side Components	P80-PDU_HV_default.GTL	ODB++	L1 in stackup
☑	Signal/Power Layer 1	P80-PDU_HV_default.G1	OBD++	L2 in stackup
☑	Signal/Power Layer 2	P80-PDU_HV_default.G2	ODB++	L3 in stackup
☑	Signal/Power Layer 3	P80-PDU_HV_default.G3	OBD++	L4 in stackup
☑	Signal/Power Layer 4	P80-PDU_HV_default.G4	ODB++	L5 in stackup
☑	Signal/Power Layer 5	P80-PDU_HV_default.G5	OBD++	L6 in stackup
☑	Signal/Power Layer 6	P80-PDU_HV_default.G6	ODB++	L7 in stackup
☑	Signal/Power Layer 7	P80-PDU_HV_default.G7	OBD++	L8 in stackup
☑	Signal/Power Layer 8	P80-PDU_HV_default.G8	ODB++	L9 in stackup
☑	Signal/Power Layer 9	P80-PDU_HV_default.G9	OBD++	L10 in stackup
☑	Signal/Power Layer 10	P80-PDU_HV_default.G10	ODB++	L11 in stackup
☑	Bottom Side Components	P80-PDU_HV_default.GBL	OBD++	L12 in stackup
☑	Bottom Paste	P80-PDU_HV_default.GBP	OBD++	Bottom Paste
☑	ODB++	P80-PDU_HV_default.zip	ODB	Netlist etc.
☑	Stackup detailes	Stack-up.pdf	ACROBAT	

All files are in millimeters and showed from top view.
Format: 4:3
Any changes/production optimizations must be approved by GomSpace.