Hardware Change Log

*[P80 PMU]*

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| Document Title: | PMU\_Hardware\_Change\_Log | | |
| Document Reference: |  | | |
| Document Revision: |  | Date: | *08-03-2018* |
| **Comments** | | | |
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| Action | Name | Function | Signature | Date |
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| Prepared: | JOKR |  |  | 08-03-2018 |
| Verified by: |  |  |  |  |
| Approved by: |  |  |  |  |

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# Changelog

|  |  |  |  |
| --- | --- | --- | --- |
| Date | Revision | Author | Description |
|  |  |  |  |
| 12-12-2017 | 1 |  | Initial release first prototype - EDA build no. 17.1.9.592 |

# Purpose and Scope

This document describes the changes for each revision of the P80 PMU.

Raw PCB Number: 100608

# Changes to be implemented (Pending)

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| No. | Description | BOM | Schematic | Layout | Who | Status |
|  |  |  |  |  |  |  |
| 2 | Change component L2 to the one on the IFS BOM rev 3 (in IFS) | x | x | x |  |  |
| 3 | Make protection against voltage spikes on comparator for OV/UV – VBAT\_PROTECT is going in on a pull-up + a comparator input… | x | x | x |  |  |
| 4 | Connect “VBAT\_SENS” (ucontroller) to VBAT\_KS, instead of VBAT\_FUSED as done today.  This will reduce current consumption (70uA) when KS is active/pressed. |  | x | x | jove | idea |
| 5 | Over Voltage protection shall sense on battery side (VBAT\_FUSED).  Implement: R155, R158, R161 should have option to connect to VBAT\_FUSED. |  | x | x | Jove |  |
| 6 | Noise on CUR\_OUT. Move CUR\_OUT trace away from L2.  L2 introduce noise in the signal, which cause SC circuit to trigger random |  | x | x | Jove |  |
| 7 | SC protection shall have hysteresis. Noise on CUR\_OUT and on the RC-charging curve (C74/R168) cause random trigger of SC. |  | x | x | Jove |  |
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# Revision 2 changes implemented

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| No. | Description | BOM | Schematic | Layout | Who | Status |
| 1 | Edit Heater layout to have parallel tracks – reducing magnetic dipole during heating. |  |  | X | MOGN | Done |