Hardware Change Log

*[P80 ACU]*

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| Document Title: | ACU\_Hardware\_Change\_Log | | |
| Document Reference: |  | | |
| Document Revision: |  | Date: | *05-03-2018* |
| **Comments** | | | |
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| Action | Name | Function | Signature | Date |
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| Prepared: | JOKR |  |  | 05-03-2018 |
| Verified by: |  |  |  |  |
| Approved by: |  |  |  |  |

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# Changelog

|  |  |  |  |
| --- | --- | --- | --- |
| Date | Revision | Author | Description |
|  |  |  |  |
| 12-12-2017 | 1 |  | Initial release first prototype - EDA build no. 17.1.9.592 |

# Purpose and Scope

This document describes the changes for each revision of the P80 ACU.

Raw PCB Number: ??

# Changes to be implemented (Pending)

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| No. | Description | BOM | Schematic | Layout | Who | Status |
| 1 | Suggestion: VBAT switch R126 or T2, if they have a bad soldering/disconnect, a single failure can turn off VBAT\_OUT. Cheap solution is to make a redundant open collector circuit (duplicate these components) |  | X |  | JOKR |  |
| 2 | Page 3: net BOOST\_OUT and V\_BAT are connected together. Net should only have one name. |  | X |  | JOKR |  |
| 3 | VCC routing on mid-layer 5 can be removed. Vias connected through VCC plane layer 7 |  |  | X | JOKR |  |
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# Revision 2 changes implemented

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| No. | Description | BOM | Schematic | Layout | Who | Status |
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