Altium Designer

Design Rule Verification Report

Date:	3/7/2025		
Time:	11:21:40	**Warnings:**	0
Elapsed Time:	00:00:00	**Rule Violations:**	0
Filename:	C:\Users\Public\Documents\Altium\EGR314_Ian_Subsytem\Sensor_Suite.PcbDoc		

Summary

Warnings	Count
Total	**0**

Rule Violations	Count
Clearance Constraint (Gap=10mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=15mil) (Max=50mil) (Preferred=15mil) (All)	0
Routing Topology Rule(Topology=Starburst) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) (All)	0
Minimum Annular Ring (Minimum=9.842mil) (All)	0
Hole Size Constraint (Min=5.906mil) (Max=248.031mil) (All)	0
Hole To Hole Clearance (Gap=17.716mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=7.874mil) (All),(All)	0
Silk To Solder Mask (Clearance=5.906mil) (IsPad),(All)	0
Silk to Silk (Clearance=10mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Prefered=500mil) (All)	0
Total	**0**