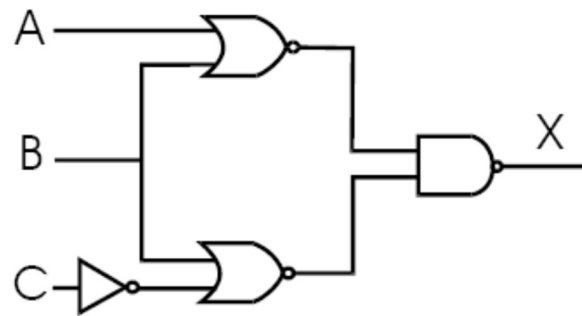


L3 practice problems

Alternate logic symbols

1. The following logic circuit diagram is taken from Question 3 in L1 Practice.



- Identify the connections that do not follow bubble-to-bubble matching.
- Redraw the diagram using appropriate logic symbols such that it follows bubble-to-bubble matching. Your diagram should show clearly how logic 0 can be produced at output X.
- Redraw the diagram using appropriate logic symbols such that it follows bubble-to-bubble matching. Your diagram should show clearly how logic 1 can be produced at output X.

Circuit connection diagram

2. Given the following quad-AND and quad-OR ICs, draw the circuit connection diagram for the implementation of $(A+B)(C+D)$. Include pin numbers and connections for V_{CC} and Gnd in your diagram.

