## **SC1005 Digital Logic Tutorial 3**

## **Digital arithmetic**

1. Perform the following <u>unsigned</u> binary addition and subtraction.

2. Perform the following <u>two's complement</u> additions. Clearly indicate whether or not an overflow occurs.

3. Perform the following <u>two's complement</u> subtractions. Clearly indicate whether or not an overflow occurs. Check by converting to decimal values.

4. Perform the following <u>unsigned</u> binary multiplications. Verify with decimal values.

5. Perform the following <u>signed 2's complement</u> binary multiplications. Verify with decimal values.

## **Answers**

- 1.
- a. 11011111
- b. 10000110
- 2.
- a. 10111111 (no overflow)
- b. 10011110 (no overflow)
- c. 10001110 (overflow)
- d. 10000000 (overflow)
- 3.
- a. 11110001 (no overflow)
- b. 10011111 (overflow)
- c. 11101011 (no overflow)
- d. 11110100 (no overflow)
- 4.
- a. 1011100110
- b. 100011110
- 5.
- a. 000010110
- b. 110111110