Problem 5-2

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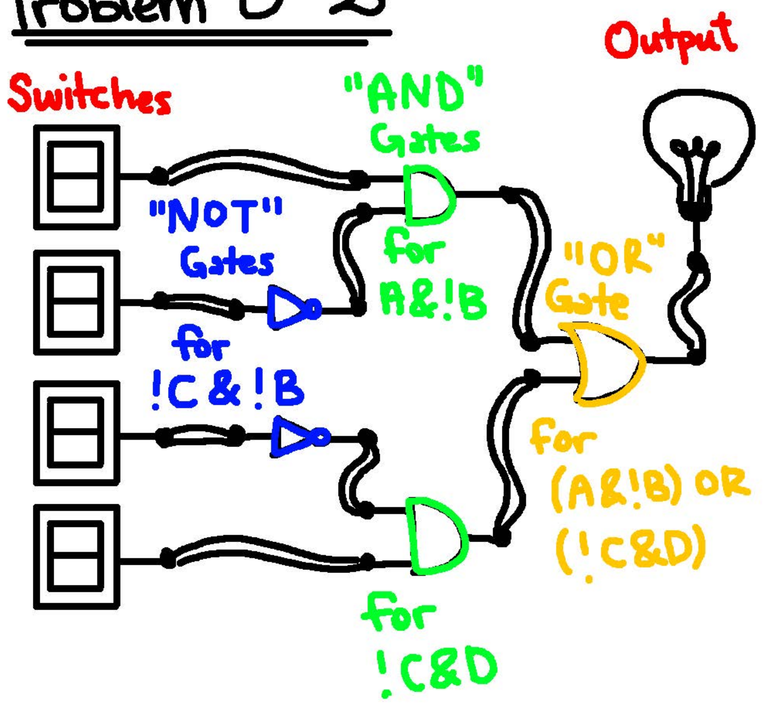

Problem 5-3

Problem 5-3

icode:ifun ← M_1[PC]
rA:rB ← M_1[PC+1]
valP ← PC+2
} Fetch

valA ← R[rA]
valB ← R[rB]
} Decode
grabbing the registers' values

valE ← valA + valB
Set CC
} Execute
actually performing the operation, then setting our flags. our added value is valE

} Memory

R[rB] ← valE } Write Back
Writing our summed value back to rB, equivalent to R[%reg] = valE where %reg is a register

PC ← valP } PC update
setting PC back to valP originally when we offset by 2 in "fetch"