0300CST302052201

		1/3	1/3	A EDO	ON TO	(3)
Reg No.:	Name:	1/E	18/		40 3	100
	APJ ABDUL KALAM TECHNOLOGICAL UNIVERS	P	THR!	QV/		AR
	Sixth Semester B.Tech Degree Examination June 2022 (2019	Sch	eme	Valentin Park	18,18	* NY N
			CAE	S) DOM		
			1	THUF		

Course Code: CST302 Course Name: COMPILER DESIGN

	Course Name. COM ILEX DESIGN	
k. M		Hours
		Marks
	•	(3)
		(-)
	•	(3)
		(3)
	$A \rightarrow + *$	
	What are viable prefixes?	(3)
	What are the different parsing conflicts in the SLR parsing table?	(3)
	Differentiate between synthesized attributes and inherited attributes with an	(3)
	example.	
	What is the role of activation record in compiler design?	(3)
	Explain code motion with an example.	(3)
	Write the algorithm for partitioning a sequence of three-address instructions into	(3)
	basic blocks	
	PART B	•
	Module I	
a)	Explain the working of different phases of a compiler. Illustrate with a source	(8)
	language statement.	
b)	Explain different compiler construction tools.	(6)
	OR	
a)	Explain the role of transition diagrams in recognition of tokens.	(7)
b)	Explain bootstrapping with an example.	(7)
	a) b)	Answer all questions, each carries 3 marks. Find the lexemes in the following programming statement. sum = a * (b - 10); Define tokens and patterns for the above statement. Explain the importance of sentinels in input buffering used in lexical analysis With an example write the steps to remove left recursion? Find FIRST set and FOLLOW set of each nonterminal in the following grammar E → E A E (E) - E id A → + * What are viable prefixes? What are the different parsing conflicts in the SLR parsing table? Differentiate between synthesized attributes and inherited attributes with an example. What is the role of activation record in compiler design? Explain code motion with an example. Write the algorithm for partitioning a sequence of three-address instructions into basic blocks PART B— Answer one full question from each module, each carries 14 marks. Module I a) Explain the working of different phases of a compiler. Illustrate with a source language statement. b) Explain different compiler construction tools. OR a) Explain the role of transition diagrams in recognition of tokens.

0300CST302052201

Module II

(6)13 a) i. Show that the grammar $S \rightarrow iCtSeS \mid iCtS \mid b$, $C \rightarrow a$ is ambiguous. ii. Eliminate ambiguity from the above grammar. Construct a Recursive descent Parser for handling Arithmetic Expressions. (8) **b**) OR (6) Write Non-recursive predictive parsing algorithm. (8) Prove that the following grammar is not LL(1) S → iEtSS' | a $S \rightarrow eS \mid \varepsilon$ $E \rightarrow b$ **Module III** (9)Construct canonical LR(0) collection of items for the grammar below. 15 a) $S \rightarrow L = R \mid R$ $L \rightarrow *R \mid id$ $R \rightarrow L$ Prove that this grammar is not SLR(1). What is handle pruning? Indicate the handles in the reduction of the sentence (5)aaabbb to the start symbol using the grammar $S \rightarrow aABb$, $A \rightarrow aA \mid a$, $B \rightarrow bB \mid b$ OR (9)Derive LR (1) parsing table for following grammar S → Aa | bAc | Bc | bBa $A \rightarrow d$ $B \rightarrow d$ b) Write all moves by the LR parser for parsing the input 'bdc'. [use the parsing (5)table created in question number 16.a] **Module IV** 17 a) Write the SDD for a simple type declaration and draw the annotated parse tree for (7)the declaration float a, b, c. b) With an SDD for a desk calculator, write the steps involved in the bottom up (7)

evaluation for the expression (3*5) -2.

0300CST302052201

OR

10	a)	Explain static allocation and neap allocation strategies.	(7)
	b)	Construct the DAG and three address code for the expression a+a*(b-c)+b*(b-	(7)
		c)+b	
		Module V	
19	a)	With suitable examples explain loop optimization techniques	(7)
	b)	With suitable example of a basic block, explain the code-improving	(7)
		transformations of a basic block.	
		OR	
20	a)	Explain issues in design of a code generator	(6)
	b)	Write the code generation algorithm. Using this algorithm generate code	(8)
		sequence for the expression $x = (a - b) + (a + c) + (a + c)$	

TRACE KTU