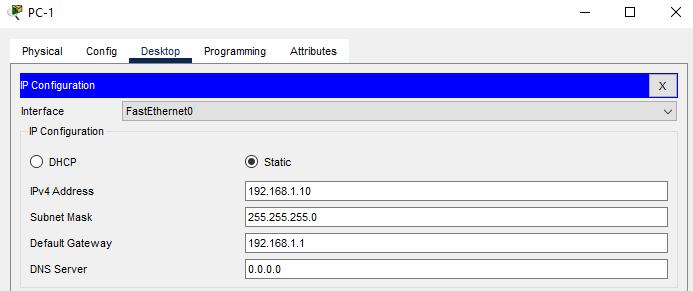
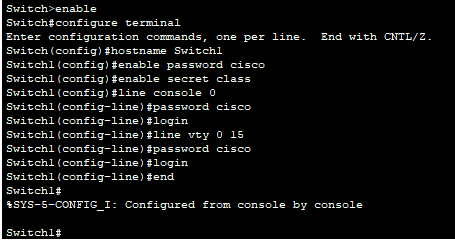
Tracy Marie Andrea Madresta BSIT-3A February 25, 2023

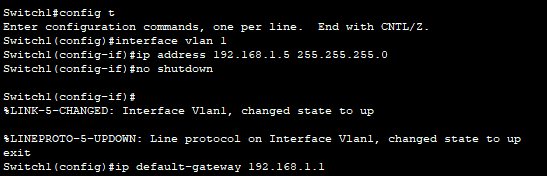
**Lab 2.3.5 Configuring Basic Routing and Switching**

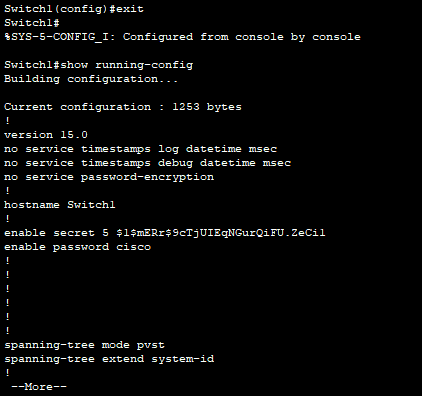
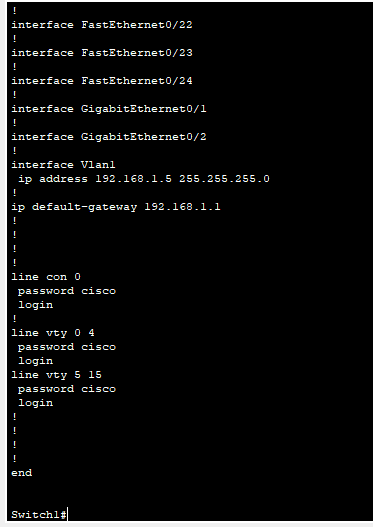
**Step 1:**

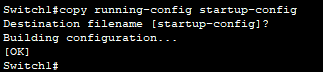
**Step 2:**



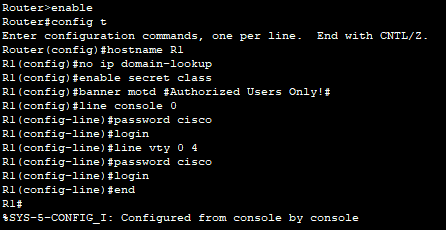
**Step 3:**



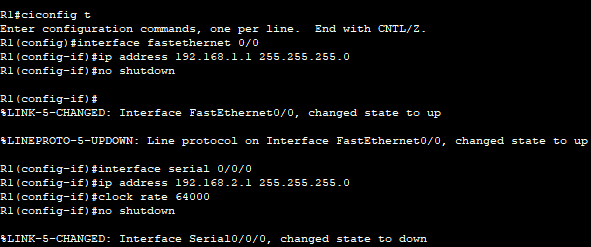
**Step 4:**

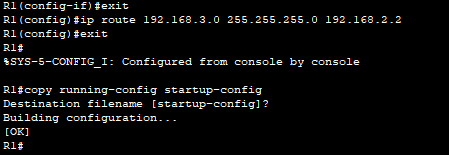


**Step 5:**

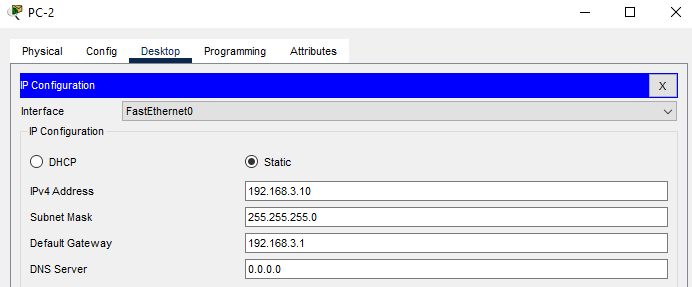


**Step 6:**

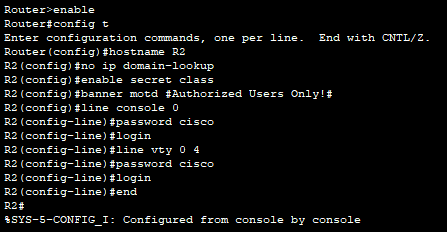


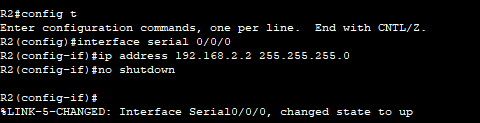


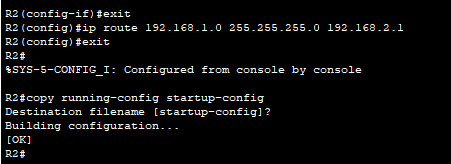
**Step 7:**



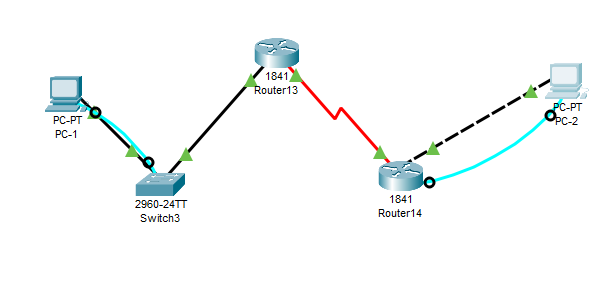
**Step 8:**





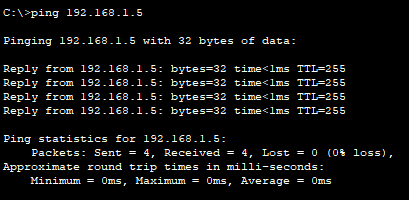


**Step 9:**

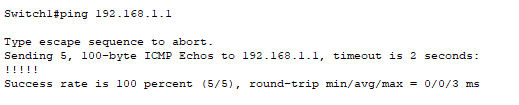


**Step 10:**

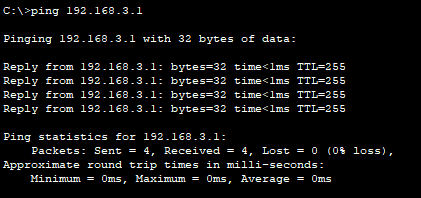
**A:**

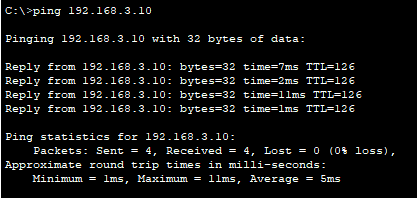


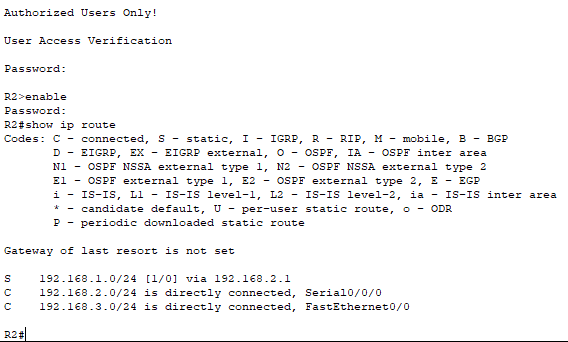
**B:**

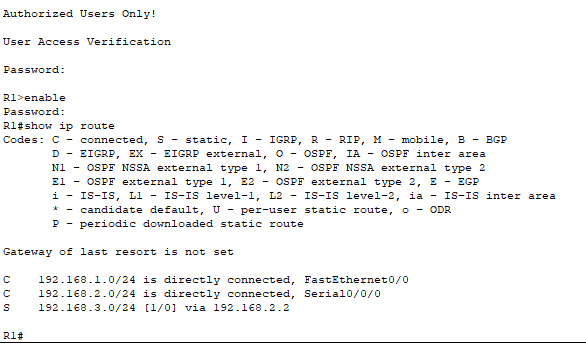


**C:**

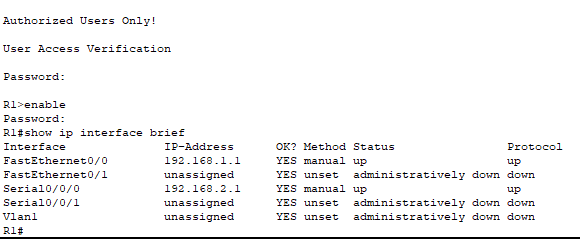


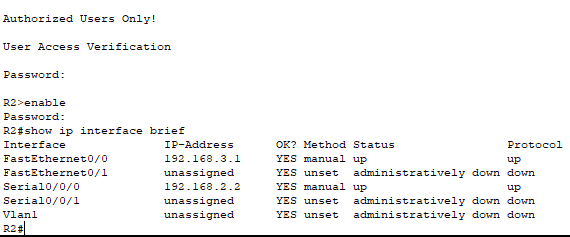
**D:**

**E:**

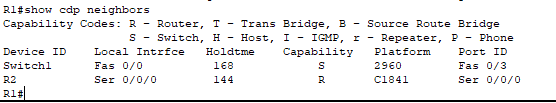


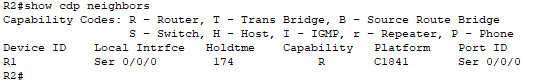
**B:**





**G:**

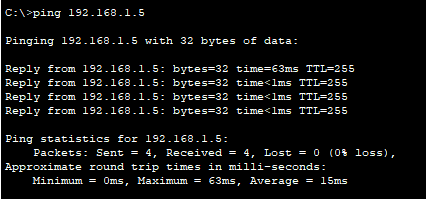


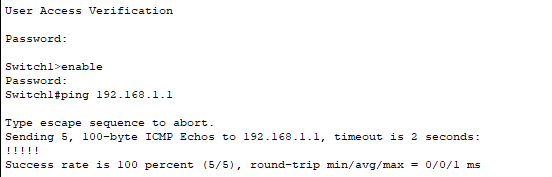


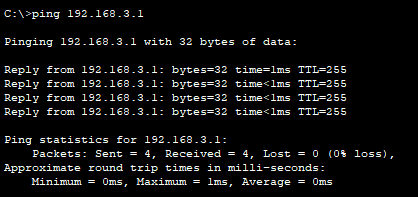
**Step 11 & 12:**

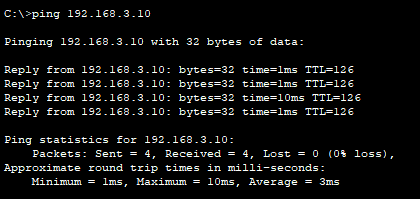


**Step 13:**

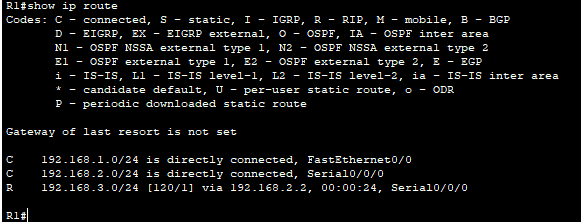
**A:**

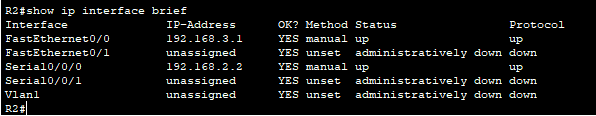
**B:**

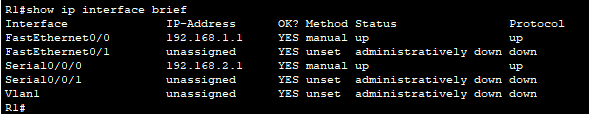
**C:**



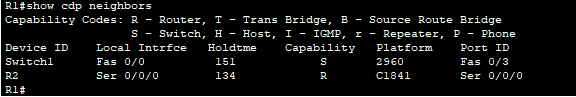
**D:**

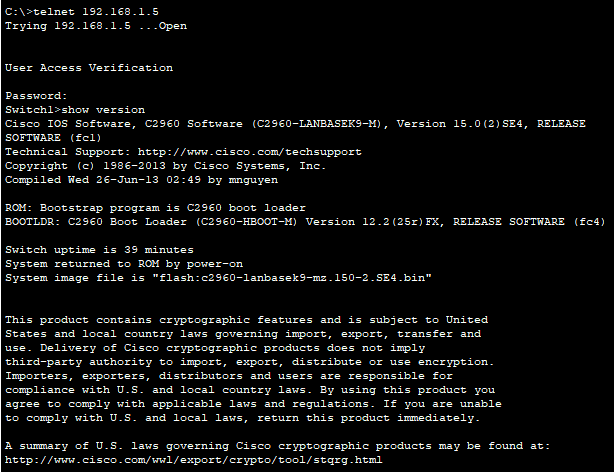
**E:**

**F:**



**G:**



**Step 14:**

