VDIC - Testbench test specification

Daniel Stankiewicz

28.10.2021

1 Remote github repository

https://github.com/TraitorablePL/VDIC

2 Test specification

In the current state testbench checks the following actions:

- AND operation
- OR operation
- ADD operation
- SUB operation
- Data result flag Carry
- Data result flag Overflow
- Data result flag Negative
- Data result flag Zero
- CTL Data response packet if CRC3 is correct
- CTL Error response packet if parity bit is correct
- Error flag invalid CRC
- Error flag invalid Data
- Error flag invalid invalid Operation
- Repetition of transaction