

VDIC - Testbench test specification

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1 Remote github repository

<https://github.com/TraitorablePL/VDIC>

2 Test specification

In the current state testbench checks the following actions:

- AND operation for random and extreme values
- OR operation for random and extreme values
- ADD operation for random and extreme values
- SUB operation for random and extreme values
- DATA result flag Carry for ADD and SUB operations
- DATA result flag Overflow for ADD and SUB operations
- DATA result flag Negative for all operations
- DATA result flag Zero for all operations
- CTL Data response packet if CRC3 is correct
- CTL Error response packet if Parity bit is correct
- Error flag invalid CRC4
- Error flag invalid Data
- Error flag invalid Operation for all unsupported operations
- Repetition of transaction for each operation
- Each operation after reset