

LAB 3 BEHAVIORAL REPORT

Academic Integrity (more info @ <https://aisc.uci.edu/>): You are encouraged to discuss the labs at a high level, but the code/equations/simulations you come up with should be your own. By typing “yes” at the end of this question and filling in your name, you certify that the work you are turning in is your own work. Is the work you are turning in your own? yes

If you worked on any portion of your report or vhdl code with other students (discussion at high level & debugging; if more, please describe), please list their names here: _____

Student Name: Andy Tran

Student ID: 57422363

Date Completed: 5/6/2021

Time Spent: Reviewing Digital Design Material 20minutes

Design/Preparation Work: 15 minutes

VHDL Coding & Debugging: 30 minutes

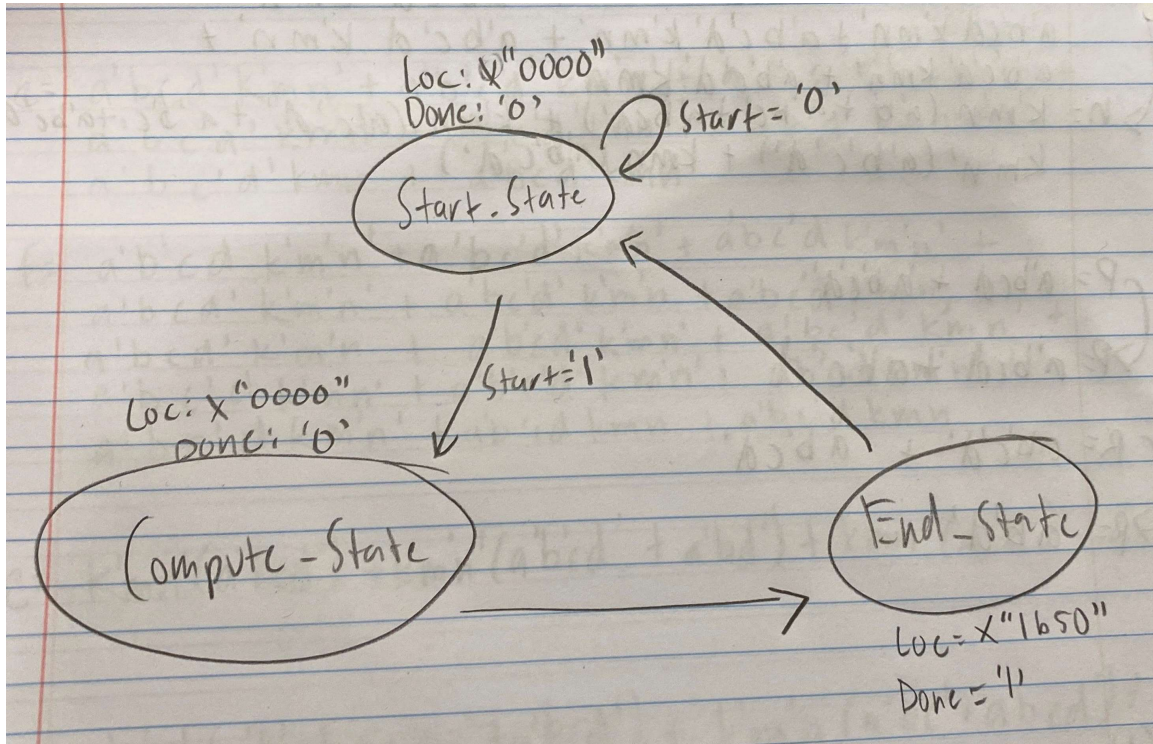
BEHAVIORAL OVERVIEW

Replace this text w/ the % you feel you completed the lab. Be sure to list your general procedure of how you completed this lab & material (if any) you reviewed to help you complete this lab. Regardless of % stated, provide any details of difficulties (if any) you encountered during this lab. A few sentences are sufficient.

I think I did 100% of this lab. I reviewed the videos and lecture 5 and 6 to get a pretty good basis for what to do. I read the instructions and built a fsmd diagram for the states. Then I implemented it into a process that had the states and register in the same process. I then used the basic formula given to get an estimate for the assert value for location, estimate means just without the decimal points.

LAB 3 FSMD

Show your FSMD for Lab 3 here. You can use Visio, another UML Diagramming tool, or attach a picture of your FSM as long as it is legible. If you attach a picture, ensure it is the correct orientation. Be sure to show what computations you want to complete in each step of your FSMD for your behavior.



LAB 3 BEHAVIORAL SIMULATION GRAPH

Show a screenshot of your final graph here. You should crop it to the appropriate size so that it is legible.



