

LAB 2 BEHAVIORAL REPORT

Academic Integrity (more info @ <https://aisc.uci.edu/>): You are encouraged to discuss the labs at a high level, but the code/equations/simulations you come up with should be your own. By typing “yes” at the end of this question and filling in your name, you certify that the work you are turning in is your own work. Is the work you are turning in your own? yes

If you worked on any portion of your report or vhdl code with other students (discussion at high level & debugging; if more, please describe), please list their names here: _____

Student Name: Andy Tran

Student ID: 57422363

Date Completed: 4/26/21

Time Spent: Reviewing Digital Design Material: 3 Hours

Design/Preparation Work: 2 Hours

VHDL Coding & Debugging: 3 Hours

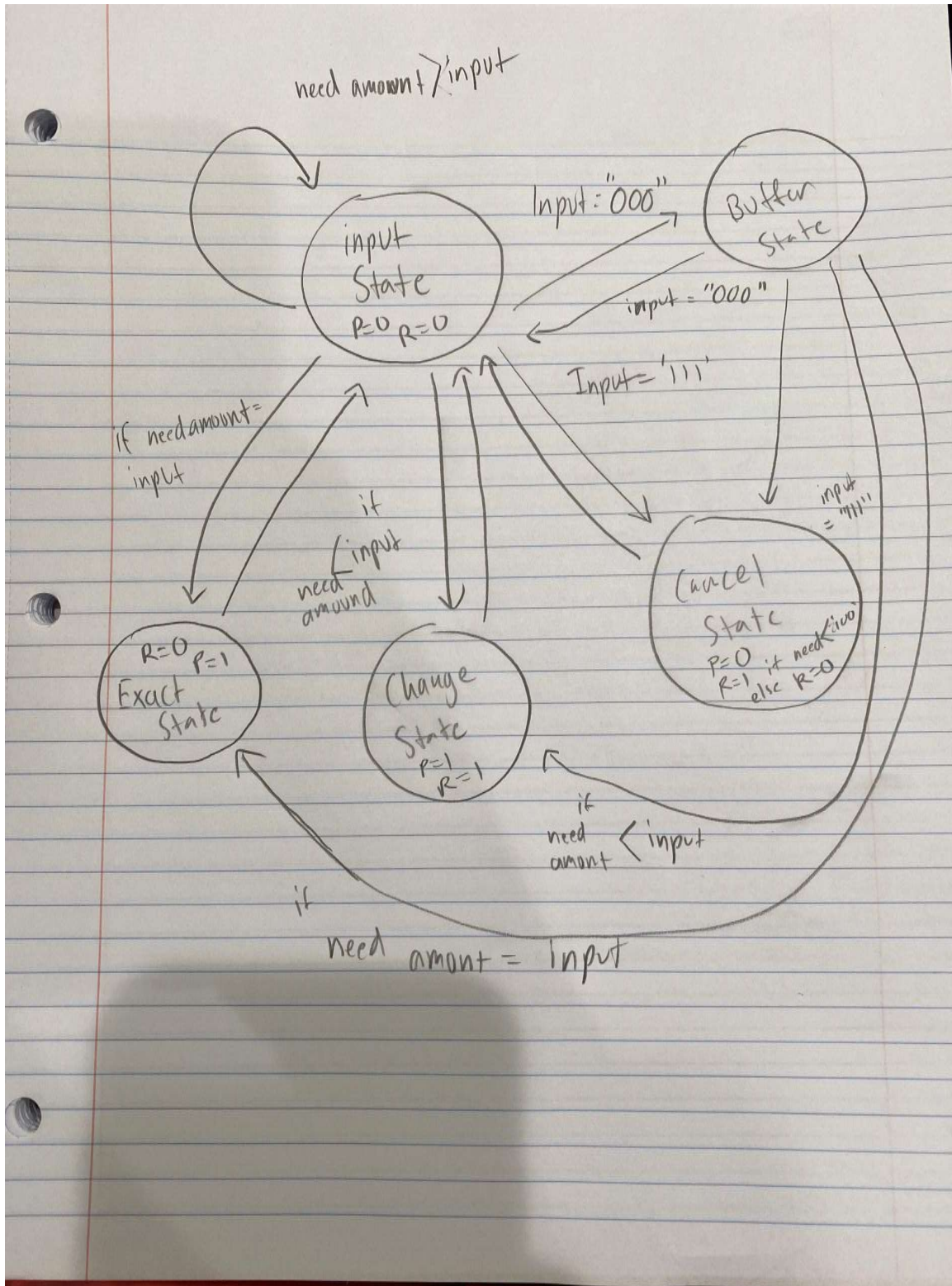
BEHAVIORAL OVERVIEW

Replace this text w/ the % you feel you completed the lab. Be sure to list your general procedure of how you completed this lab & material (if any) you reviewed to help you complete this lab. Regardless of % stated, provide any details of difficulties (if any) you encountered during this lab. A few sentences are sufficient.

I feel I have completed this lab 100%. I started off rewatching the lecture video and looking over the wristwatch exmaple. Then I built an FSM Diagram for the states and how each would ge there. I had a very difficult time starting and debugging, but eventually I figured it out. I was having trouble in the beginning with my states, so I started over. I built a version from only If statments and slowly transtitioned over.

LAB 2 FSM

Show your FSM for Lab 2 here. You can use Visio, another UML Diagramming tool, or attach a picture of your FSM as long as it is legible.



LAB 2 BEHAVIORAL SIMULATION GRAPH

Show a screenshot of your final graph here. You should crop it to the appropriate size so that it is legible.

Im sorry if it is a alittle hard to read but it is all there and it works. The black makes it extremely hard to screenshot and have it be crystal clear.

