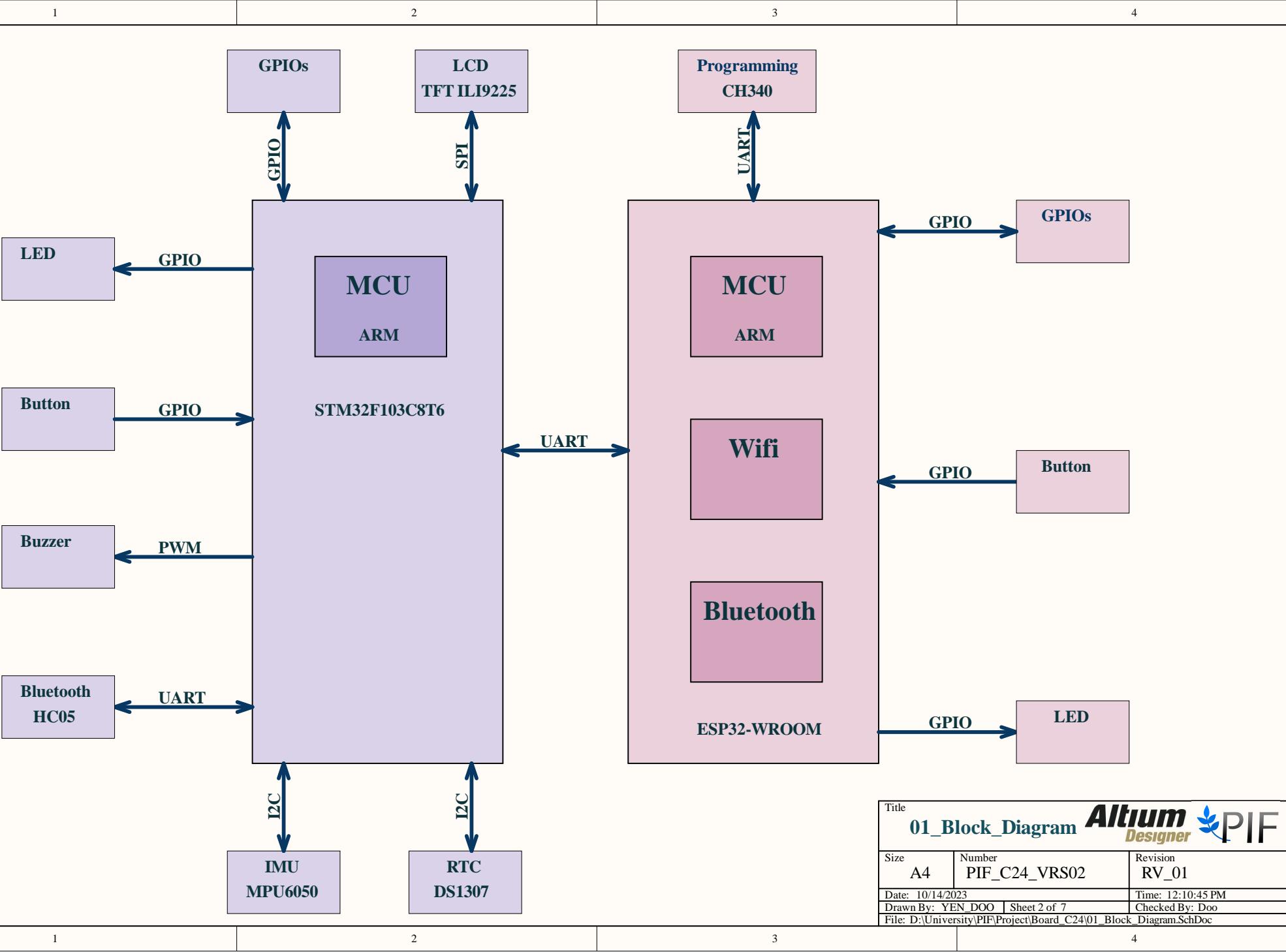
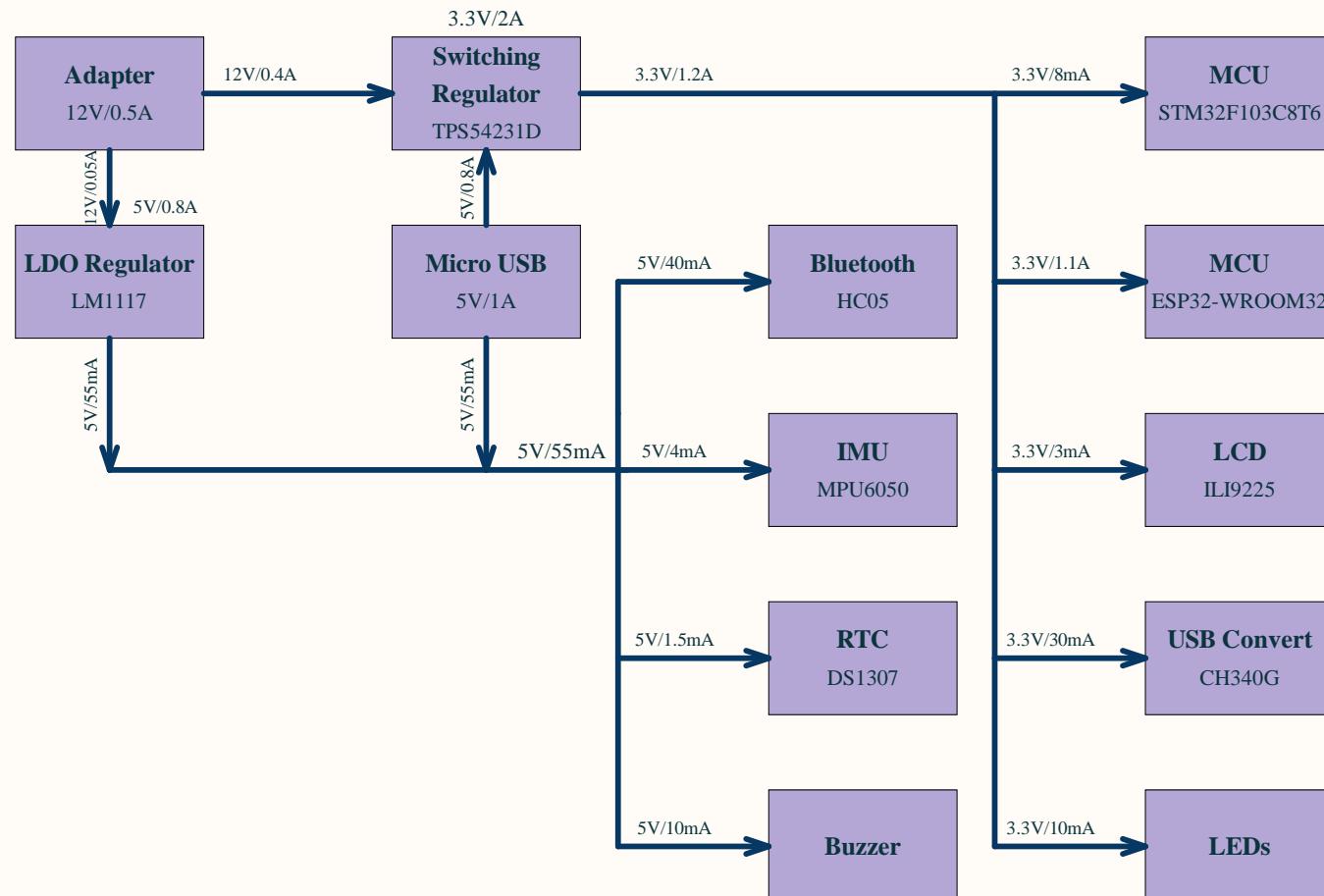




APPROVALS	DATE	PROJECT	Altium		
ENG: *					*
DSN: *					*
CHK: *		DOCUMENT REVISION: Not in version control			DESIGN ITEM: Not in version control
REFERENCE DOCUMENTS		TITLE *			
BOM:		ASSY DWG: SIZE A3 CAGE CODE DWG NO. REV			
FAB DWG:		PCB DWG: SCALE: FILE NAME 00_All_Sheet.SchDoc SHEET 1 OF 7			

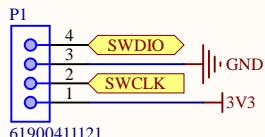
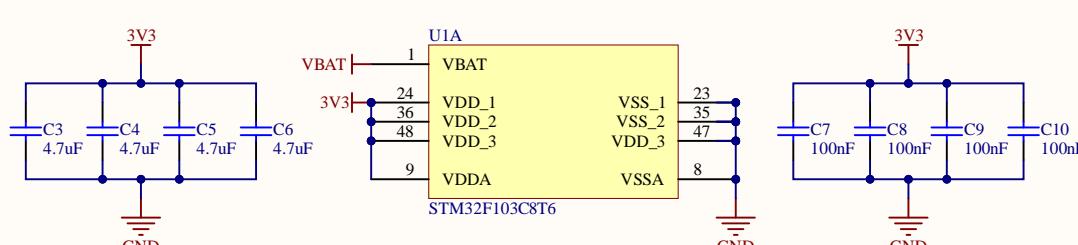
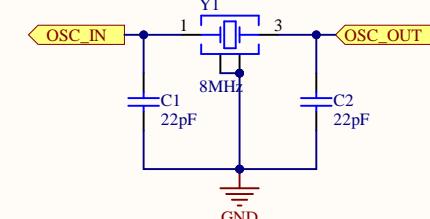
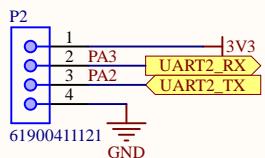
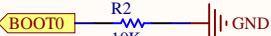
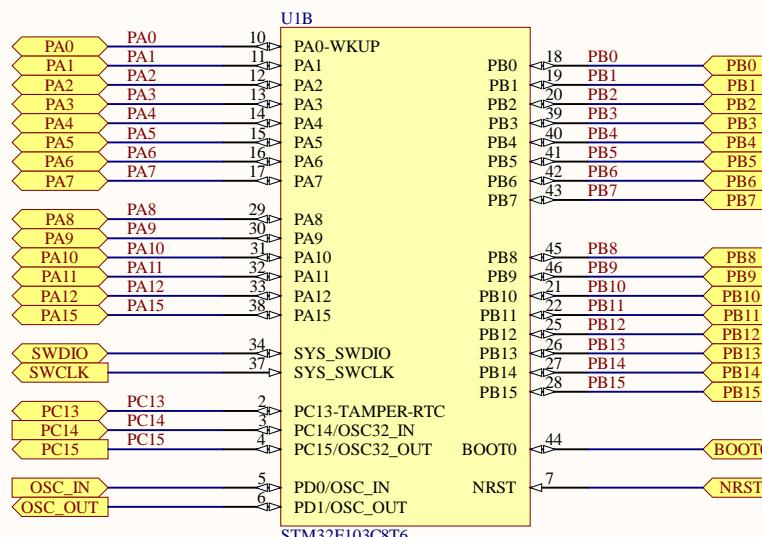
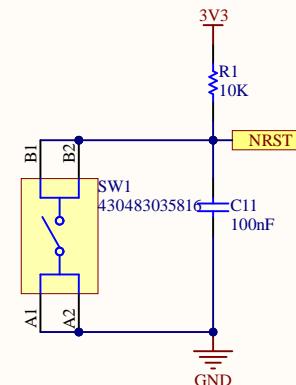


Title 01_Block_Diagram 		
Size A4	Number PIF_C24_VRS02	Revision RV_01
Date: 10/14/2023		Time: 12:10:45 PM
Drawn By: YEN_DOO	Sheet 2 of 7	Checked By: Doo
File: D:\University\PIF\Project\Board C24\01_Block Diagram.SchDoc		



Title 02_Power_Diagram		
Size A4	Number PIF_C24_VRS02	Revision RV_01
Date: 10/14/2023		Time: 12:10:47 PM
Drawn By: YEN_DOO	Sheet 3 of 7	Checked By: Doo
File: D:\University\PIF\Project\Board_C24\02_Power_Diagram.SchDoc		

A

SWD**MICROCONTROLLER - STM32****CRYSTAL****UART DEBUG****BOOT LOADER****BLINK LED****RESET BUTTON**

Title

03 MCU**Altium Designer**

Size

A4

Number

PIF_C24_VRS02

Revision

RV_01

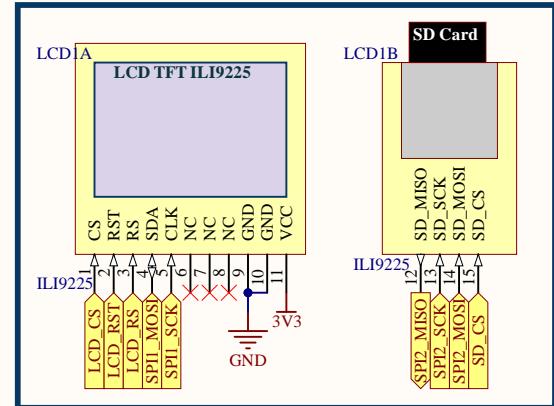
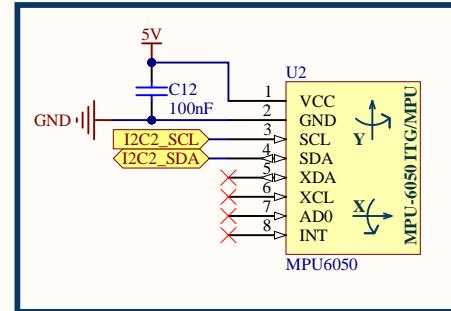
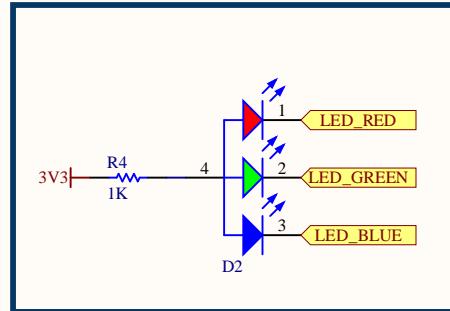
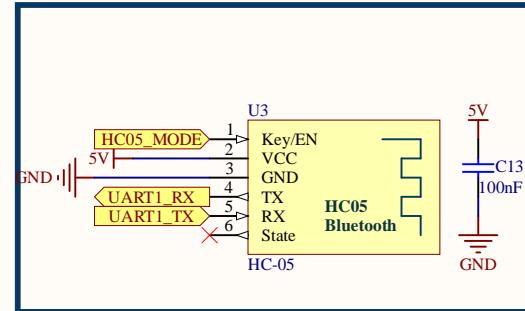
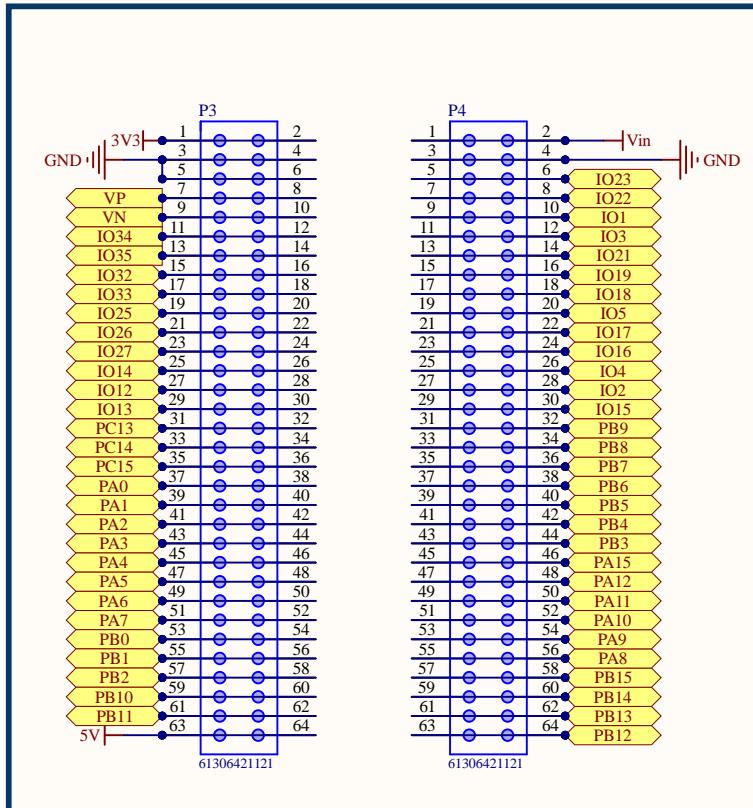
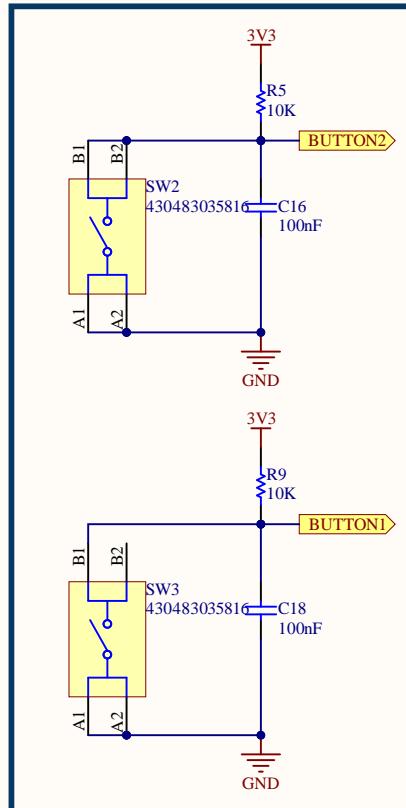
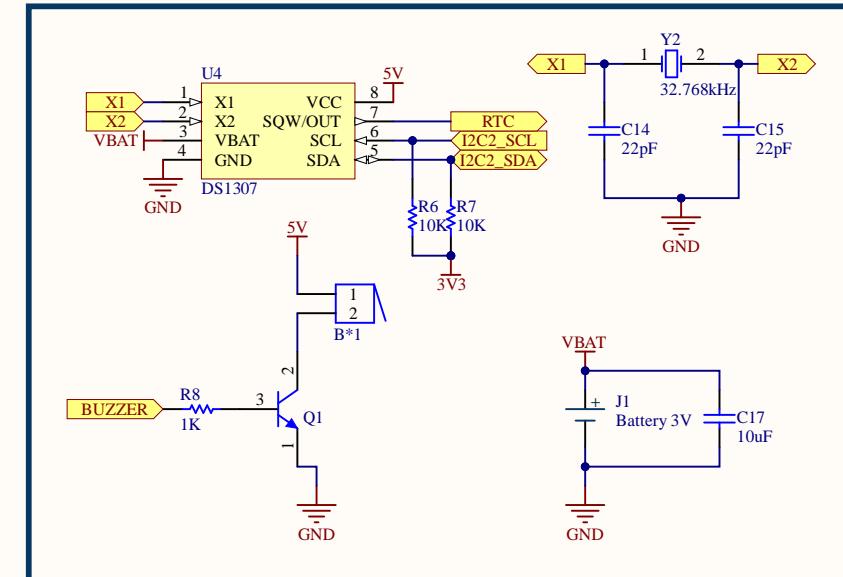
Date: 10/14/2023

Time: 12:10:48 PM

Drawn By: YEN_DOO

Checked By: Doo

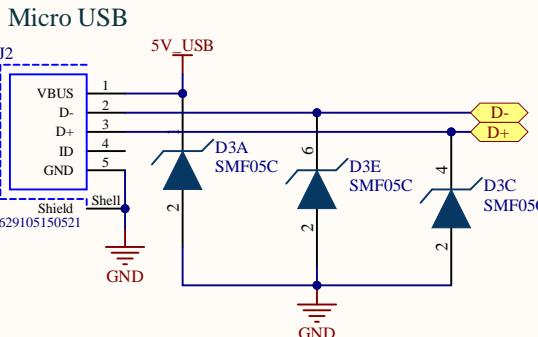
File: D:\University\PIF\Project\Board C24\03 MCU.SchDoc

LCD**IMU****LED RGB****Bluetooth****GPIOs****BUTTON****RTC**

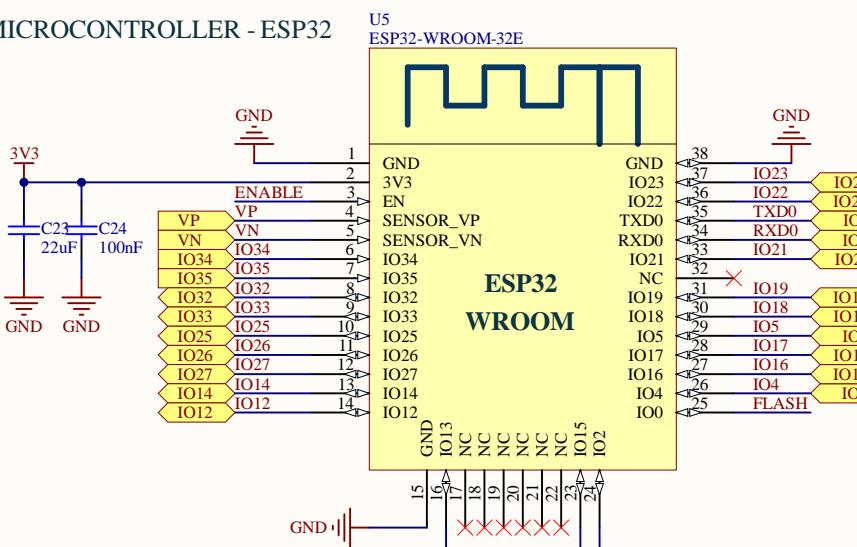
Title
04_GPIOS

Altium
Designer

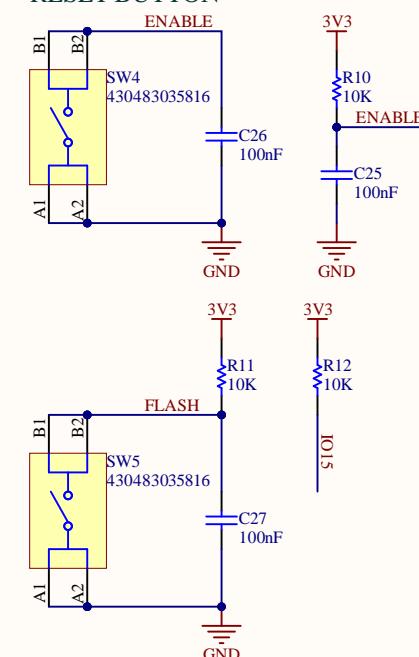
Size	Number	Revision
A4	PIF_C24_VRS02	RV_01
Date:	10/14/2023	Time: 12:10:49 PM
Drawn By:	YEN_DOO	Sheet 5 of 7
Checked By:	Doo	
File:	D:\University\Material\Altium\Library\YenDoo\04_GPIOS.SchDoc	



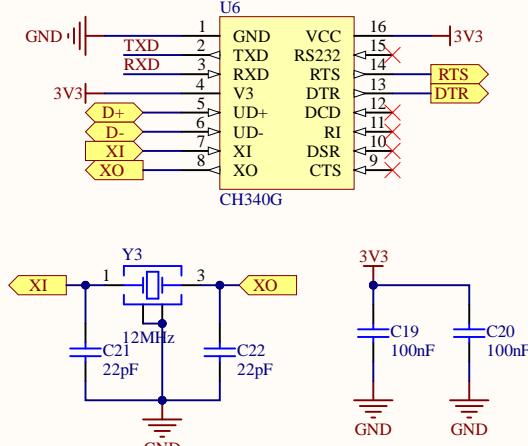
MICROCONTROLLER - ESP32



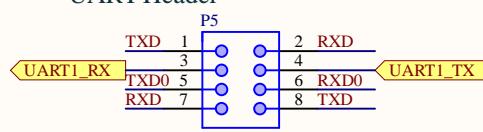
RESET BUTTON



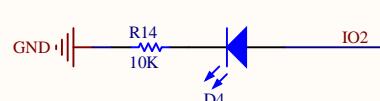
USB to serial



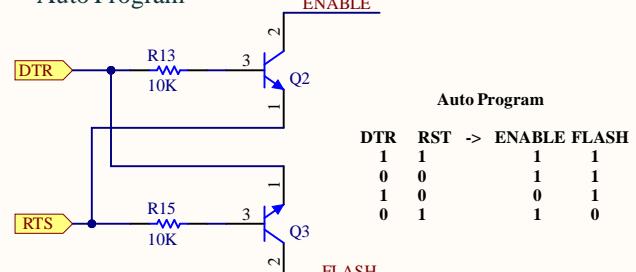
UART Header



Blink LED



Auto Program

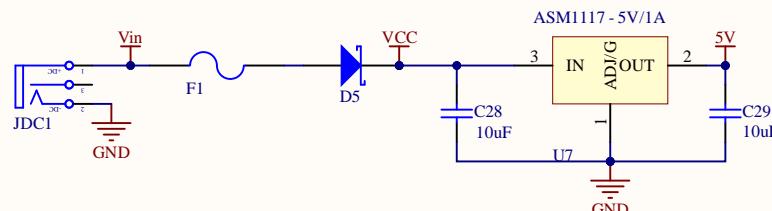


Title
05_ESP32

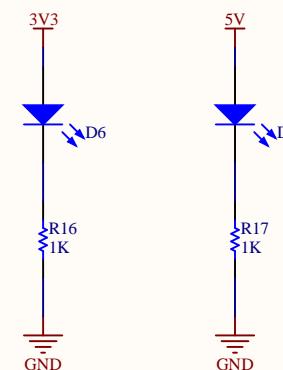
Altium
Designer

Size	Number	Revision
A4	PIF_C24_VRS02	RV_01
Date: 10/14/2023	Time: 12:10:50 PM	
Drawn By: YEN_DOO	Sheet 6 of 7	Checked By: Doo
File: D:\University\PIF\Project\Board C24\05_ESP32.SchDoc		

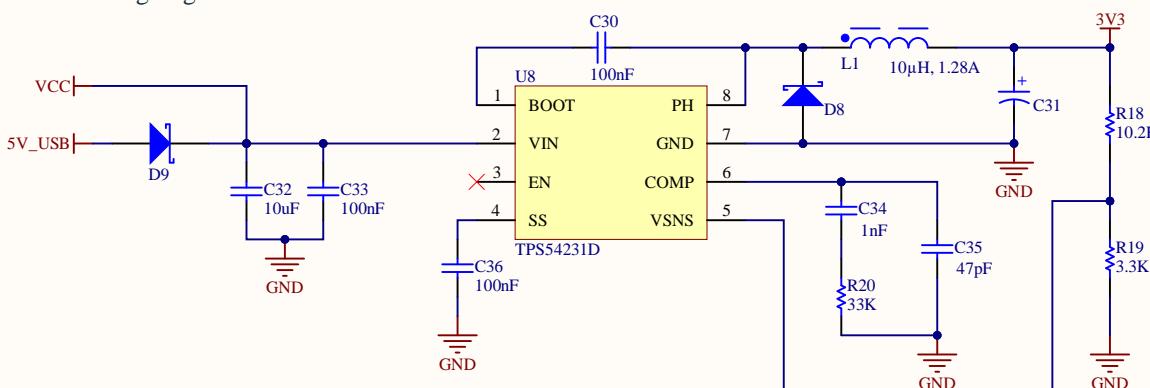
LDO Regulator



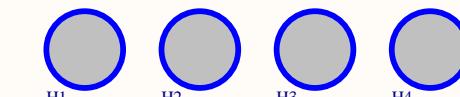
LEDs Power



Switching Regulator



Drill



Title

06_Power_Supply

Altium
Designer

Size

A4

Number

PIF_C24_VRS02

Revision

RV_01

Date: 10/14/2023

Time: 12:10:51 PM

Drawn By: YEN_DOO

Checked By: Doo

File: D:\University\PIF\Project\Board_C24\06_Power_Supply.SchDoc