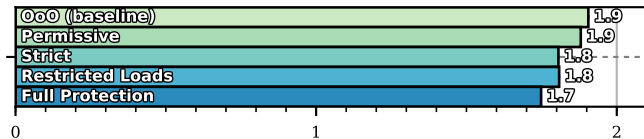
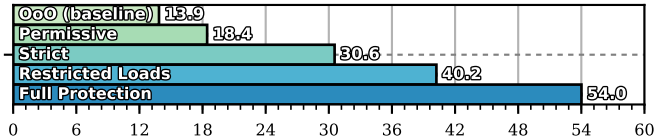


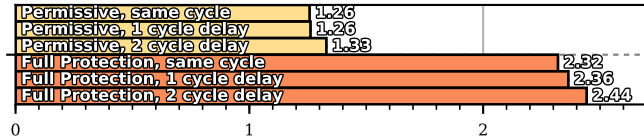
a) Cycles Breakdown, Normalized to OoO



b) Memory-Level Parallelism



c) Impact of NDA on Latency from Dispatch to Issue (Cycles)



d) Impact of NDA Logic Latency on CPI