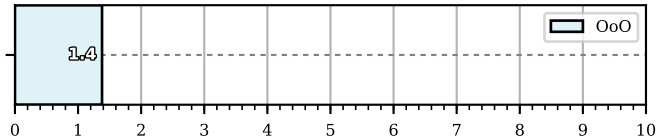
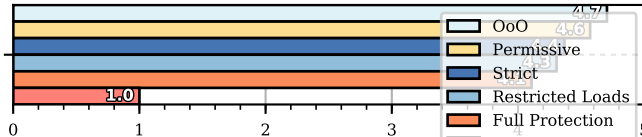


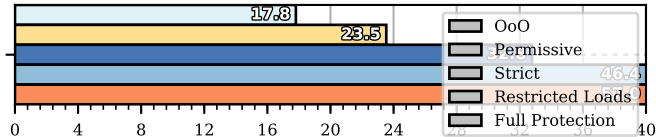
Cycles break-down, normalized to O3



Cycles per Instruction (Sensitivity Test)



Memory-Level Parallelism



Latency to Issue