2024 Digital IC Design Homework II

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| NAME |  | | |
| Student ID |  | | |
| **Functional Simulation Result** | | | |
| **FIFO Pass** | | **LIFO Pass** | **CIPU Pass** |
| **Stage 1** | | | |
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| **Stage 2** | | | |
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| **Stage 3** | | | |
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| **Description of your design** | | | |
| LIFO 和 LIFOFIFO架構如下:   * State register: 循序電路，存 next stage 模組產生的 state * Next stage: 組合電路，根據 input 和目前的 state 決定下一個 state * Output logic: 循序電路，根據 state 決定 output * Datapath: 循序電路，根據 state和 input 進行運算   下圖是 FSM 的 state: | | | |