

B. Tech (IT) COMPUTER ARCHITECTURE (CS7451)

Activity No.	Activity Name
A1.1	<p>Open Book Discussion</p> <ul style="list-style-type: none">• Different types of computers• Functional units of a digital computer<ul style="list-style-type: none">◦ Hardware components◦ Software components• Different architectures (Von Neumann and Harvard)• Byte and word addressing (Big-endian and Little-endian styles)• From high level language into machine language• About instruction• Different types of memory<ul style="list-style-type: none">◦ CPU registers◦ Cache◦ Main Memory◦ Secondary Storage◦ Remote Storage• MIPS Instructions <p>References:</p> <ol style="list-style-type: none">1. David A. Patterson and John L. Hennessy, "Computer Organization and Design: The Hardware/Software Interface", Fourth Edition, Morgan Kaufmann / Elsevier, 2009.2. Carl Hamacher, Zvonko Vranesic, Safwat Zaky and Naraig Manjikian, "Computer Organization and Embedded Systems", Sixth Edition, Tata McGraw Hill, 2012.3. Class notes
A1.2	<p>Tracing CPU information on Linux</p> <p>Reference:</p> <p>https://www.tecmint.com/check-linux-cpu-information/</p>
A1.3	<p>Translation from a C program into machine code (X86 architecture and Linux Platform)</p> <ol style="list-style-type: none">a. Converting high level language program into machine codeb. Tracing Assembly code instructions (identifying high-level program in assembly code)c. Tracing machine code (identifying assembly instructions in machine code) <p>Reference:</p> <p>http://csapp.cs.cmu.edu/2e/ch3-preview.pdf</p>
A1.4	<p>Case study on MIPS32 Instruction Set</p> <p>References:</p> <p>https://www2.cs.duke.edu/courses/fall13/compsci250/MIPS32_QRC.pdf</p> <p>https://www.cs.cornell.edu/courses/cs3410/2008fa/MIPS_Vol2.pdf</p> <p>http://www.dsi.unive.it/~gasparetto/materials/MIPS_Instruction_Set.pdf</p>