



In low power mode, setting the Start bit initiates a single conversion at the end of which it is automatically reset putting the ADC into “ShutDown” mode. This process takes about 728ms from Start =1 to BUSY.0 = 0 (power up stabilization and channel conversion)

ConfigReg (\$00)
ConfigReg.7 => init
ConfigReg.0 => Start

ConverReg (\$07)
ConverReg.0 => LowPwr

ACR (\$0B)
ACR.0 => Vref
ACR.1, ACR.2 => Mode1

BSR (\$0C)
BSR.0 => Conversion
BSR.1 => Busy POR

Channel Readings along with any ADC failures are appended to the end of the Event Table at the bottom of the txt file

Event 2: ADC Sensor Read Flow Chart