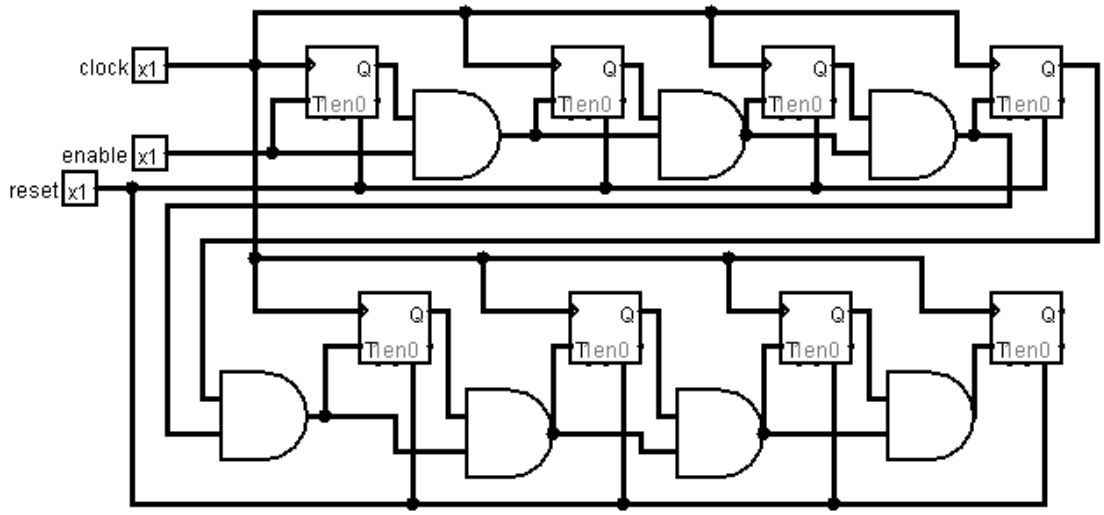
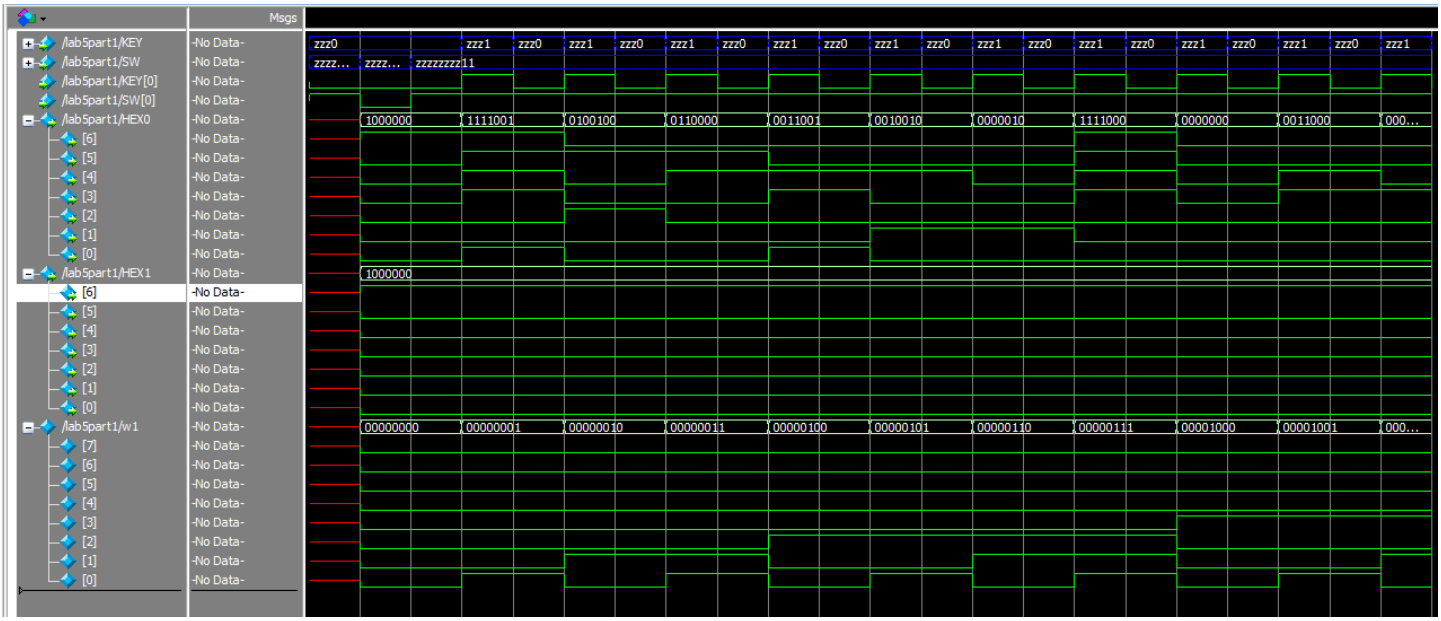
CSC258 Lab5 Prelab

Prart 1 1. schematic



4.



5. a) The Logic utilization (in ALMs) is 14/32,070 (<1%)

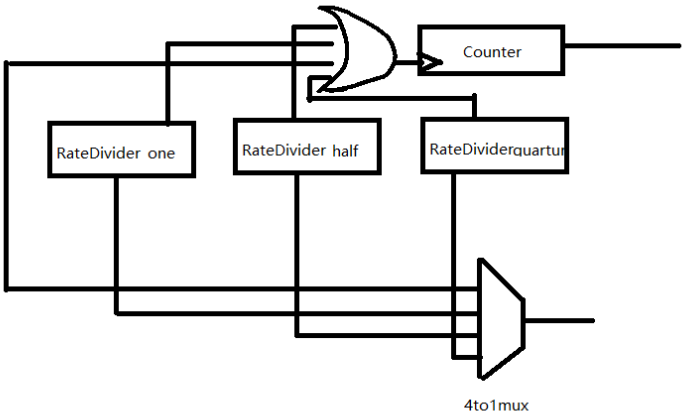
b) I cannot find the report. But I guess it is 50MHz.

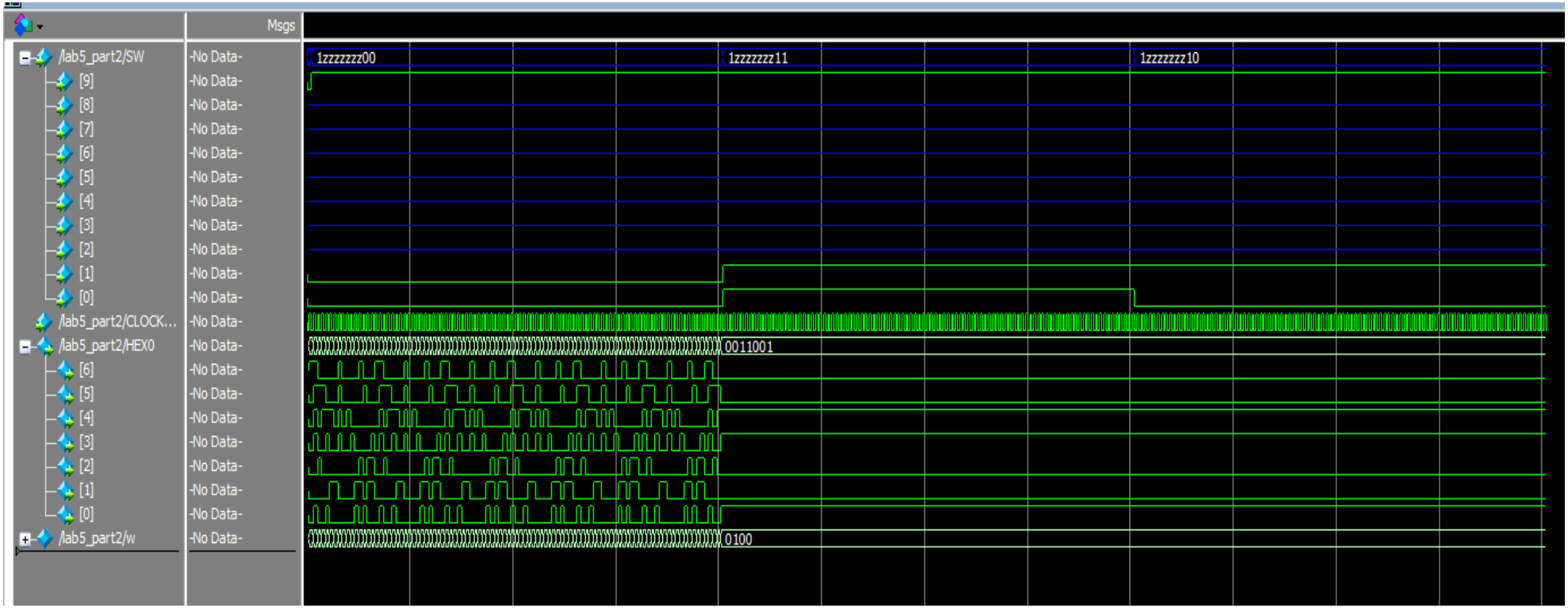
7. The T flip flop are vertically placed.

Part 2:

0. A counter with Log 2 5000000 = 26 to count to 50 million.

1.

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[7]
[6]
[5]
[4]
[3]
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/lab5_part2/HEX0
[6]
[5]
[4]
[3]
[2]
[1]
[0]
/lab5_part2/w
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1zzzzzz11
1zzzzzz10
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0100