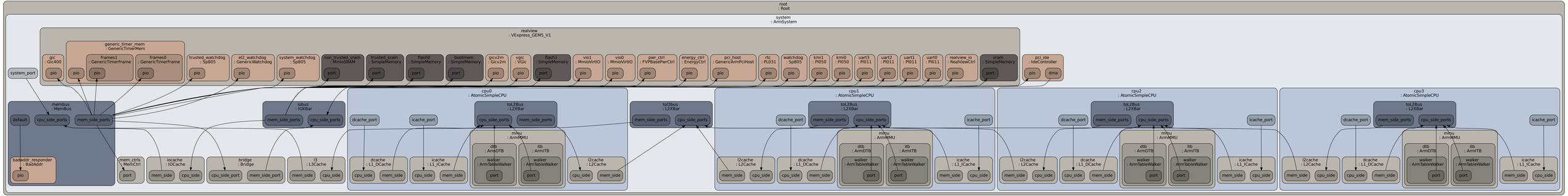
root
: Root
system
: ArmSystem
realview
: VExpress_GEM5_V1
generic_timer_mem
: GenericTimerMem
frames1
: GenericTimerFrame
frames0
: GenericTimerFrame
gic
: Gic400
trusted_watchdog
: Sp805
el2_watchdog
GenericWatchdog
system_watchdog
: Sp805
non_trusted_sram
: MmioSRAM
trusted_sram
: SimpleMemory
flash0
: SimpleMemory
bootmem
: SimpleMemory
gicv2m
: Gicv2m
vgic
: Vgic
flash1
: SimpleMemory
vio1
: MmioVirtIO
vio0
: MmioVirtIO
pwr_ctrl
: FVPRaslePwrCtrl
energy_ctrl
: EnergyCtrl
pci_host
: GenericArmPciHost
rtc
: PL031
watchdog
: Sp805
kmi1
: PI050
kmi0
: PI050
uart3
: PI011
uart2
: PI011
uart1
: PI011
uart0
: PI011
realview_io
: RealViewCtrl
vram
: SimpleMemory
pci_ide
: IdeController
pio
dma
port
system_port
membus
: MemBus
default
cpu_side_ports
mem_side_ports
badaddr_responder
: BadAddr
mem_ctrls
: MemCtrl
iobus
: IOXBar
iocache
: IOCache
bridge
: Bridge
l3
: L3Cache
cpu0
: AtomicSimpleCPU
dcache_port
icache_port
toL2Bus
: L2XBar
mmu
: ArmMMU
dtb
: ArmDTB
itb
: ArmITB
walker
: ArmTableWalker
dcache
: L1_DCache
icache
: L1_ICache
l2cache
: L2Cache
toL3Bus
: L2XBar
cpu1
: AtomicSimpleCPU
cpu2
: AtomicSimpleCPU
cpu3
: AtomicSimpleCPU