

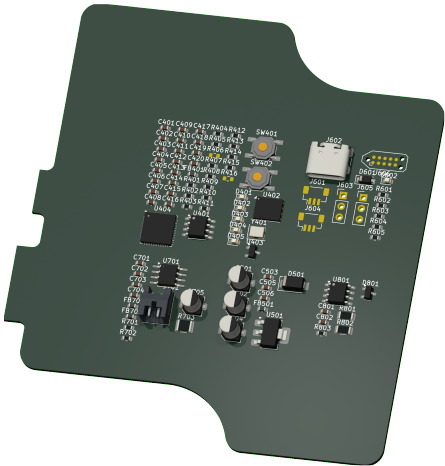
SMPBR / Pump_board

Rev 1.0

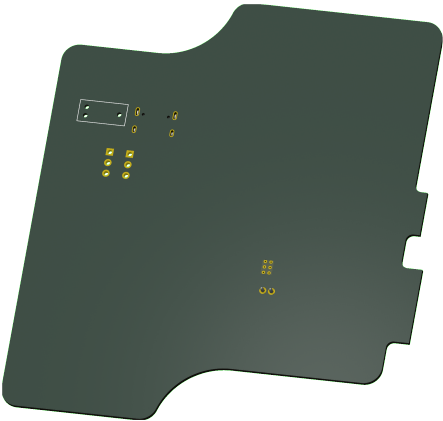
2025-07-29

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TOP VIEW



BOTTOM VIEW



NOTES


Not fitted components are marked as X

DESIGN CONSIDERATIONS

INFO NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout notes.

Designed for: TrendBit				
Designed by: Petr_Malaník				
Sheet: /				
File: pump_board.kicad_sch			Project: SMPBR	
Title:			Board: Pump_board	
Size: A4	Date: 2024-12-03	GIT hash:		Rev: 1.0
KiCad E.D.A. 9.0.1			Id: 1/8	

Revision History



Designed for: TrendBit
Designed by: Petr_Malaník

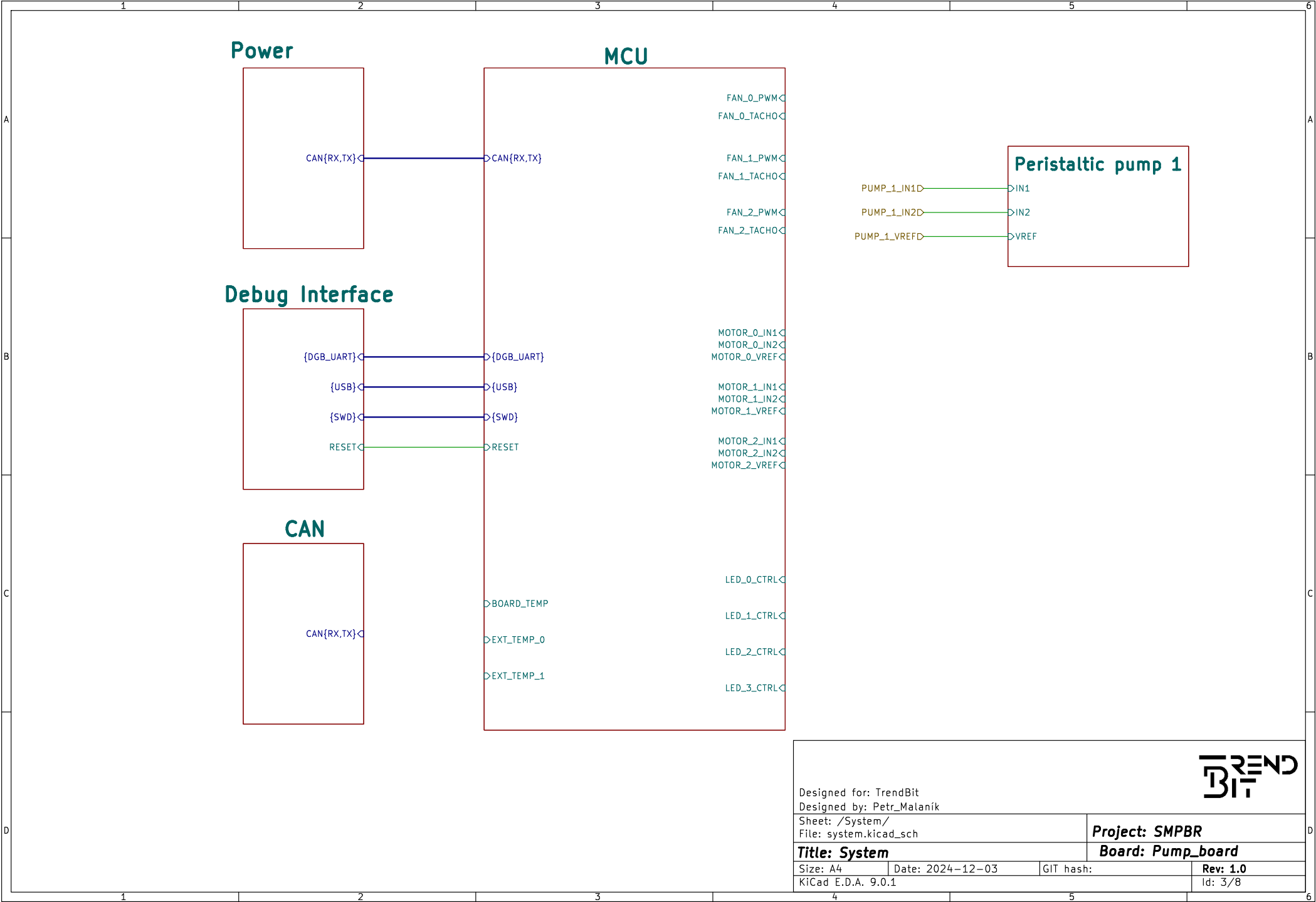
Sheet: /Revision History/
File: revision_history.kicad_sch

Project: SMPBR

Title: Revision History

Board: Pump_board

Size: A4	Date: 2024-12-03	GIT hash:	Rev: 1.0
KiCad E.D.A. 9.0.1			Id: 2/8



Designed for: TrendBit
Designed by: Petr_Malaník

Sheet: /System/
File: system.kicad_sch

Title: System

Size: A4

Date: 2024-12-03

GIT hash:

KiCad E.D.A. 9.0.1

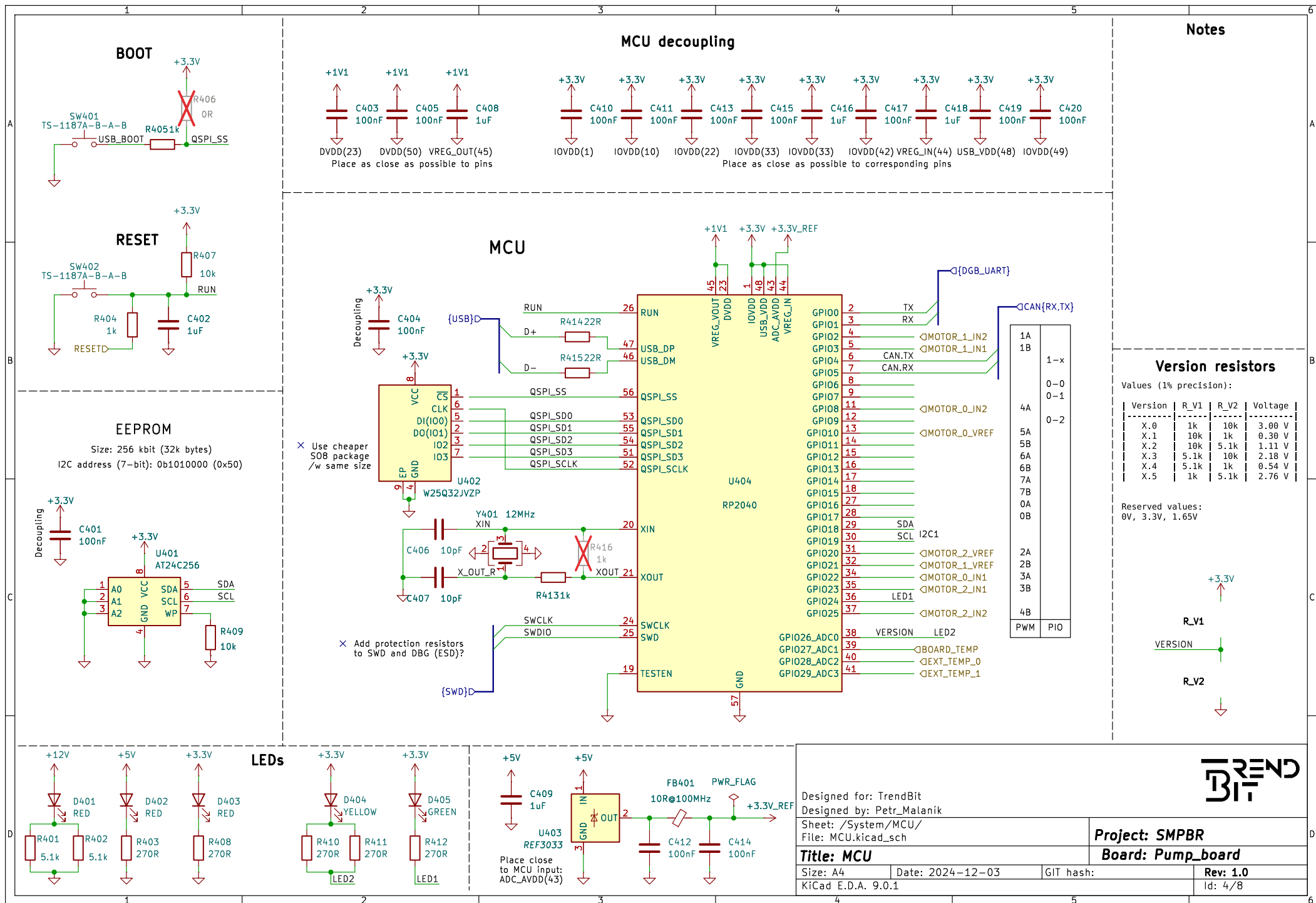
Project: SMPBR

Board: Pump_board

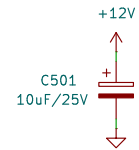
Rev: 1.0

Id: 3/8



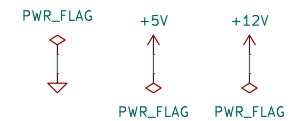
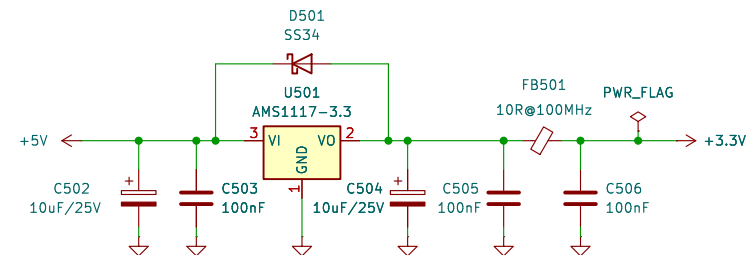


✕ Input merging 12V from barrel Jack vs SATA



✕ 5V source, maybe only 3.3V step down?

Supply – 3.3V



Designed for:
Designed by: Petr_Malaník

Sheet: /System/Power/
File: power.kicad_sch

Title: Power

Size: A4
KiCad E.D.A. 9.0.1

Date:

GIT hash:

Project: SMPBR

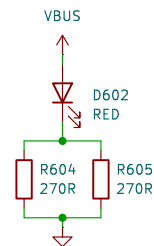
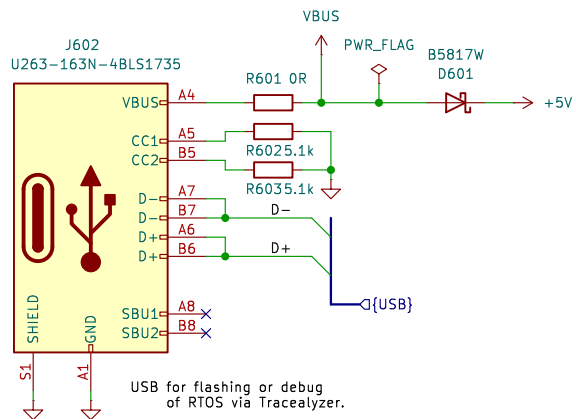
Board: Pump_board

Rev:

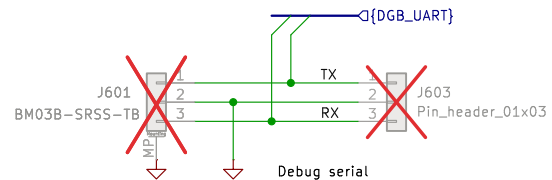
Id: 5/8

**TREND
BIT**

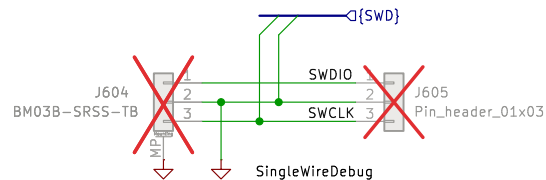
USB – Update & Debug



Serial Debug

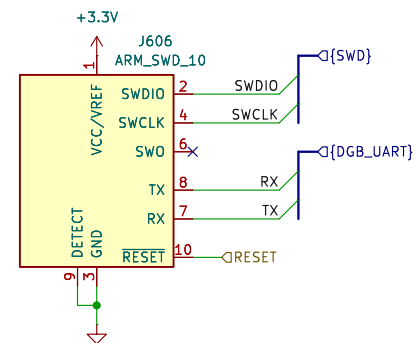


SWD Debug



Pinout of these connectors is intended to be used with RPi debug Probe

Tag–connector



**TREND
BIT**

Designed for:
Designed by: Petr_Malaník

Sheet: /System/Debug Interface/
File: debug.kicad_sch

Title: Debug Interface

Project: SMPBR

Board: Pump_board

Size: A4

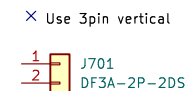
Date:

GIT hash:

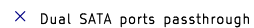
Rev:

KiCad E.D.A. 9.0.1

Id: 6/8



Rev:
Id: 7/8



**TREND
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Rev:
Id: 8/8