

Bottom View



| Designed for: Tro Designed by: Pe | | | | BIT BIT | , | |
|--------------------------------------|---------------|-------------|----------------|---------------------------|----------|----------------|
| Sheet: File: motor_shiel | .d_peristalti | c.kicad_pcb | Project: SMPBR | | [| |
| Title: | | | | Board: shield-peristaltic | | 1 |
| Size: A4 | Date: 2024 | | GIT hash | : e95eb84 | Rev: 1.0 | |
| KiCad E.D.A. 9.0.1+1 + KiBot v1.8.5 | | | | | ld: 1/3 | ٦ |
| 4 | | | 5 | | T. | — ₆ |

Top Assembly (Scale 1:1)

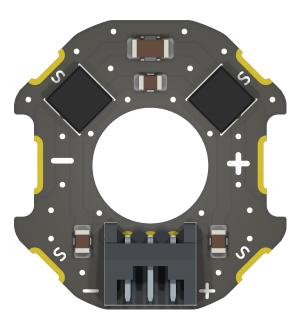
Components

| Type | Front Side | Back Side | Total |
|-------|------------|-----------|-------|
| THT | 1 | 0 | 1 |
| SMD | 6 | 0 | 6 |
| Total | 7 | 0 | 7 |

ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

- 1) DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
- 2) DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
- 3) IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
- 4) CONFORMAL COATING IS NOT REQUIRED.
- 5) DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.





| | 国语 (2) | | | | | |
|-------------------------------------|---------------------------|--|--|--|--|--|
| Proj | Project: SMPBR | | | | | |
| Во | Board: shield-peristaltic | | | | | |
| GIT hash: e95e | eb84 Rev: 1.0 | | | | | |
| KiCad E.D.A. 9.0.1+1 + KiBot v1.8.5 | | | | | | |
| | Во | | | | | |

| KiCad E.D.A. 9.0.1+1 + KiBot v1.8.5 | Id: 2/3 | | 3 | 4 | 5 | |

