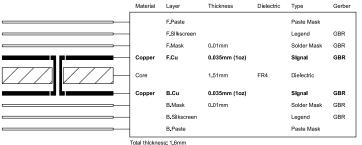
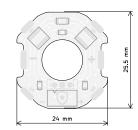
## **Top Fabrication (Scale 1:1)**

## **Layer Stack Legend**





Note: external layer thicknesses are specified after plating

DIMENSIONS OF CIRCUMSIZED RECTANGLE SHOWN ON THIS DRAWING FOR REFERENCE ONLY.

"Edge\_Cuts.GBR" SUFFIX.

3) SEE SEPARATE DRILL FILES WITH ".DRL" SUFFIX FOR HOLE LOCATIONS.

FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED)

2) OUTLINE DEFINED IN SEPARATE GERBER FILE WITH

SELECTED HOLE LOCATIONS SHOWN ON THIS DRAWING FOR REFERENCE ONLY.

4) SURFACE FINISH: HAL LEAD-FREE

1) FABRICATE PER IPC-6012A CLASS 2.

- 5) SOLDERMASK ON BOTH SIDES OF THE BOARD SHALL BE LPI, COLOR BLACK.
- 6) SILK SCREEN LEGEND TO BE APPLIED PER LAYER STACKUP USING WHITE NON-CONDUCTIVE EPOXY INK.
- 7) ALL VIAS ARE TENTED ON BOTH SIDES UNLESS SOLDERMASK OPENED IN GERBER.
- 8) VENDOR SHOULD FOLLOW ROHS COMPLIANT PROCESS AND Pb FREE FOR MANUFACTURING
- 9) PCB MATERIAL REQUIREMENTS:
  - A. FLAMMABILITY RATING MUST MEET OR EXCEED UL94V-0 REQUIREMENTS.
  - B. Tg 170 C OR EQUIVALENT.
  - C. EQUIVALENT MATERIAL SHALL BE ROHS COMPLIANT, HALOGEN FREE AND APPROVED BY TRENDBIT.
- 10) DESIGN GEOMETRY MINIMUM FEATURE SIZES:

BOARD SIZE 24.000 × 25.515 mm BOARD THICKNESS 1.600 mm TRACE WIDTH 1.000 mm TRACE TO TRACE 0.200 mm MIN. HOLE (PTH) 0.500 mm MIN. HOLE (NPTH) N/A mm ANNULAR RING 0.250 mm COPPER TO HOLE 0.250 mm COPPER TO EDGE 0.250 mm HOLE TO HOLE 0.250 mm

All dimensions are in millimeters unless otherwise specified.

Designed for: TrendBit Designed by: Petr\_Malaník

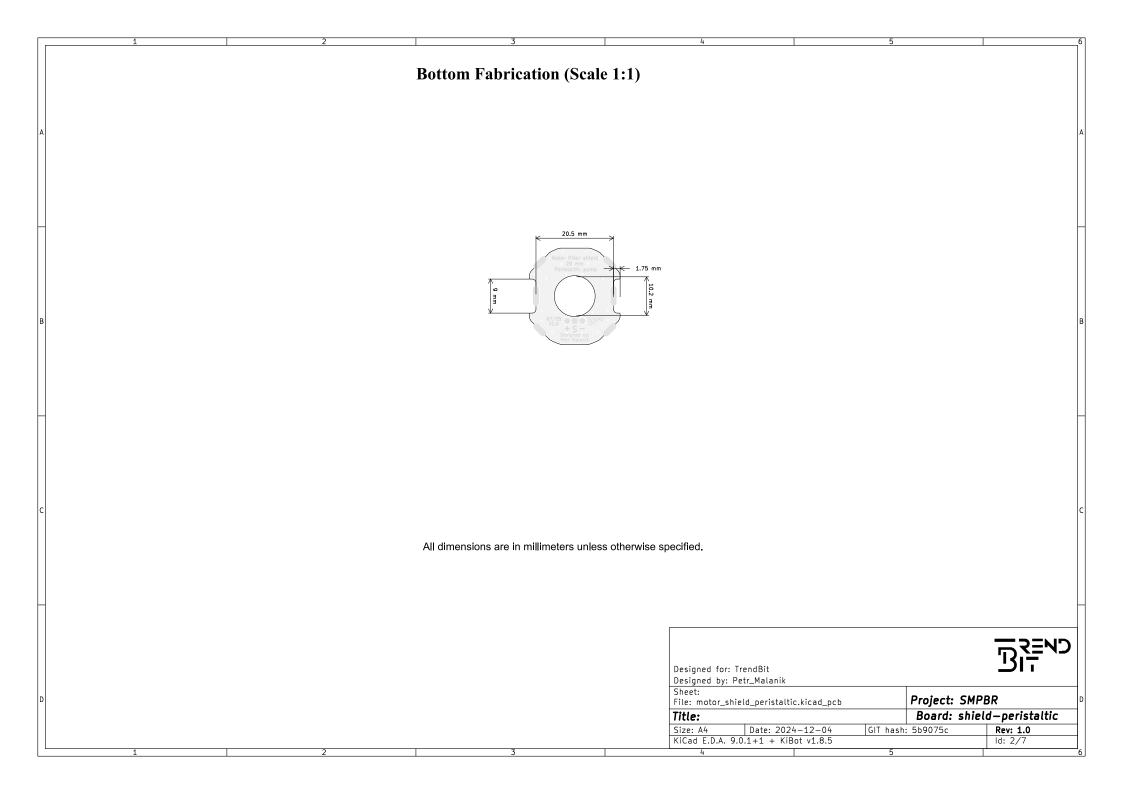
File: motor\_shield\_peristaltic.kicad\_pcb

Board: shield-peristaltic Title: Size: A4 Date: 2024-12-04 GIT hash: 5b9075c Rev: 1.0

KiCad E.D.A. 9.0.1+1 + KiBot v1.8.5

ld: 1/7

Project: SMPBR



## **Drill Drawing L1 - L2 (Scale 1:1)**

## **Drill Table**

Symbol	Count	Hole Size	Plated	Hole Shape	Drill Layer Pair	Hole Type
×	22	0.50mm (19.69mils)	PTH	Round	F.Cu - B.Cu	Via
0	6	0.60mm (23.62mlls)	PTH	Slot	F.Cu - B.Cu	Pad
+	3	0.80mm (31.50mlls)	PTH	Round	F.Cu - B.Cu	Pad
	Total 31					

