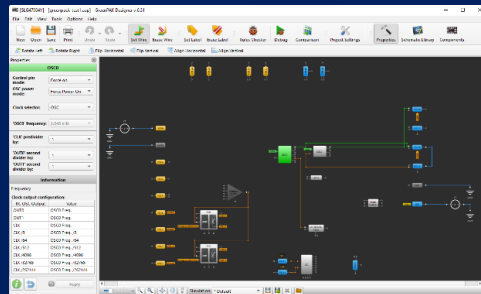


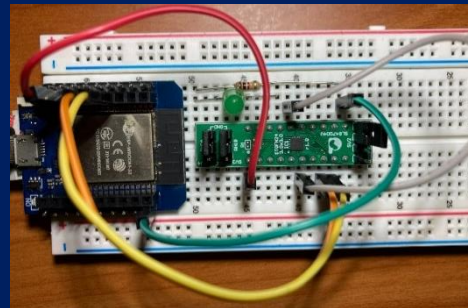
# Project Design using Quercus

## GreenPAK\* Designer Software [GoConfigure Software Hub]



1) The FPGA functionality is specified using the GoConfigure drag & drop interface. The output is an FPGA file.

## Quercus Project Board (version - white)



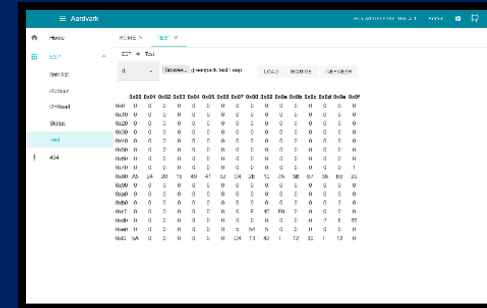
ESP32

FPGA

3) The ESP32 uses the 'ant' firmware to program the FPGA.

4) The ESP32 will also be available to perform other functions specified by the user via an application interface. (TBD)

## 'Aardvark' [Quercus Tool]



2) The Aardvark web interface is used to wirelessly upload the resulting FPGA application code to the ESP32.