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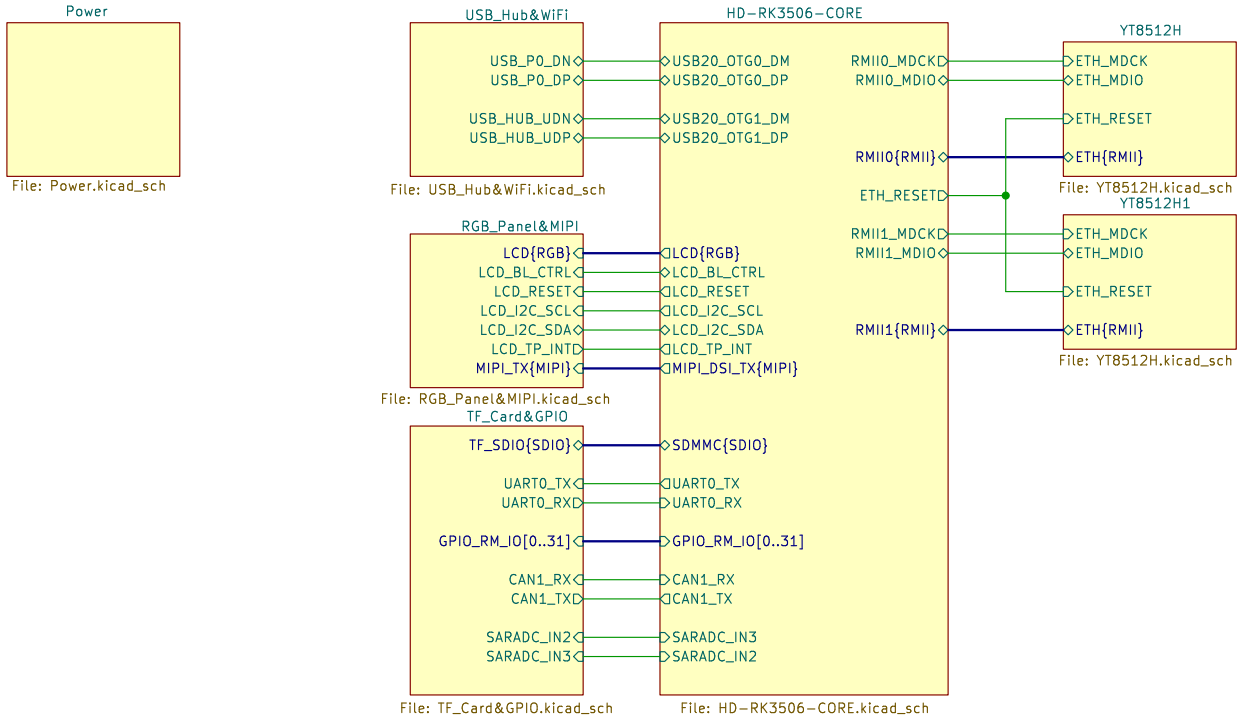

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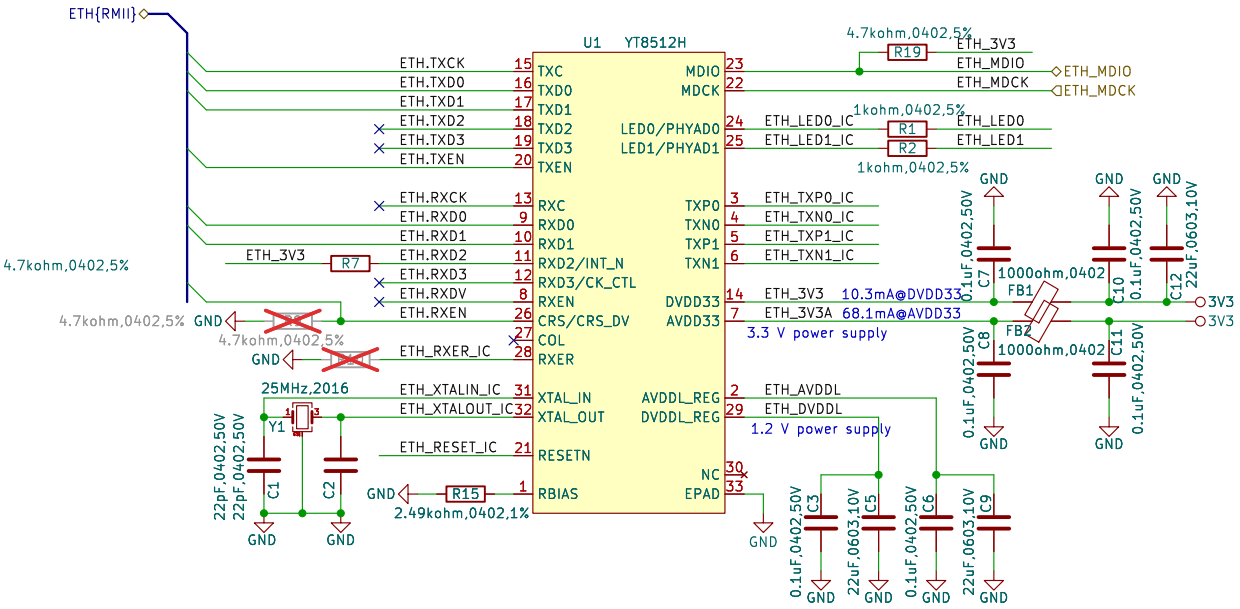
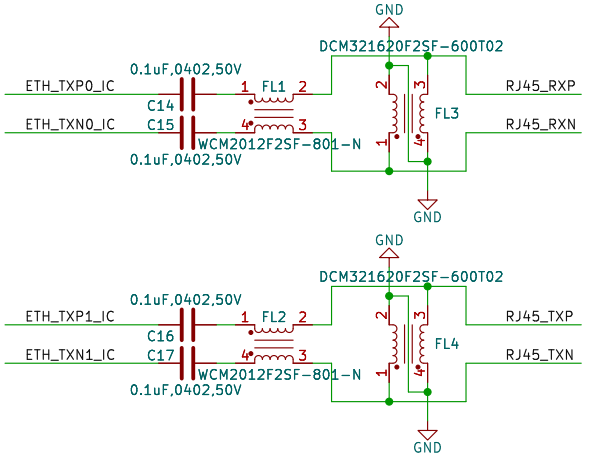
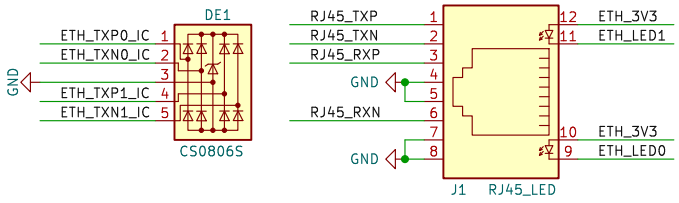






Power On Strapping for PHY address setting

LED0/PHYAD[0](PD)	LED0/PHYAD[1](PD)	PHY Address
0	0	00000
0	1	00010
1	0	00001
1	1	00011

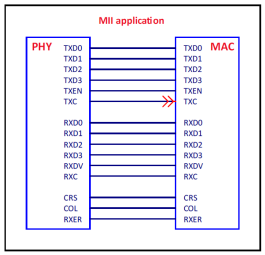
Power On Strapping for LED function selection

RXD1(PD)	LED function
0	LED function
1	WOL function

Power On Strapping for MII/RMII mode selection

RXEN(PD)	RXD3(PD)	PHY Address
0	0	MII mode
0	1	MII mode
1	0	RMII2 mode
1	1	RMII1 mode

RMII2:TXC 50Mhz reference clock is output by default
RMII1:TXC 50Mhz reference clock is input



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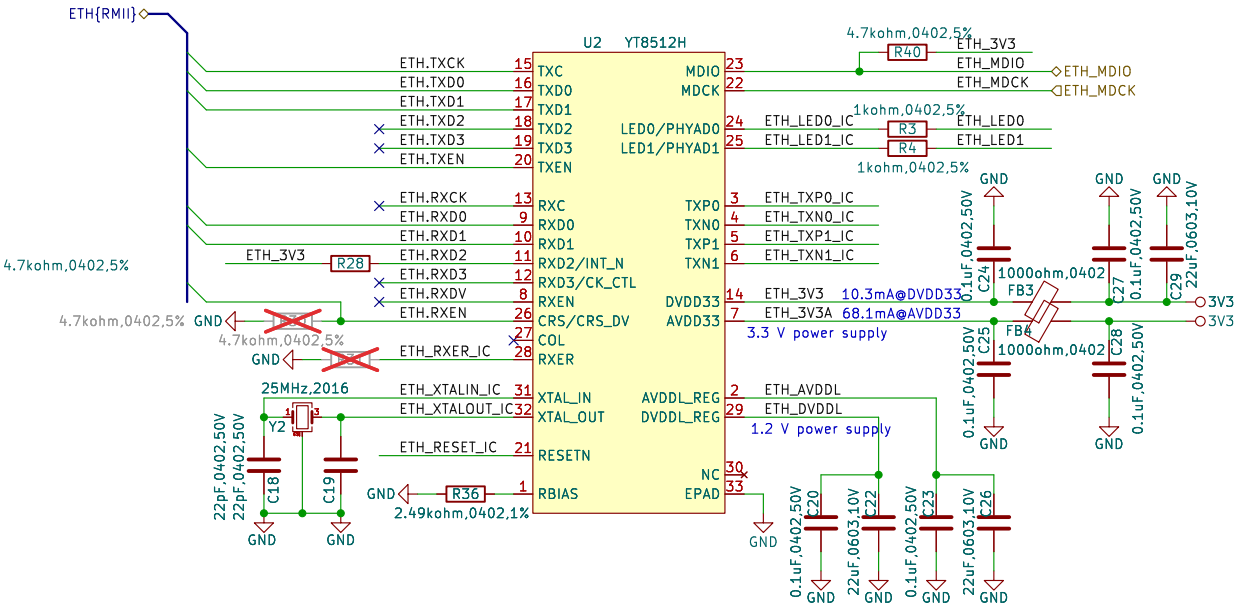
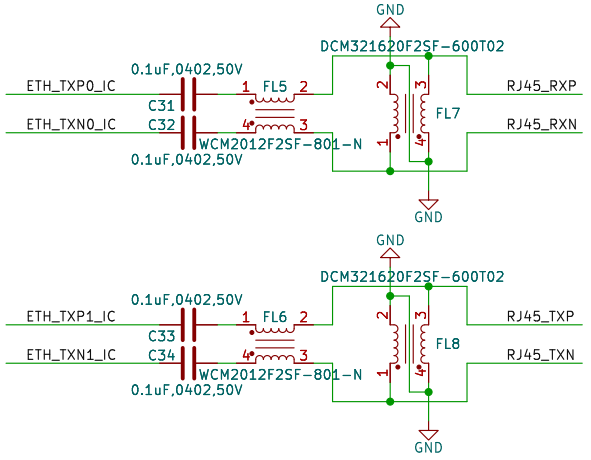
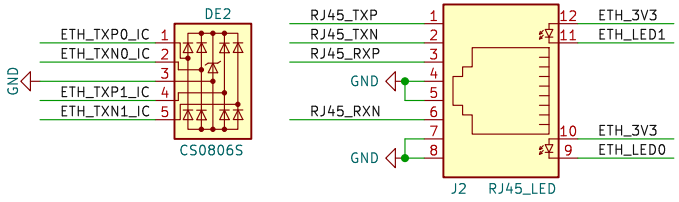






Power On Strapping for PHY address setting

LED0/PHYAD[0](PD)	LED0/PHYAD[1](PD)	PHY Address
0	0	00000
0	1	00010
1	0	00001
1	1	00011

Power On Strapping for LED function selection

RXD1(PD)	LED function
0	LED function
1	WOL function

ETH.RXD1 — 4.7kohm,0402,5% — ETH_3V3

ETH_LED0_IC — R32 — 4.7kohm,0402,5% — ETH_3V3

ETH_LED1_IC — R33 — 4.7kohm,0402,5%

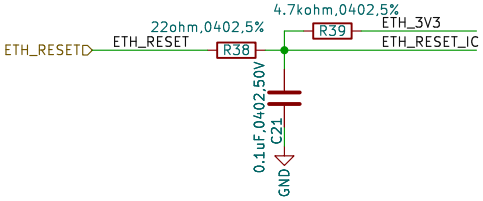
Power On Strapping for MII/RMII mode selection

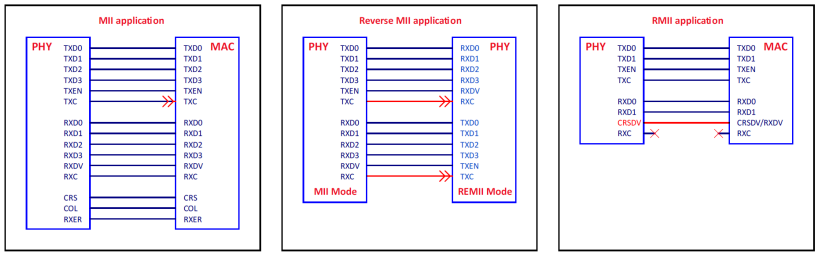
RXEN(PD)	RXD3(PD)	PHY Address
0	0	MII mode
0	1	MII mode
1	0	RMII2 mode
1	1	RMII1 mode

RMII2:TXC 50Mhz reference clock is output by default
RMII1:TXC 50Mhz reference clock is input

ETH.RXD3 — 4.7kohm,0402,5% — ETH_3V3

ETH.RXDV — R35 — 4.7kohm,0402,5%





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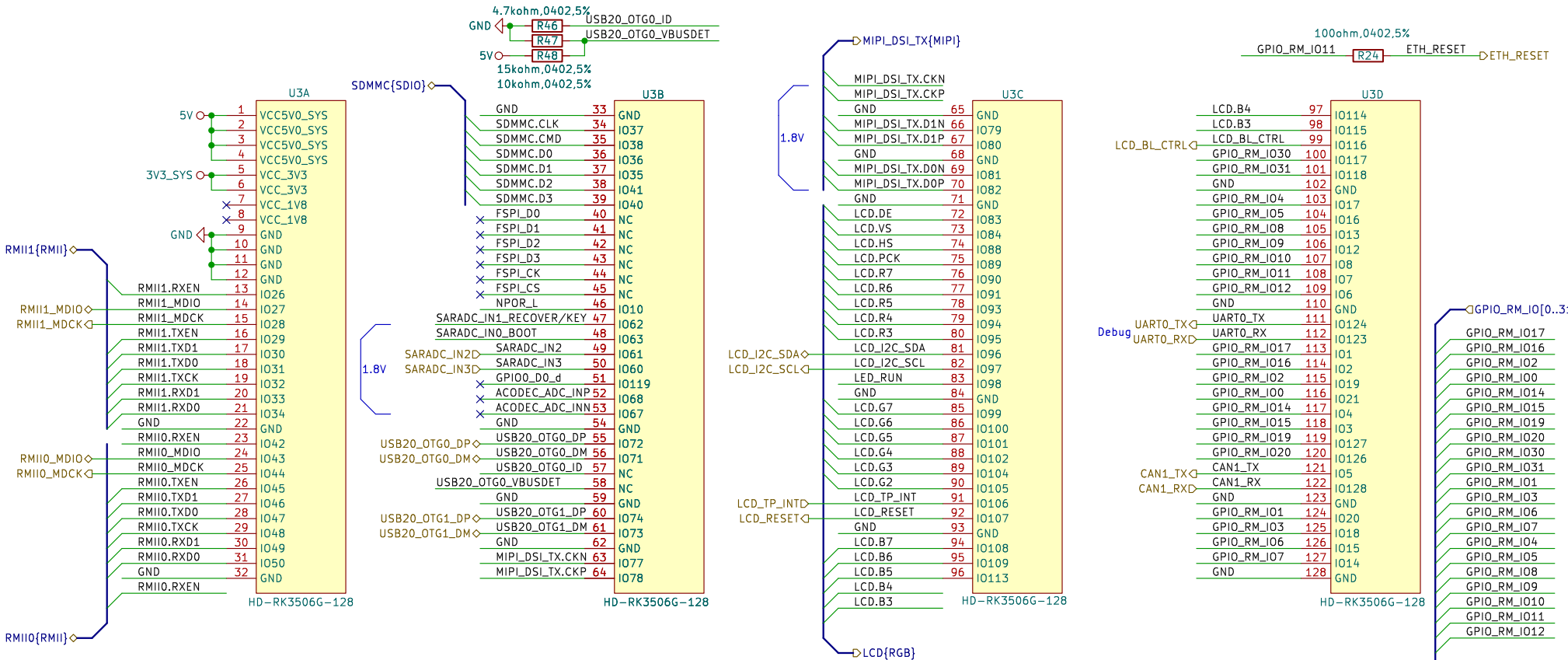
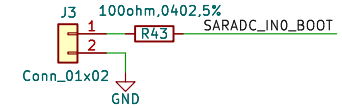
RK3506 CORE



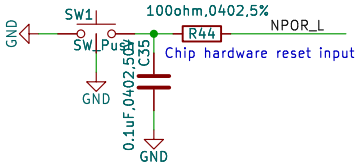
MASKROM



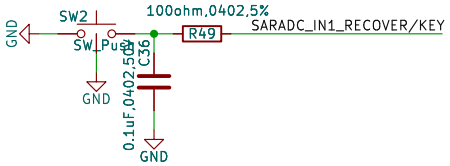
SARADC_IN0_BOOT TABLE

Item	Rup	Rdown	ADC	BOOT MODE
LEVEL1	DNP	100K	0	USB(Maskrom mode)/SPI2APB
LEVEL2	100K	12K	110	SPI2APB
LEVEL3	100K	27K	237	FSPI--USB/SPI2APB
LEVEL4	100K	51K	345	
LEVEL5	100K	82K	461	
LEVEL6	100K	120K	558	SDMMC(eMMC/SD Card)--USB/SPI2APB
LEVEL7	100K	200K	682	
LEVEL8	100K	330K	785	
LEVEL9	100K	820K	912	
LEVEL10	10K	DNP	1023	FSPI--SDMMC(eMMC/SD Card)--USB/SPI2APB

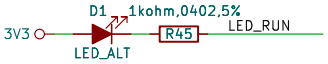
RESET



RECOVERY in SARADC_IN1



LED RUN



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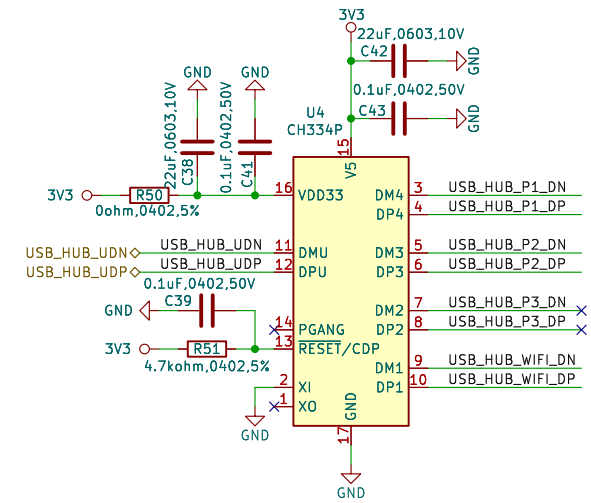
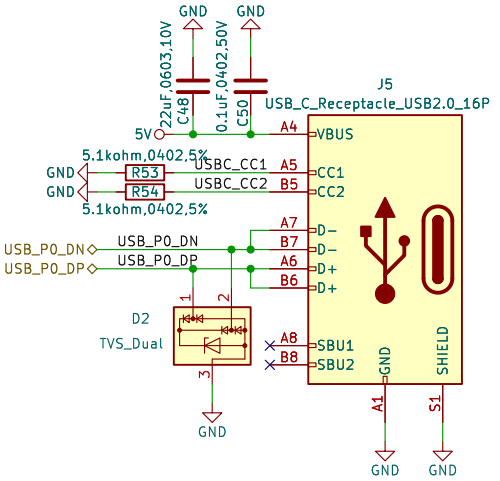
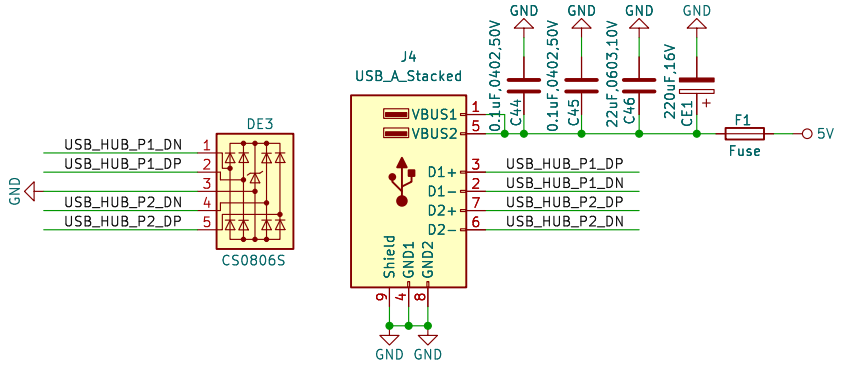
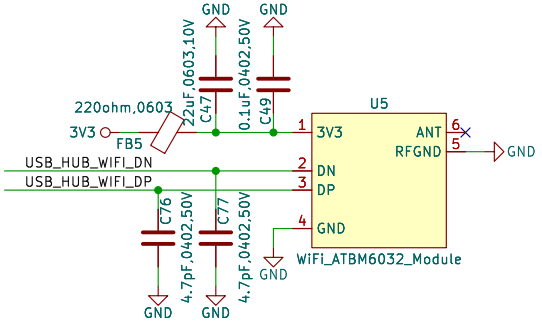
USB-HUB&WiFi









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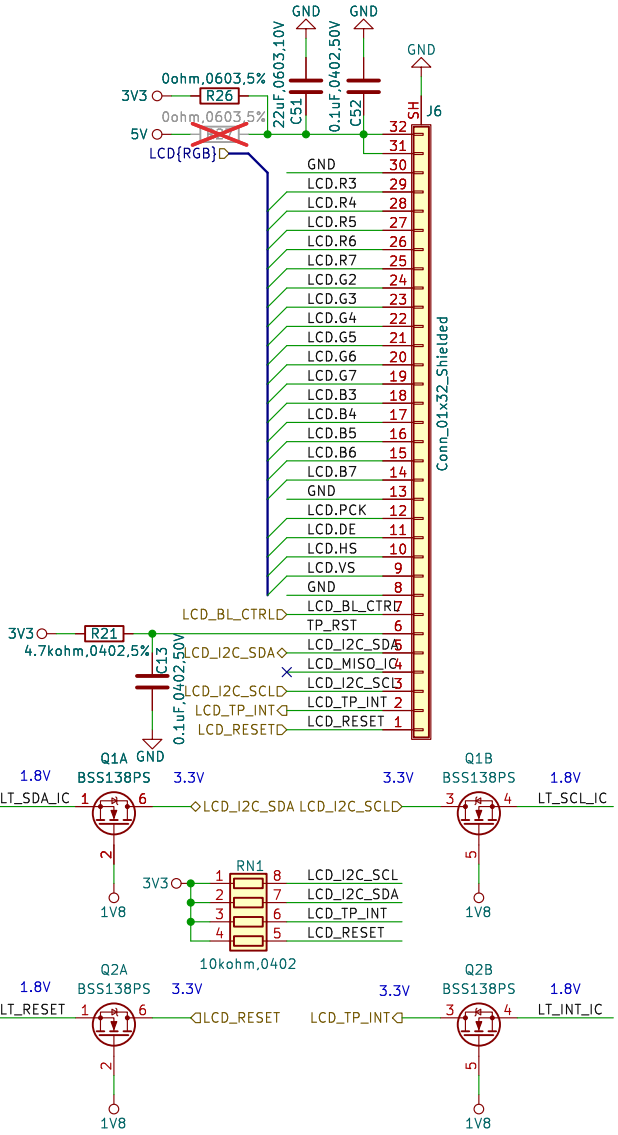
Panel



LT8912B

抱歉！龙讯的LT8912B的图纸暂时不能公开。

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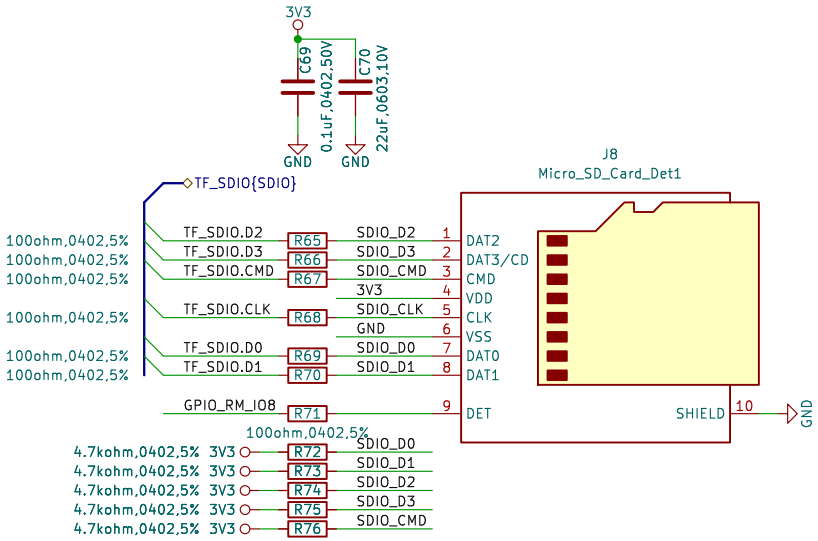
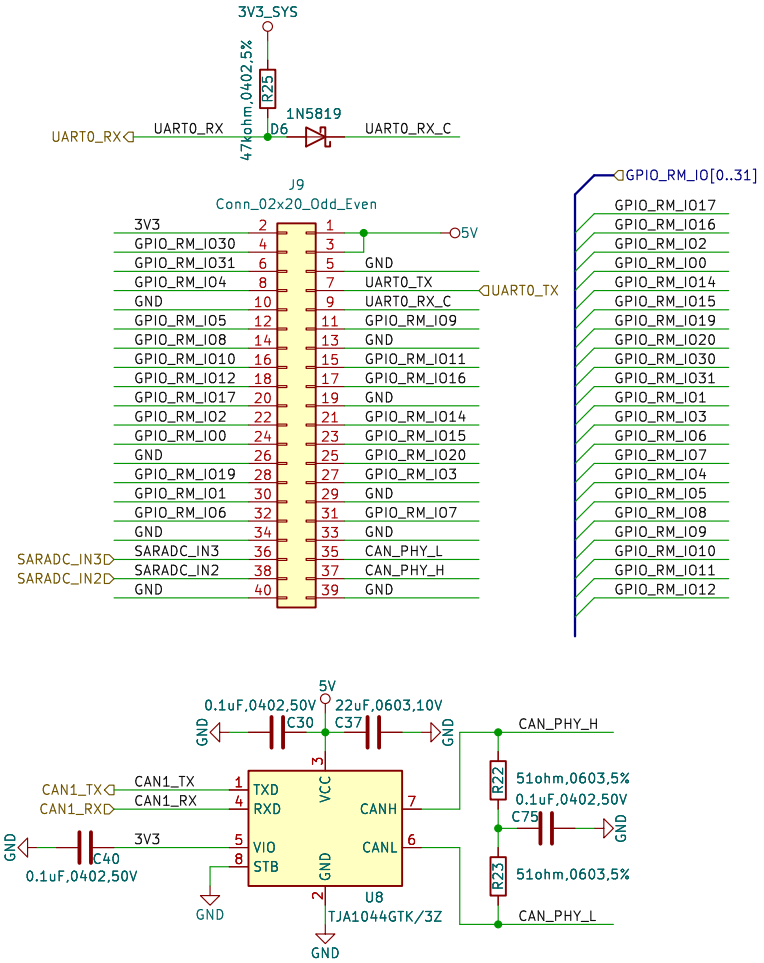
TF Card



GPIO



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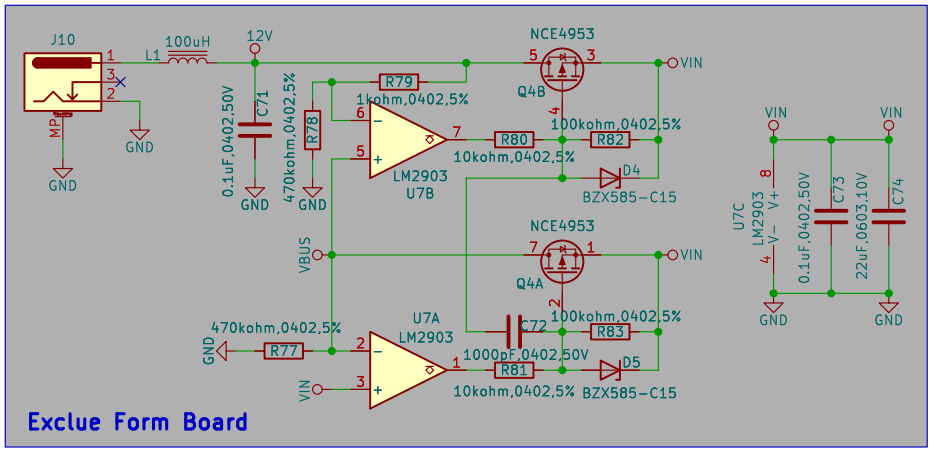
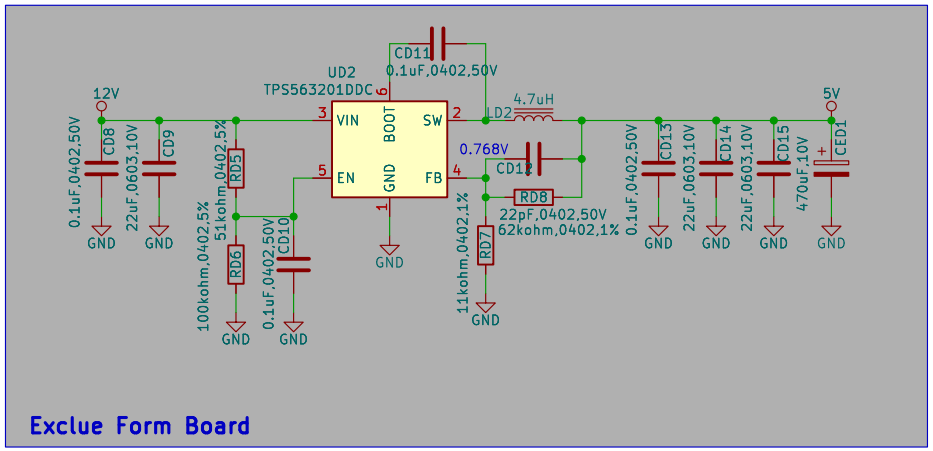
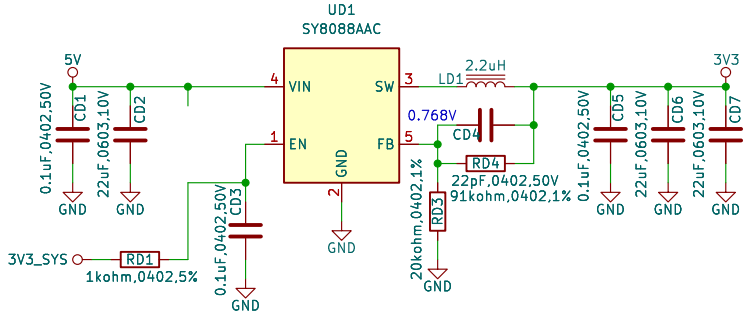
12V&VBUS Power Input



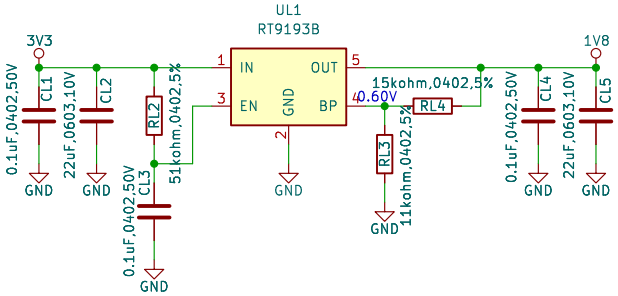
12V->5V



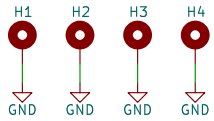
5V->3.3V



3V3->1.8V



HOLE



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