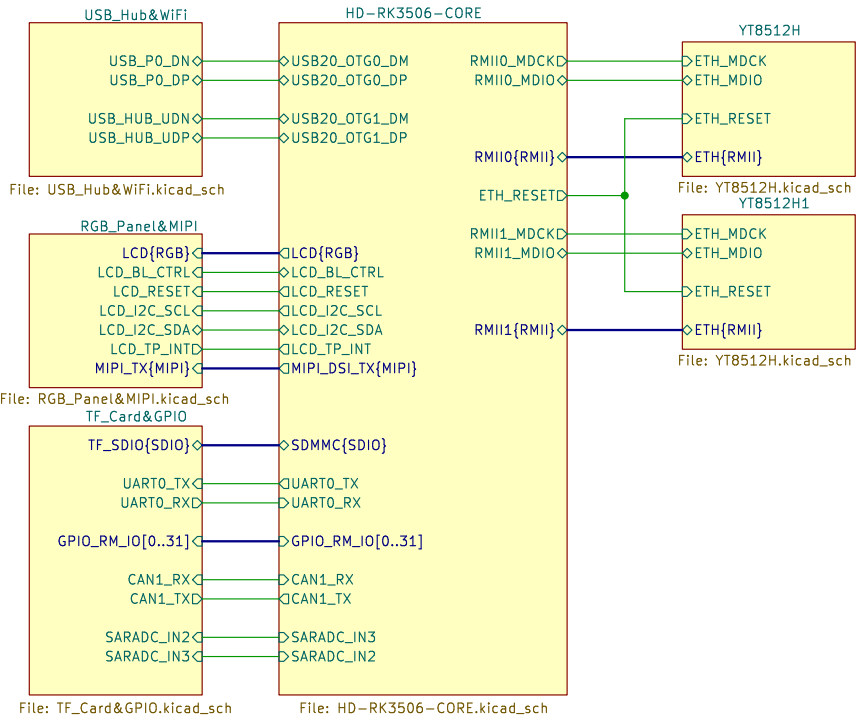
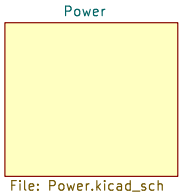
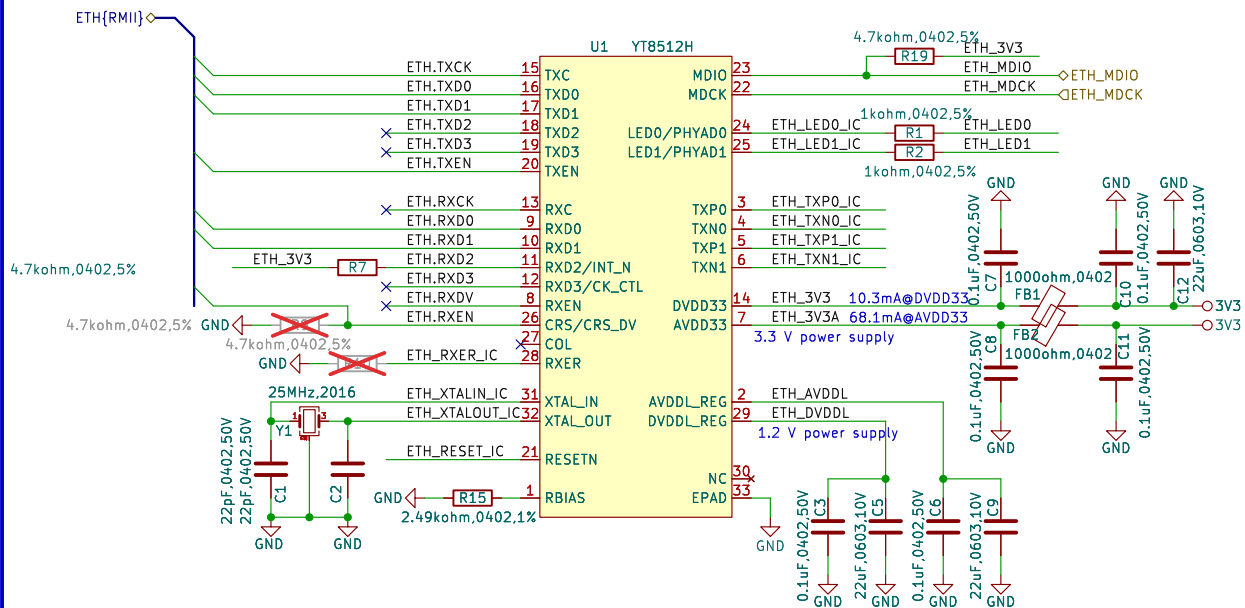


RK3506_DEVBRD_1V0



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KiCad E.D.A. 9.0.6		Id: 1/8

YT8512



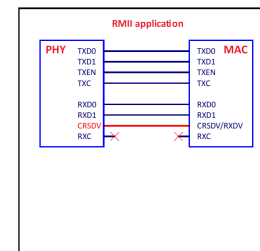
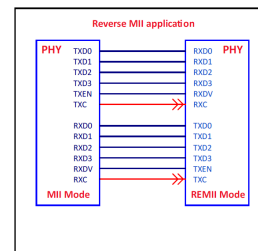
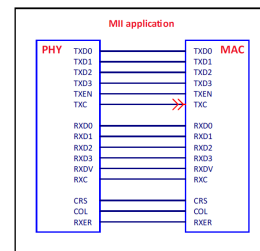
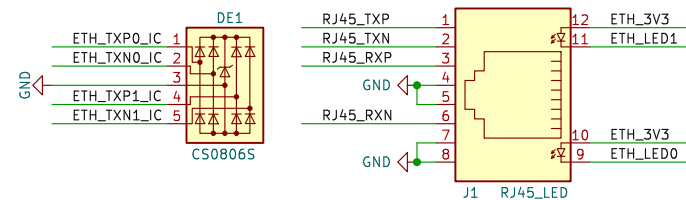
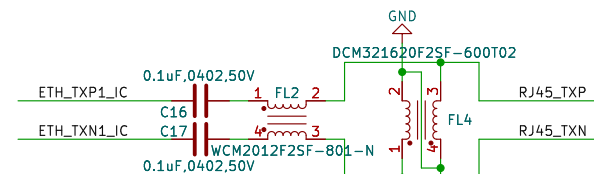
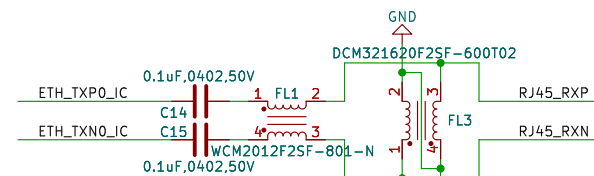
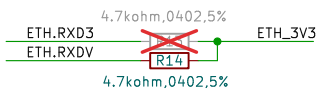
LED0/PHYAD[0](PD)	LED0/PHYAD[1](PD)	PHY Address
0	0	00000
0	1	00010
1	0	00001
1	1	00011

RXD1(PD)	LED function
0	LED function
1	WOL function



RXEN(PD)	RXD3(PD)	PHY Address
0	0	MII mode
0	1	MII mode
1	0	RMI12 mode
1	1	RMI11 mode

RMI2:TXC	50Mhz	reference clock is output by default
RMI1:TXC	50Mhz	reference clock is input



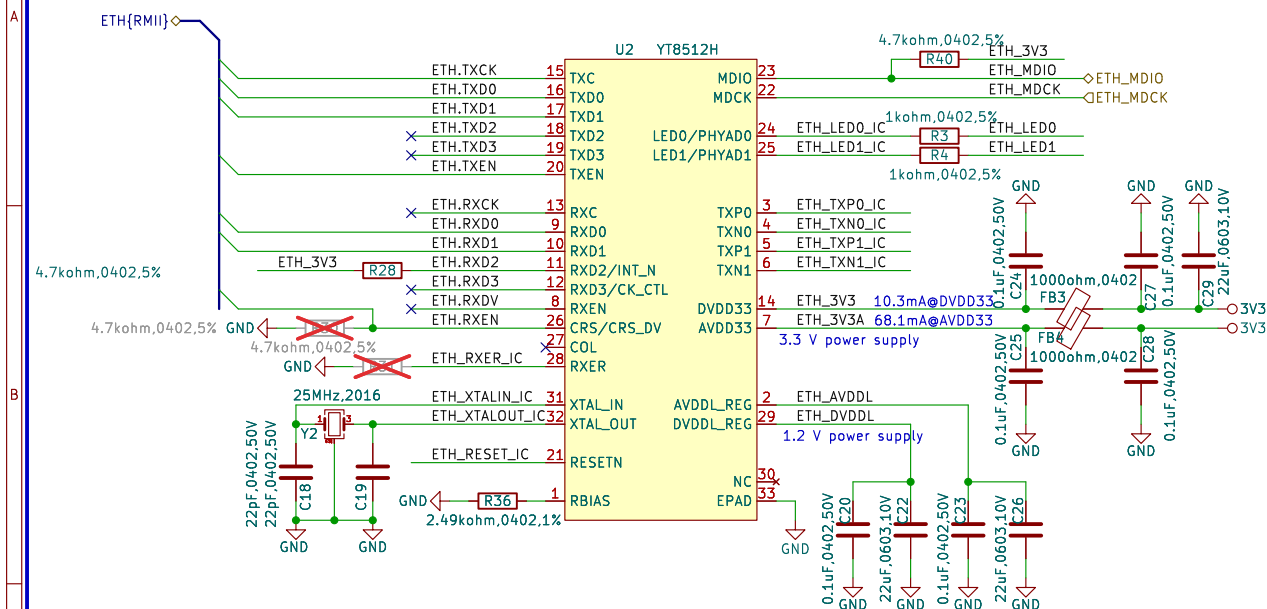
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Rev:
Id: 2/8

YT8512



Power On Strapping for PHY address setting

LED0/PHYAD[0](PD)	LED0/PHYAD[1](PD)	PHY Address
0	0	00000
0	1	00010
1	0	00001
1	1	00011

Power On Strapping for LED function selection

RXD1(PD)	LED function
0	LED function
1	WOL function

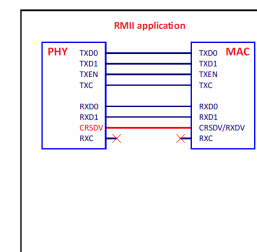
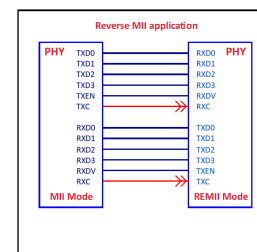
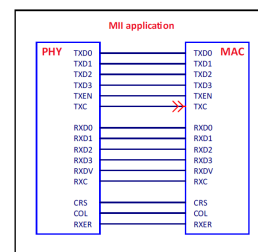
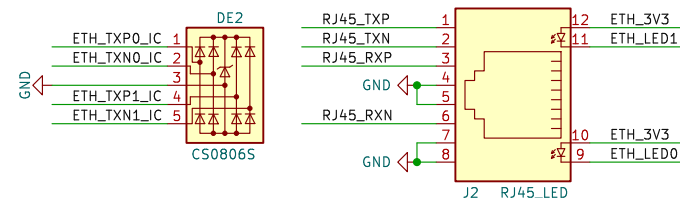
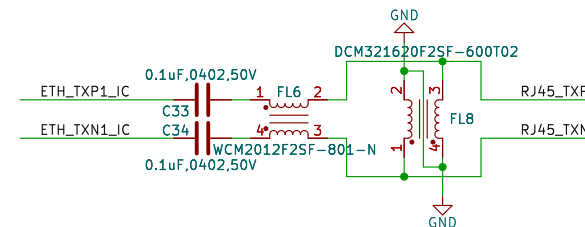
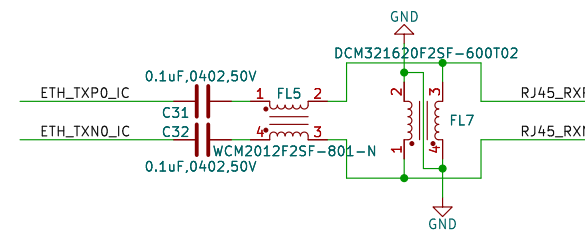
ETH_RXD1 ~~ETH_3V3~~

4.7kohm,0402,5%

Power On Strapping for MII/RMII mode selection

RXEN(PD)	RXD3(PD)	PHY Address
0	0	MI1 mode
0	1	MI1 mode
1	0	RMII2 mode
1	1	RMII1 mode

RMII2:TXC 50Mhz reference clock is output by default
RMII1:TXC 50Mhz reference clock is input



Sheet: /YT8512H1/
File: YT8512H.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 9.0.6

Rev:

Id: 3/8

RK3506 CORE

U3A

VCC5V0_SYS 1
VCC5V0_SYS 2
VCC5V0_SYS 3
VCC5V0_SYS 4
VCC3V3 5
VCC3V3 6
VCC1V8 7
VCC1V8 8
GND 9
GND 10
GND 11
GND 12
RMII1_RXEN 13
RMII1_MDIO 14
RMII1_MDCK 15
RMII1_TXEN 16
RMII1_TXD1 17
RMII1_TXD0 18
RMII1_TXCK 19
RMII1_RXD1 20
RMII1_RXD0 21
GND 22
RMII0_RXEN 23
RMII0_MDIO 24
RMII0_MDCK 25
RMII0_TXEN 26
RMII0_TXD1 27
RMII0_TXD0 28
RMII0_TXCK 29
RMII0_RXD1 30
RMII0_RXD0 31
GND 32
RMII0_RXEN

U3B

GND 33
SDMMC_CLK 34
SDMMC_CMD 35
SDMMC_D0 36
SDMMC_D1 37
SDMMC_D2 38
SDMMC_D3 39
FSPL_D0 40
FSPL_D1 41
FSPL_D2 42
FSPL_D3 43
FSPL_CLK 44
FSPL_CS 45
NPOR_L 46
SARADC_IN1_RECOVER/KEY 47
SARADC_IN0_BOOT 48
SARADC_IN2 49
SARADC_IN3 50
GPIO0_D0_d 51
ACODEC_ADC_INP 52
ACODEC_ADC_INN 53
GND 54
USB20_OTG0_DP 55
USB20_OTG0_DM 56
USB20_OTG0_ID 57
USB20_OTG0_VBUSDET 58
GND 59
USB20_OTG1_DP 60
USB20_OTG1_DM 61
GND 62
MIPI_DSI_TX_CKN 63
MIPI_DSI_TX_CKP 64

U3C

GND 65
MIPI_DSI_TX_CKN 66
MIPI_DSI_TX_D1P 67
MIPI_DSI_TX_D0P 68
MIPI_DSI_TX_D0P 69
GND 70
LCD_DE 71
LCD_VS 72
LCD_HS 73
LCD_PCK 74
LCD_R7 75
LCD_R6 76
LCD_R5 77
LCD_R4 78
LCD_R3 79
LCD_I2C_SDA 80
LCD_I2C_SCL 81
LED_RUN 82
GND 83
LCD_G7 84
LCD_G6 85
LCD_G5 86
LCD_G4 87
LCD_G3 88
LCD_G2 89
LCD_TP_INT 90
LCD_RESET 91
GND 92
LCD_B7 93
LCD_B6 94
LCD_B5 95
LCD_B4 96
LCD_B3 97

U3D

LCD_B4 97
LCD_B3 98
LCD_BL_CTRL 99
GPIO_RM_I030 100
GPIO_RM_I031 101
GND 102
GPIO_RM_I04 103
GPIO_RM_I05 104
GPIO_RM_I08 105
GPIO_RM_I09 106
GPIO_RM_I010 107
GPIO_RM_I011 108
GPIO_RM_I012 109
GND 110
UART0_TX 111
UART0_RX 112
GPIO_RM_I017 113
GPIO_RM_I016 114
GPIO_RM_I02 115
GPIO_RM_I00 116
GPIO_RM_I014 117
GPIO_RM_I015 118
GPIO_RM_I019 119
GPIO_RM_I020 120
CAN1_TX 121
CAN1_RX 122
GND 123
GPIO_RM_I01 124
GPIO_RM_I03 125
GPIO_RM_I06 126
GPIO_RM_I04 127
GPIO_RM_I05 128
GND 129

MASKROM

J3 100ohm,0402,5% SARADC_IN0_BOOT
Conn_01x02
GND

RESET

SW1 100ohm,0402,5% NPOR_L
Chip hardware reset input
0.1uF,0402,5%
GND

RECOVERY in SARADC_IN1

SW2 100ohm,0402,5% SARADC_IN1_RECOVER/KEY
0.1uF,0402,5%
GND

LED RUN

D1 1kohm,0402,5% LED_RUN
LED_ALT
3V3

Item	RUP	RDOWN	ADC	BOOT MODE
LEVEL1	000	000	0	USB (Fastboot mode)/SPI2APB
LEVEL2	000	000	1	SPI2APB
LEVEL3	000	000	2	FSPI-USB/SPI2APB
LEVEL4	000	000	3	FSPI-USB/SPI2APB
LEVEL5	000	000	4	FSPI-USB/SPI2APB
LEVEL6	000	000	5	FSPI-USB/SPI2APB
LEVEL7	000	000	6	FSPI-USB/SPI2APB
LEVEL8	000	000	7	FSPI-USB/SPI2APB
LEVEL9	000	000	8	FSPI-USB/SPI2APB
LEVEL10	000	000	9	FSPI-USB/SPI2APB
LEVEL11	000	000	10	FSPI-USB/SPI2APB
LEVEL12	000	000	11	FSPI-USB/SPI2APB
LEVEL13	000	000	12	FSPI-USB/SPI2APB
LEVEL14	000	000	13	FSPI-USB/SPI2APB
LEVEL15	000	000	14	FSPI-USB/SPI2APB
LEVEL16	000	000	15	FSPI-USB/SPI2APB
LEVEL17	000	000	16	FSPI-USB/SPI2APB
LEVEL18	000	000	17	FSPI-USB/SPI2APB
LEVEL19	000	000	18	FSPI-USB/SPI2APB
LEVEL20	000	000	19	FSPI-USB/SPI2APB
LEVEL21	000	000	20	FSPI-USB/SPI2APB
LEVEL22	000	000	21	FSPI-USB/SPI2APB
LEVEL23	000	000	22	FSPI-USB/SPI2APB
LEVEL24	000	000	23	FSPI-USB/SPI2APB
LEVEL25	000	000	24	FSPI-USB/SPI2APB
LEVEL26	000	000	25	FSPI-USB/SPI2APB
LEVEL27	000	000	26	FSPI-USB/SPI2APB
LEVEL28	000	000	27	FSPI-USB/SPI2APB
LEVEL29	000	000	28	FSPI-USB/SPI2APB
LEVEL30	000	000	29	FSPI-USB/SPI2APB
LEVEL31	000	000	30	FSPI-USB/SPI2APB
LEVEL32	000	000	31	FSPI-USB/SPI2APB
LEVEL33	000	000	32	FSPI-USB/SPI2APB
LEVEL34	000	000	33	FSPI-USB/SPI2APB
LEVEL35	000	000	34	FSPI-USB/SPI2APB
LEVEL36	000	000	35	FSPI-USB/SPI2APB
LEVEL37	000	000	36	FSPI-USB/SPI2APB
LEVEL38	000	000	37	FSPI-USB/SPI2APB
LEVEL39	000	000	38	FSPI-USB/SPI2APB
LEVEL40	000	000	39	FSPI-USB/SPI2APB
LEVEL41	000	000	40	FSPI-USB/SPI2APB
LEVEL42	000	000	41	FSPI-USB/SPI2APB
LEVEL43	000	000	42	FSPI-USB/SPI2APB
LEVEL44	000	000	43	FSPI-USB/SPI2APB
LEVEL45	000	000	44	FSPI-USB/SPI2APB
LEVEL46	000	000	45	FSPI-USB/SPI2APB
LEVEL47	000	000	46	FSPI-USB/SPI2APB
LEVEL48	00			

SARADC_IN0_BOOT TABLE				
ITEM	Rup	Rdown	ADC	BOOT MODE
LEVEL1	0x0	100x	0	USB (Maxim mode)/SP12APB
LEVEL1	100x	11x	10x	SP12APB
LEVEL1	100x	27x	217	FSP1-USB/SP12APB
LEVEL1	100x	15x	145	
LEVEL1	100x	82x	487	
LEVEL1	100x	120x	318	SDMMC/4MMC/SD Card-USB/SP12APB
LEVEL1	100x	200x	682	
LEVEL1	100x	330x	785	
LEVEL1	100x	820x	912	
LEVEL18	10x	0x0	1003	FSP1-SDMMC/4MMC/SD Card-USB/SP12APB

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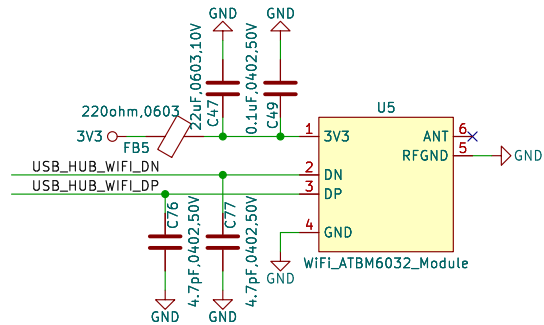
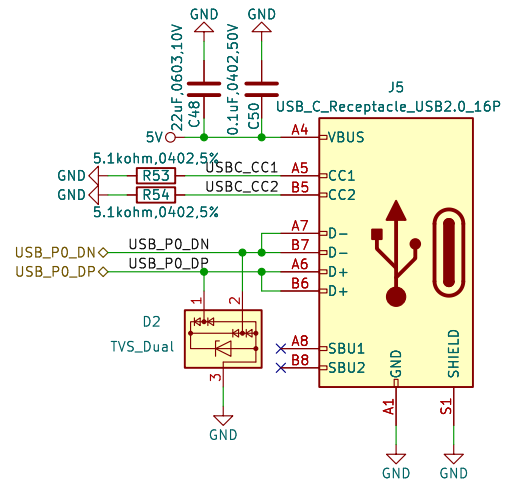
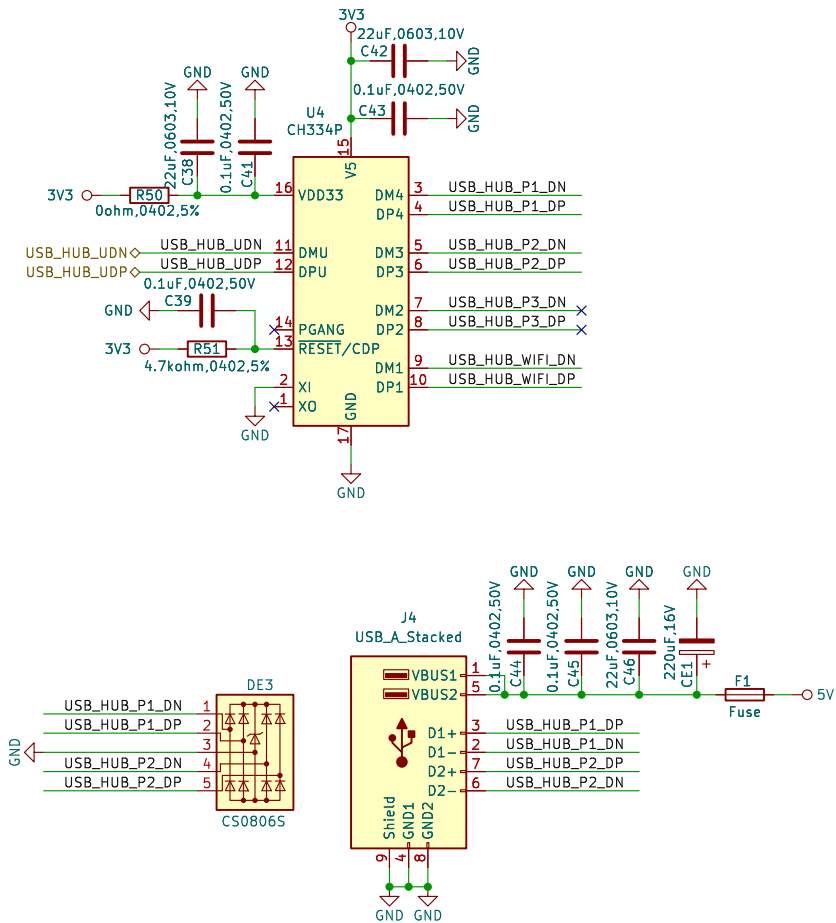
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Row

Id: 4/8

USB-HUB&WiFi



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Rev:

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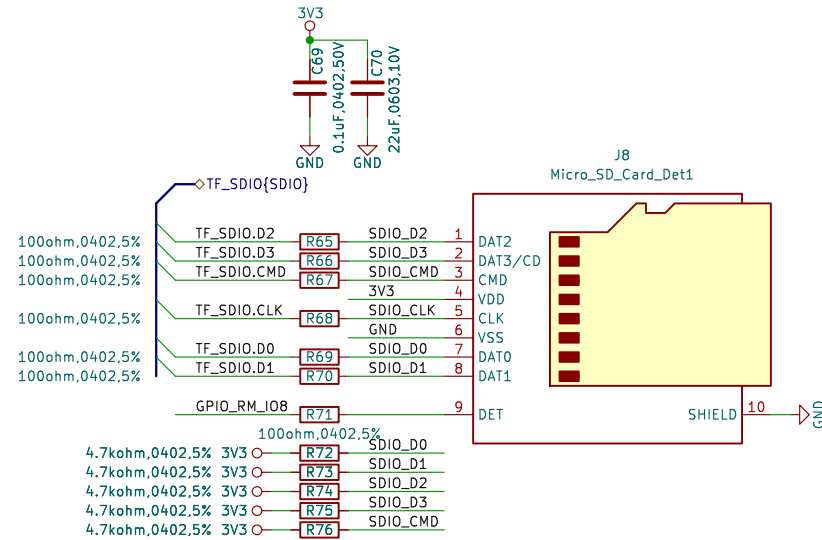
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LT8912B

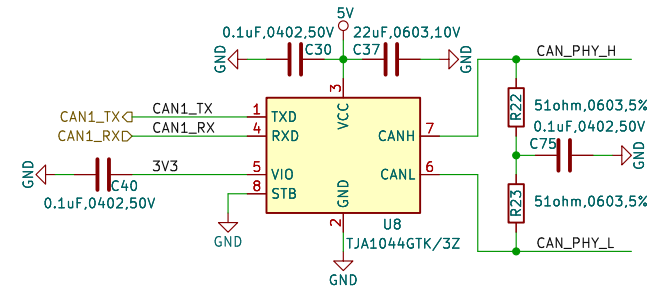
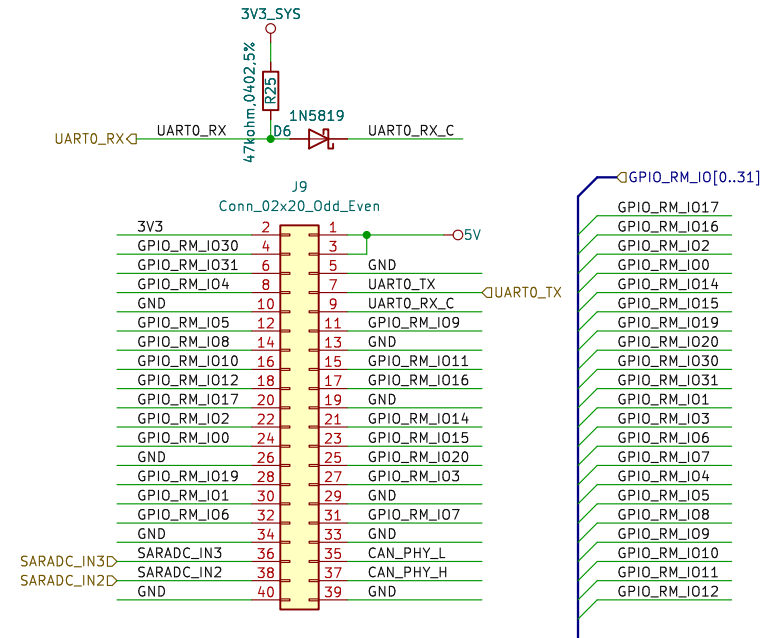


Id: 6/8

TF Card



GPIO



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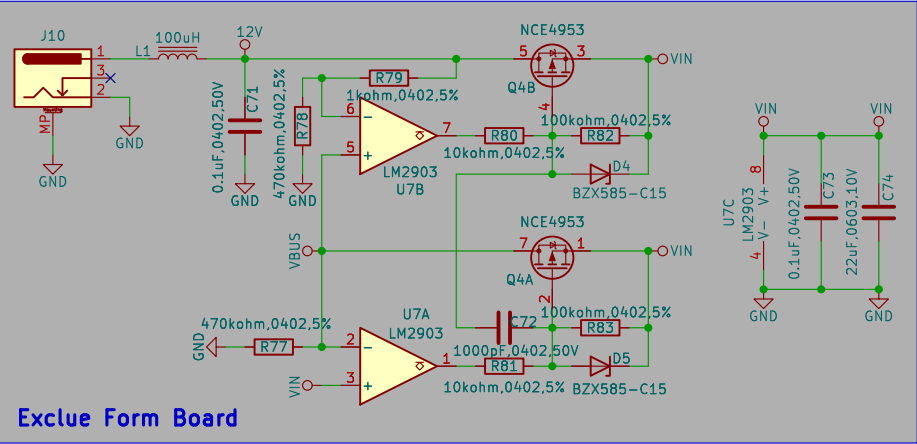
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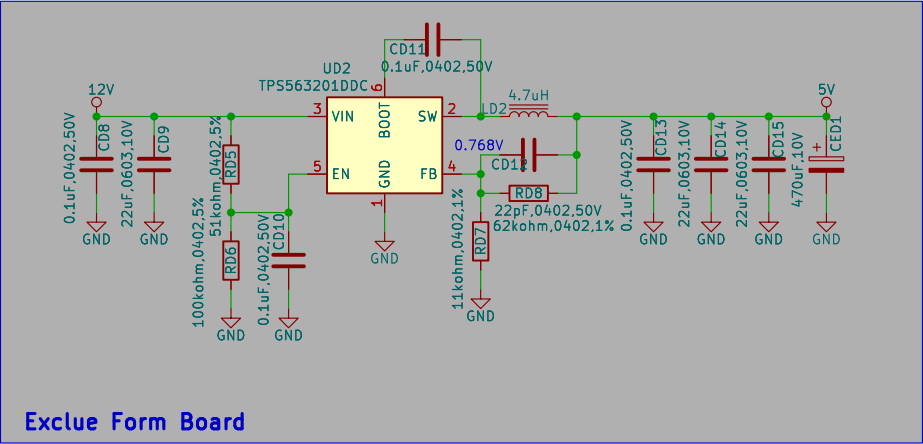
Rev:

Id: 8/8

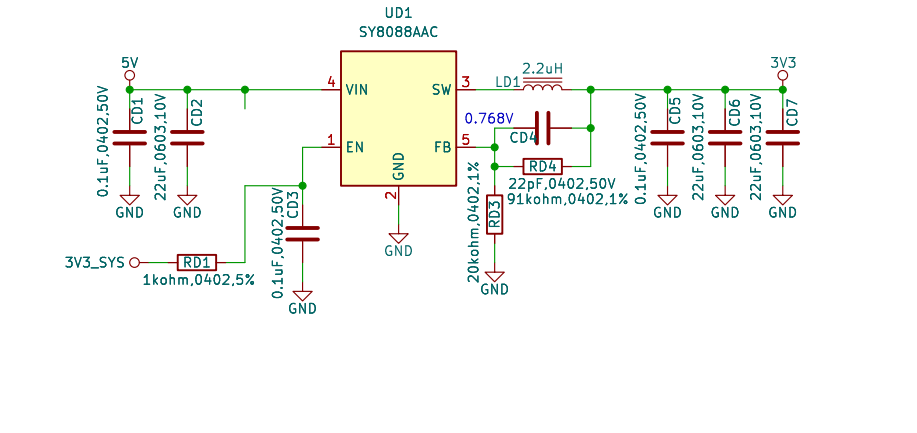
12V&VBUS Power Input



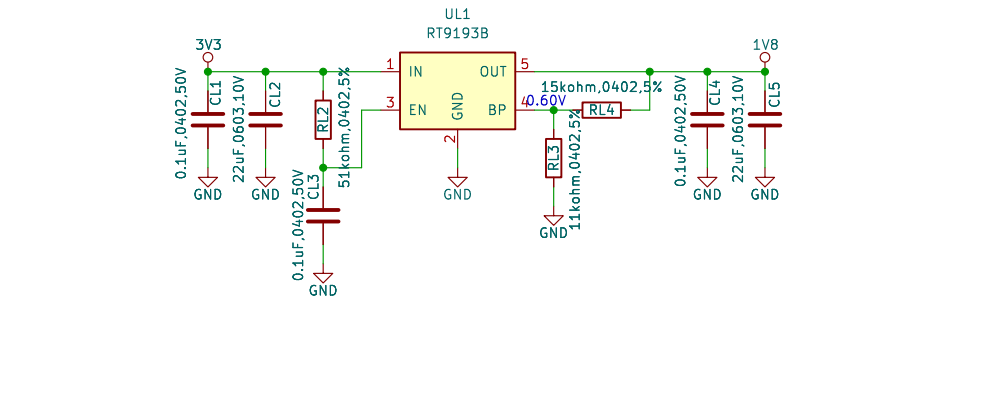
12V->5V



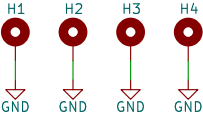
5V->3.3V



3V3->1.8V



HOLE



Sheet: /Power/		File: Power.kicad_sch	
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