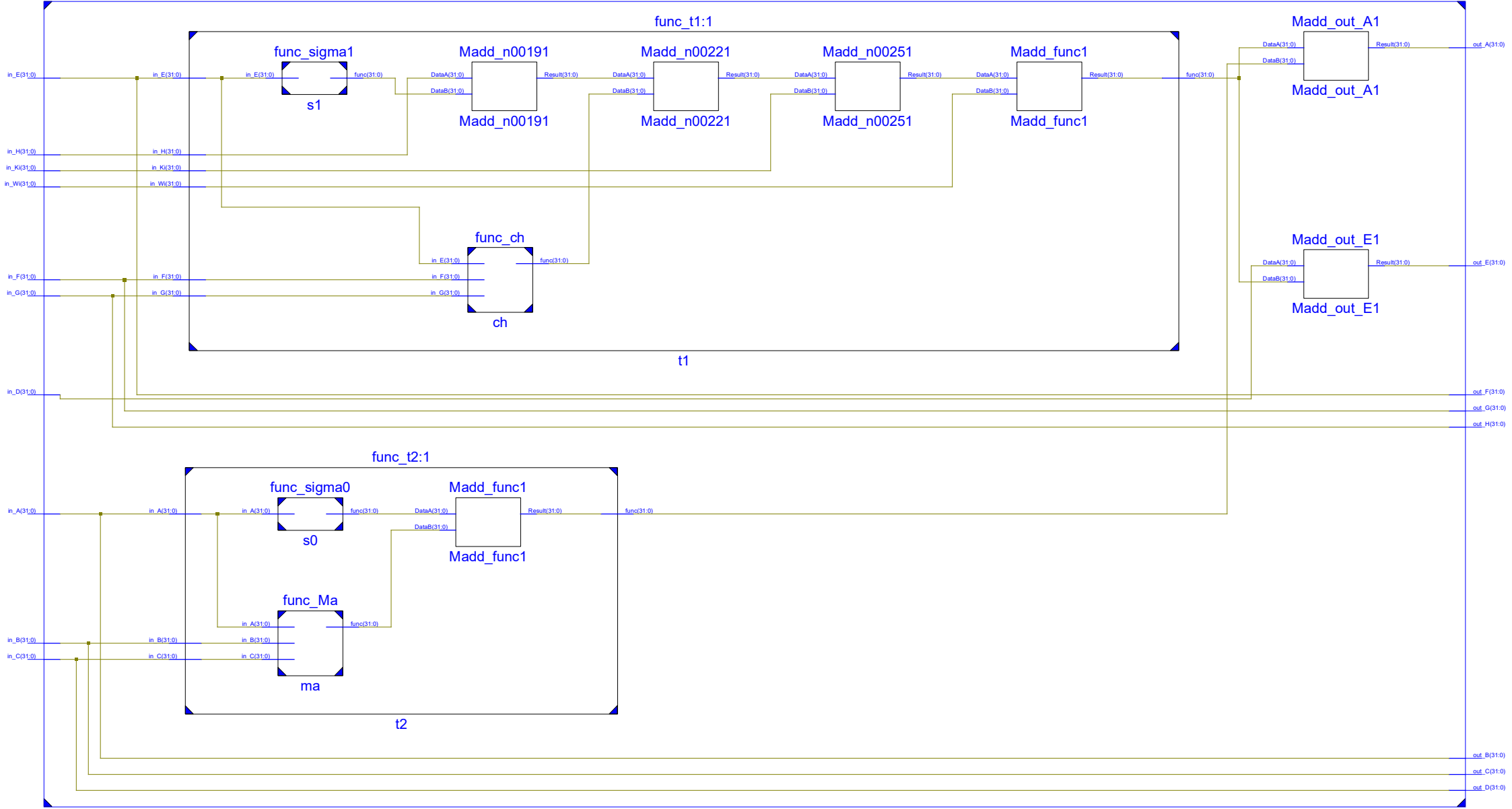


logic\_module:1



logic\_chip