

Project Aurora: VMware Only Installation (Front View)

Controller Positioning:

Controller A is on top
Controller B is on bottom

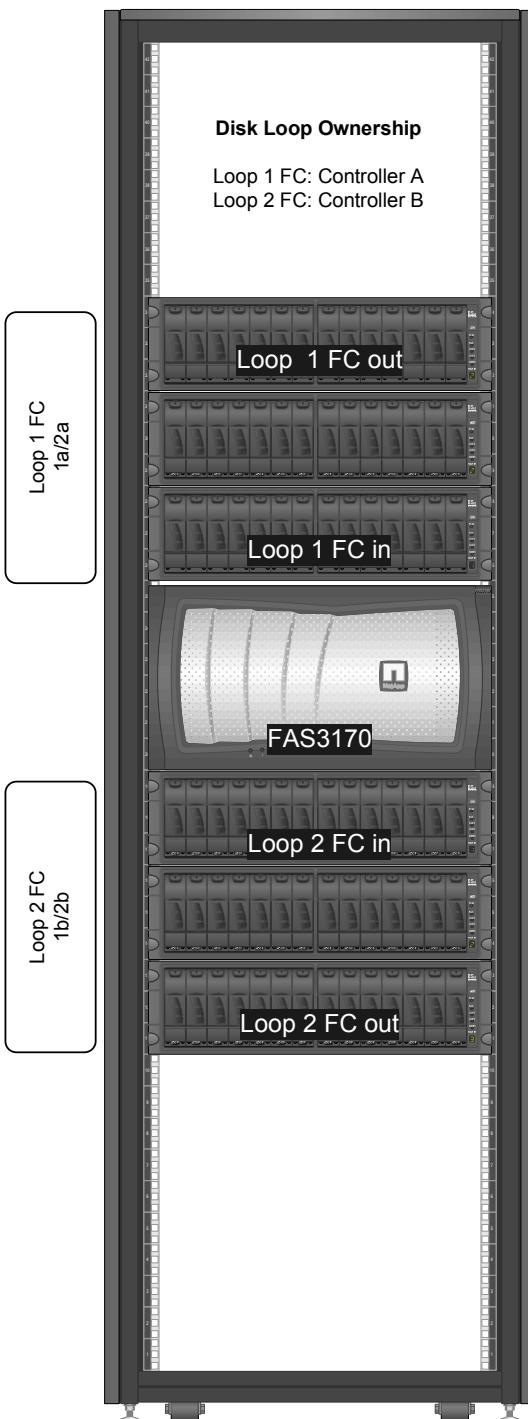
Wiring Diagram:

Controller A port 0a <-> Loop 1 FC Module A in
Controller B port 0a <-> Loop 1 FC Module B in
Controller A port 0c <-> Loop 2 FC Module A out
Controller B port 0c <-> Loop 2 FC Module B out
Controller A port 0b <-> Loop 1 FC Module B out
Controller B port 0b <-> Loop 1 FC Module A out
Controller A port 0d <-> Loop 2 FC Module B in
Controller B port 0d <-> Loop 2 FC Module A in

Card Slots

- :
- 1: Empty
- 2: Empty
- 3: Dual 10GigE Card – x1008
- 4: Dual 10GigE Card – x1008

View is from the back of the storage controllers



Project Aurora: VMware Only Installation (Back View)

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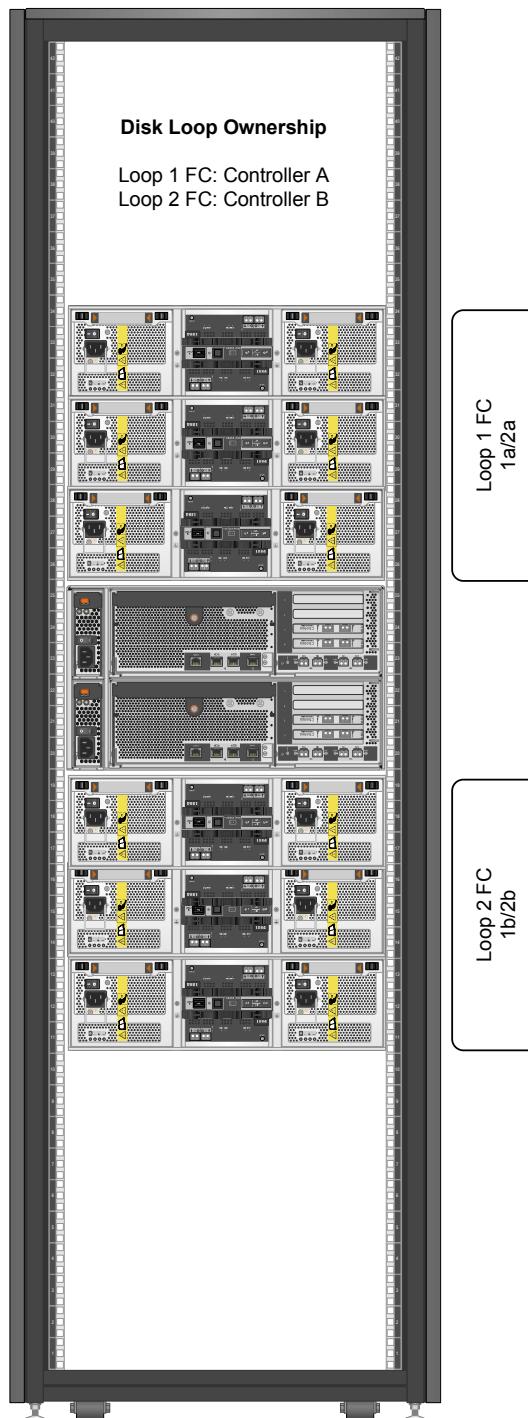
Card Slots

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View is from the back of the storage controllers

Software:

Data OnTap V7.3.x
CIFS, iSCSI, NFS
SnapRestore
DeDuplication (A-SIS/NearStore)
FlexClone
Multistore
SnapMirror
SnapVault Primary
SnapVault Secondary
SnapManager for VI
Operations Manager
Provisioning Manager
Protection Manager (w/ DR Option)



Project Aurora: Standard Base Installation (Front View)

Controller Positioning:

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Controller B is on bottom

Wiring Diagram:

Controller A port 1a <-> Loop 1 FC Module A in
Controller B port 1a <-> Loop 1 FC Module B in
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Controller B port 1b <-> Loop 2 FC Module B out
Controller A port 1c <-> Loop 3 ATA Module A in
Controller B port 1c <-> Loop 3 ATA Module B in
Controller A port 2a <-> Loop 1 FC Module B out
Controller B port 2a <-> Loop 1 FC Module A out
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Controller B port 2b <-> Loop 2 FC Module A in
Controller A port 2c <-> Loop 3 ATA Module B out
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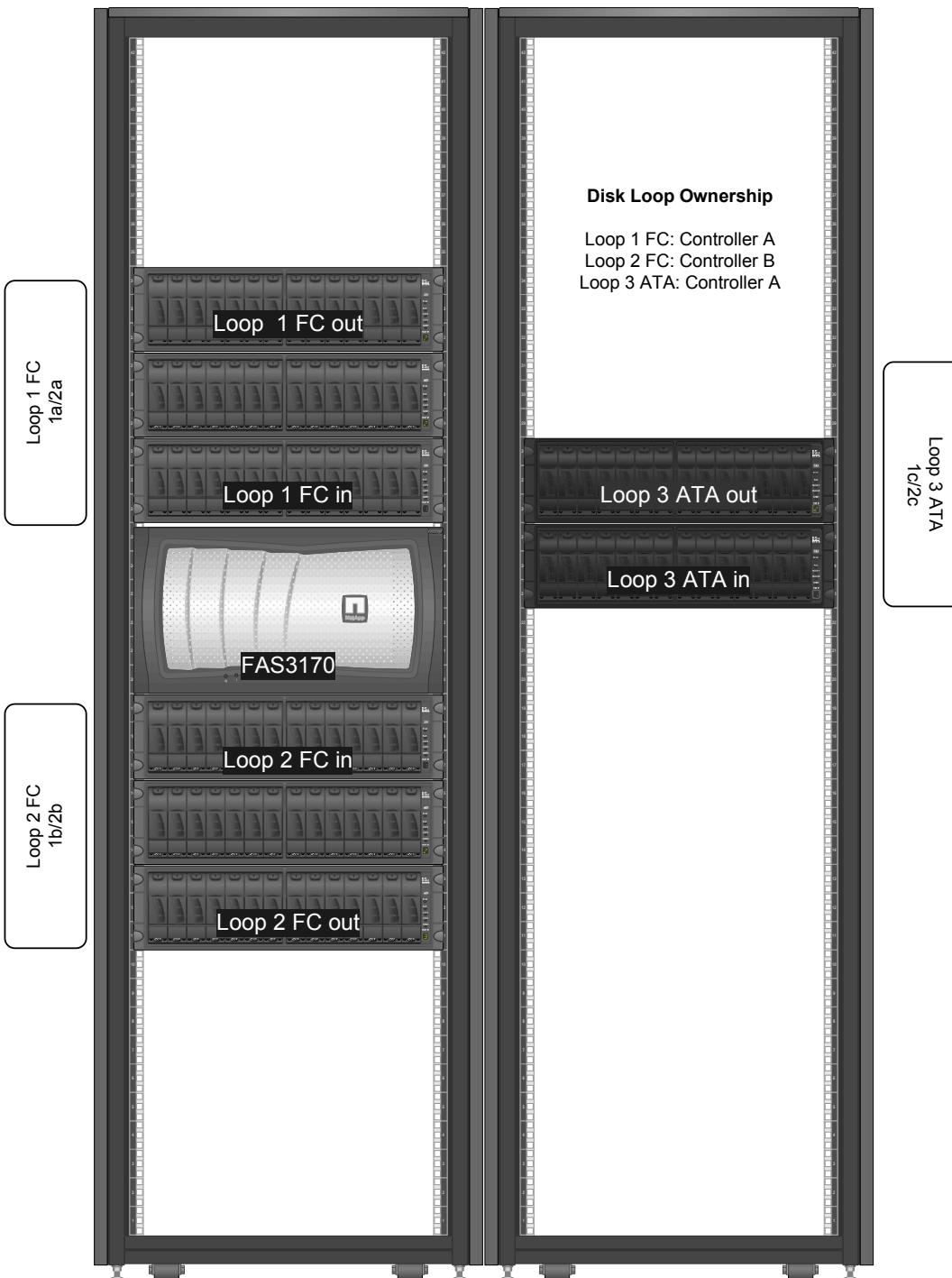
Card Slots

- 1: Quad HBA Fibre Card – x2054
- 2: Quad HBA Fibre Card – x2054
- 3: Dual 10GigE Card – x1008
- 4: Dual 10GigE Card – x1008

View is from the front of the storage controllers

Software:

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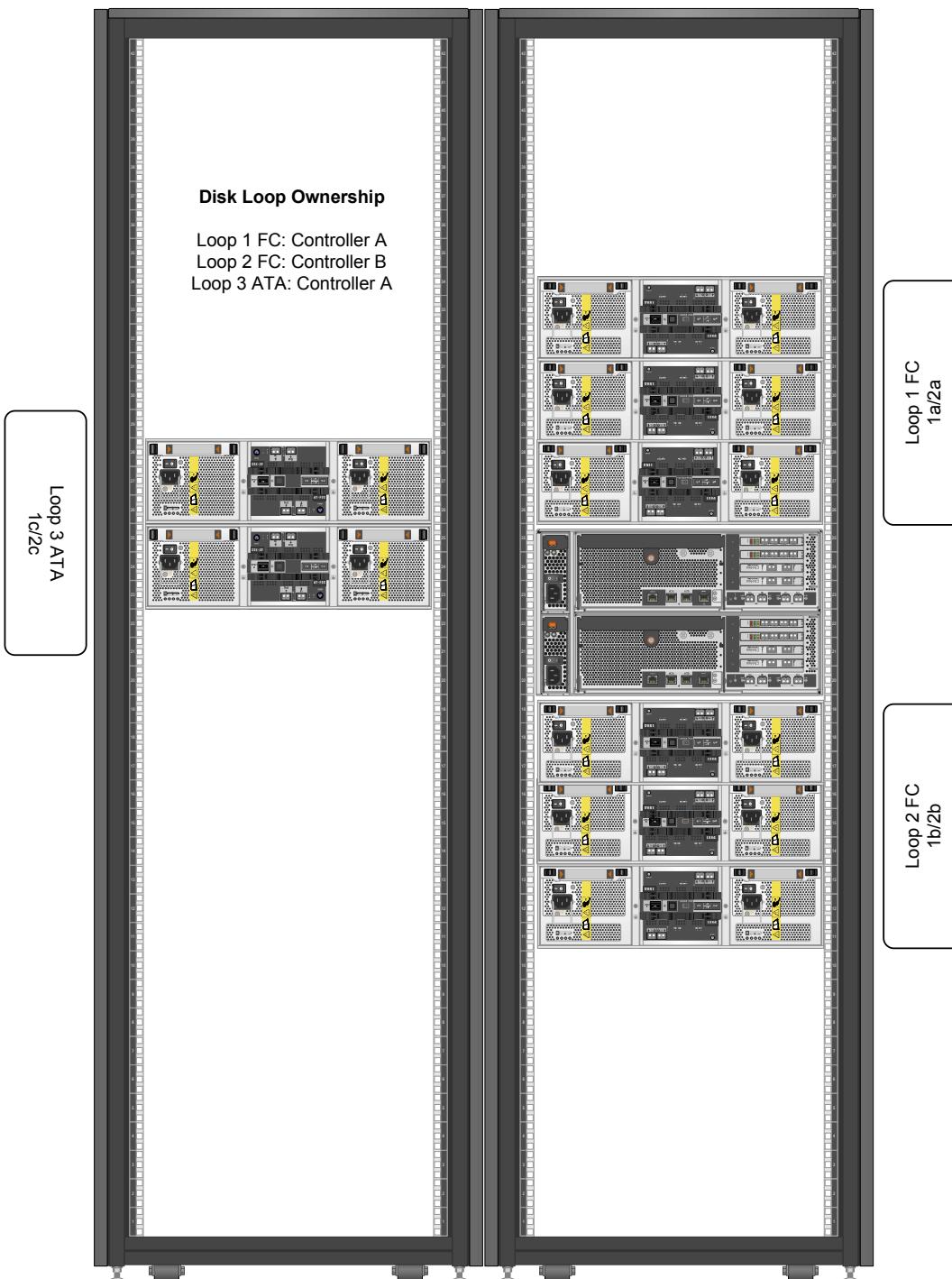
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Project Aurora: VMWare and Standard Base Installation (Front View)

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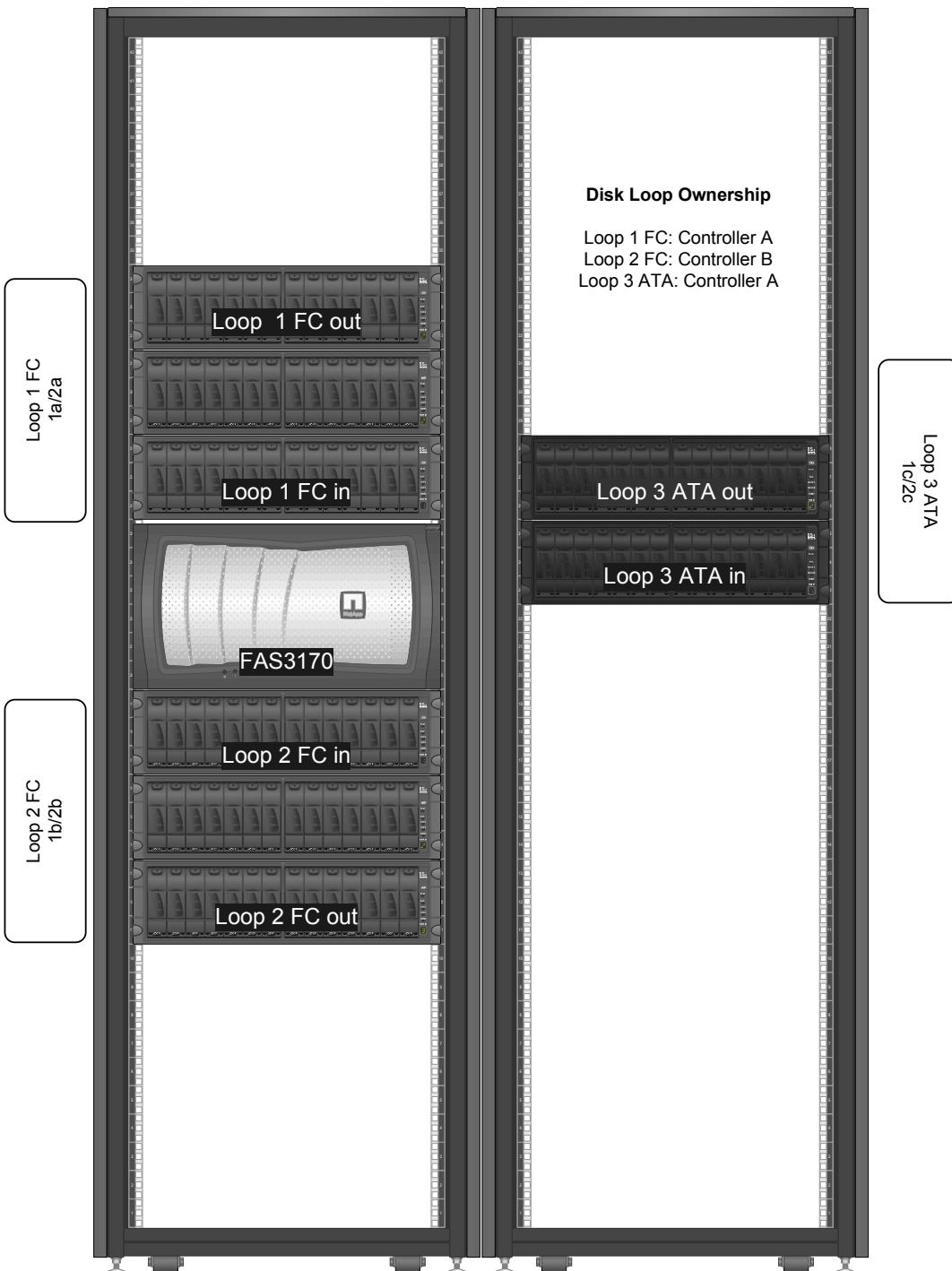
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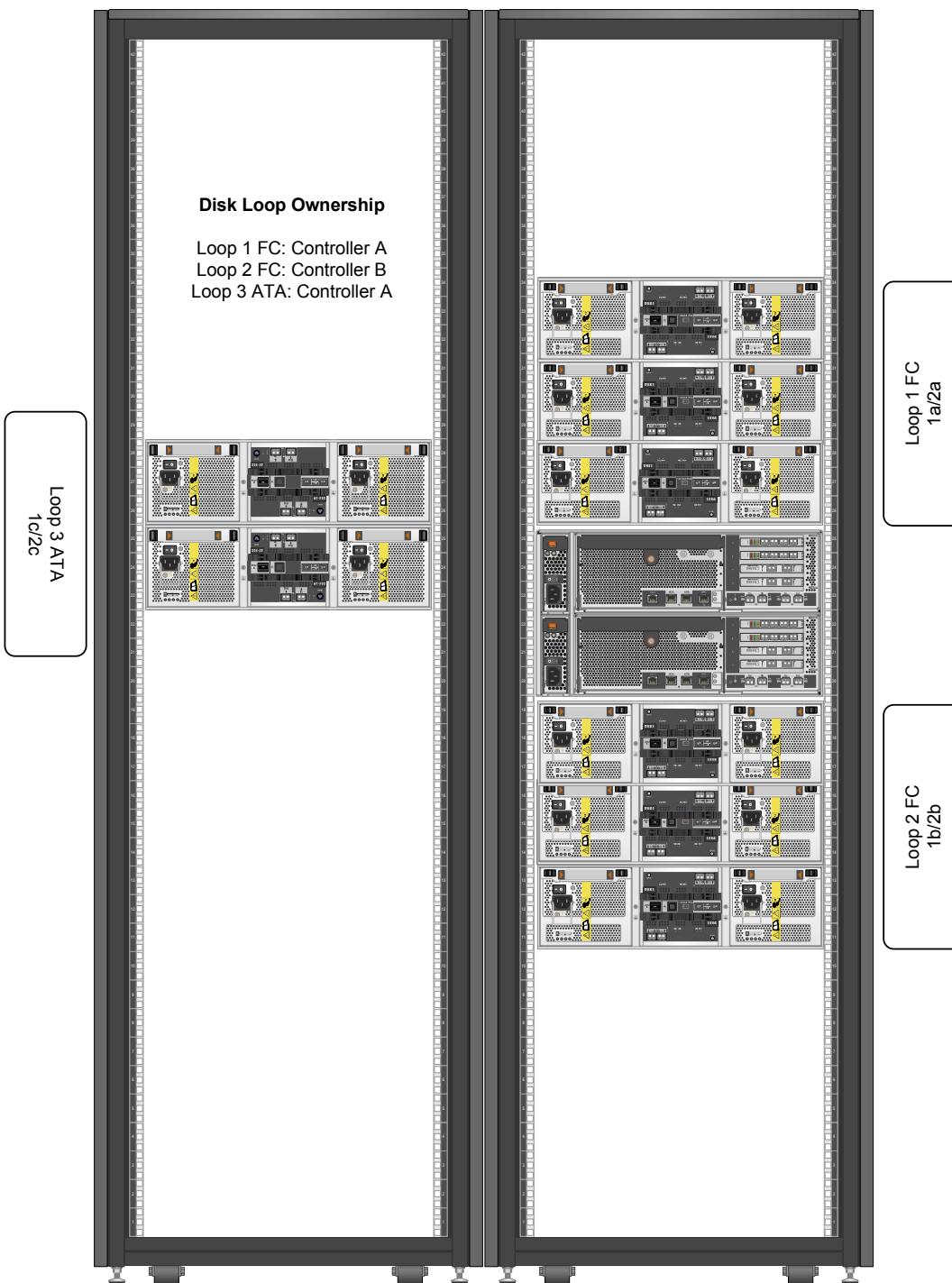
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Project Hybrid Installation (Front View)

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Controller B port 1c <-> Loop 3 ATA Module B in
Controller A port 1d <-> Loop 4 ATA Module A out
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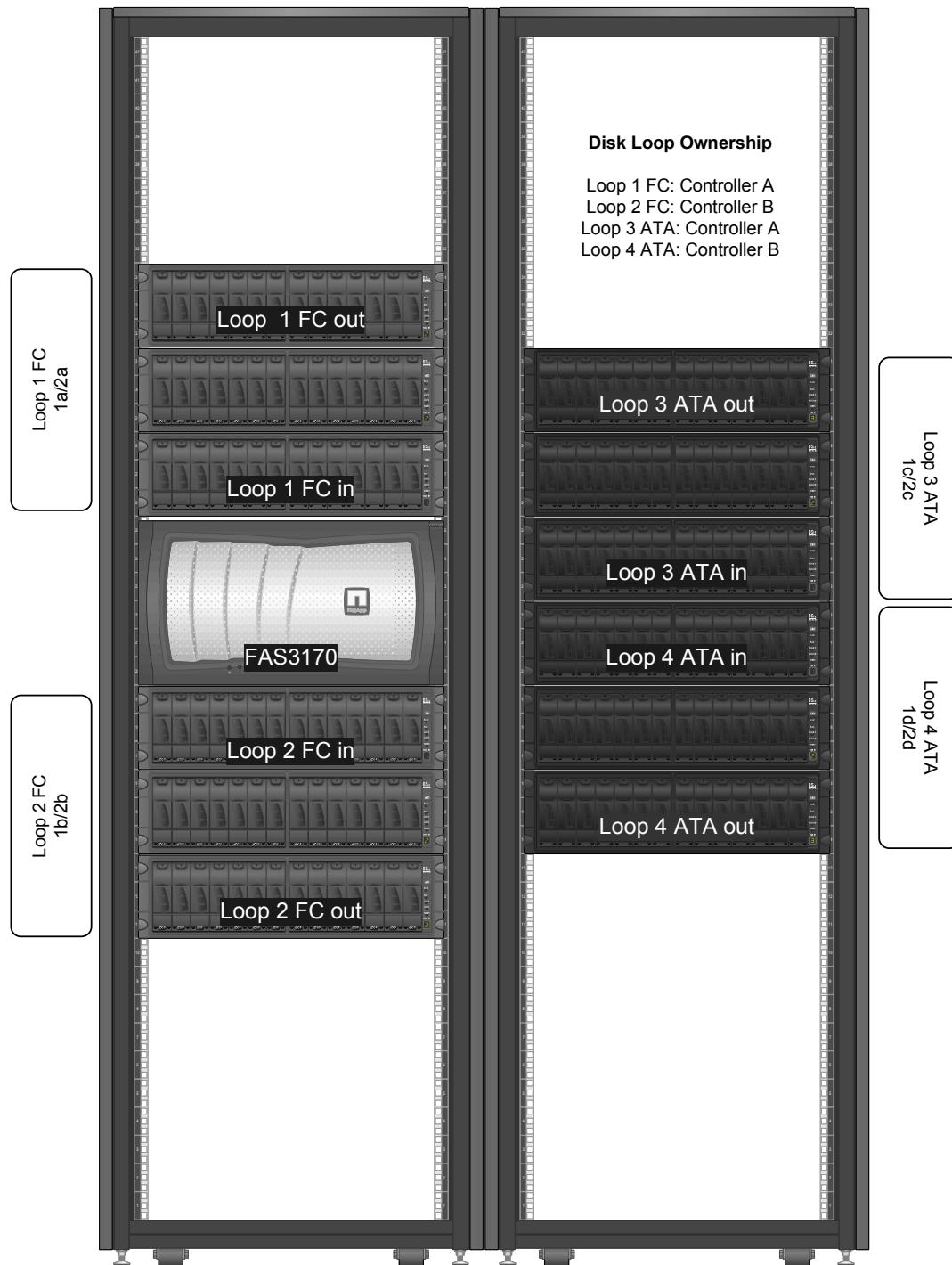
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