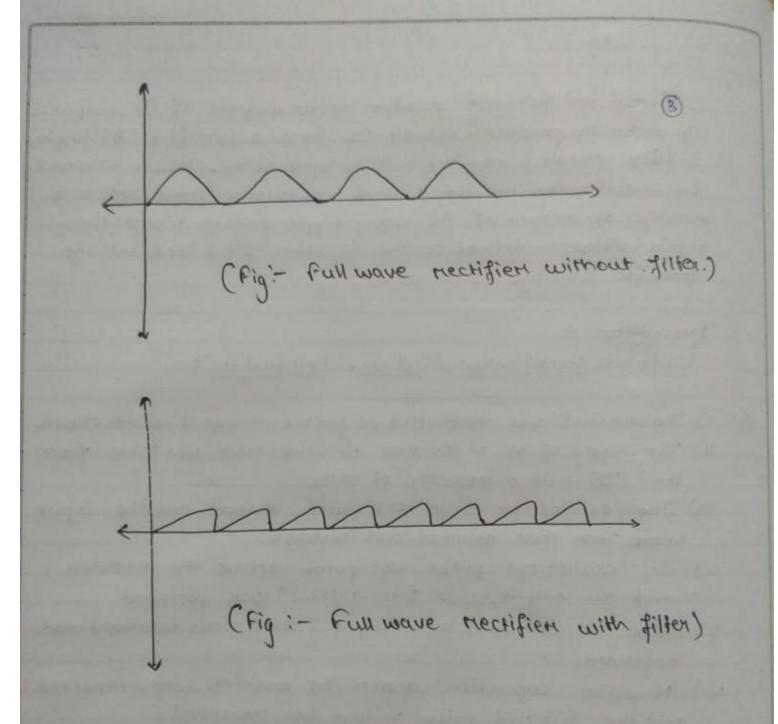
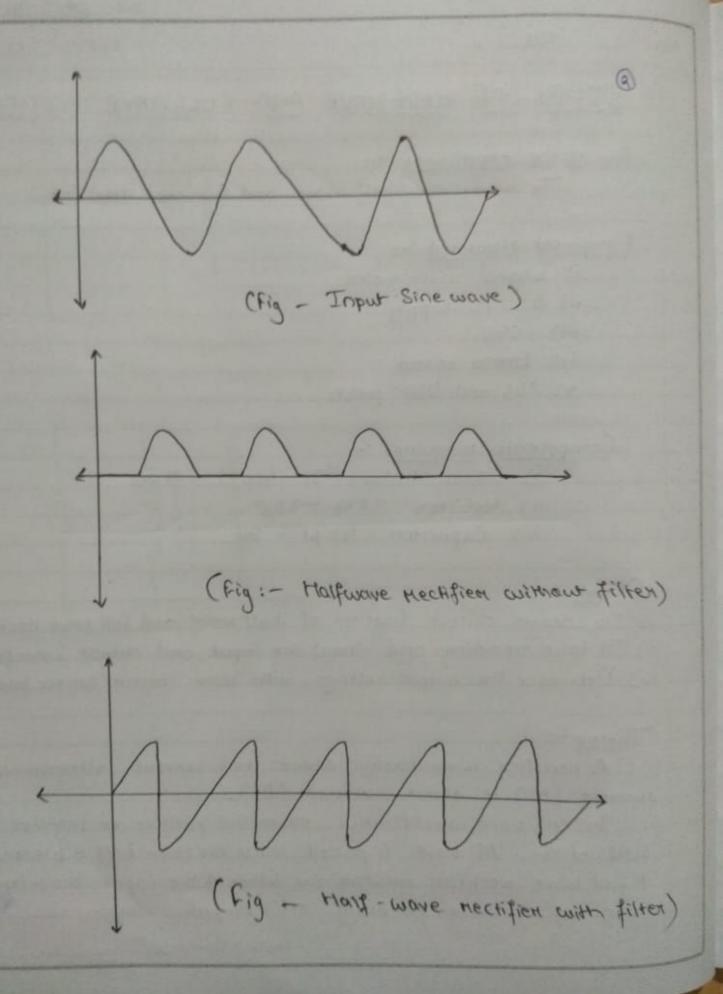
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10	Once again for unfiltered
	Once again for unfiltered readings probes were connected
	along with the values.
1117	For filter, capaciton was connected in panallel to resistor
	and tarper will raken and traced.
14)	The ripple voltage of output was measured.
-	
	RESULT:-
	The output de voltage with filten across load Ris
	6.06 v in halfwave nectifien wheneas theonetically calculate
	the de value is 1.16 v in halfwave nectifien and the output
	de voltage with filter across load R is 5.70 v in full wave
	mechifien and theoretically calculate the dc value is 0.498v
	in full wave mechifien. The form factor is 1.54 v and 1.17 v in half wave and full wave neclifien nespectively. The nipple
	factor is 1.24 v in half wave necrifien and 0.614 v in full wave
	ne clifien nespectively.
	Conclusion:
	The above experiment was conducted successfully. We can
-	corrlude that halfwave and full wave mertifien circuits can be
	built and it can measure and necound their output voltages and
	curves externationly. Half mave and full move mechifiers are
	used to convent Ac into DC voltage. Therefore it is important to
	carry out this expeniment to increase the undenstanding on
	mechicien in industries.

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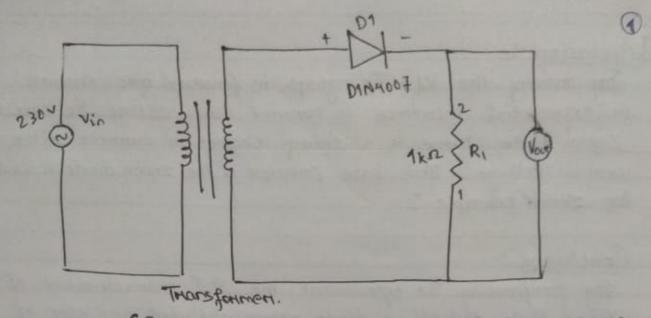
2			
Theonetically calculated the	- 0	1.16 V	7964.0
Ripple eutpur	with filter multimeter in occupate	0.3341	0.1447
Measume output	olc.vol. with filter multimeter in de mode	790.9	5.41
Ripple	Jackon Jackon John Jackon	1.241	0.6140
thom)	(NAGO)	1.598	1.174 v
a	Vm, Vhross Ymean/Vdc (DC coupling mode of CRE)	Vmean = 2.41 V Vmas = 3.41 V Vmas = 7 V	Vines 3.46V Vines = 4.65V Vines = 4.65V
Show the	0 , .	Ver-pk=15.4V	Vries = 5.4V Vpx.px = 19.4V Vriex = 7.8V
1 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	Aechifier Rechifier	Half Wave	Full



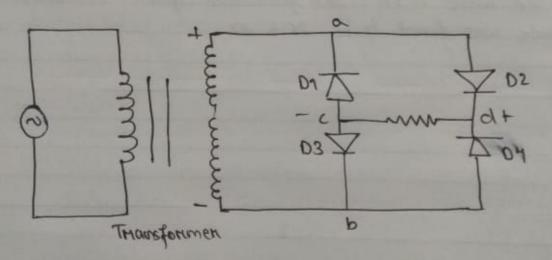


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	Halfwave and full wave mentition deliver a form of De In order to produce steady De form a mechified a fiften circuit is required in its simplest form. This is by shunting the resistor with a capaciton. There is remain an amount of Ac supply ripple voltage where ripple voltage is defined as the deviation of the load with a average of De value. Procedure: Procedure: Halfwave Rectification / Full Wave Rectification:	AC supply, achieved ill still
8	1) The circuit was constructed as pen the circuit diagnose 11) The supply of 5V on lov time sinusoidal wave was take the CDC with a frequency of 50thz 111) The ascillascope probe was puted at input and the wave form that obtained was sketched. 14) The ascillascope probe was puted across the the and the output wave from obtained was sketched 4) The DC level of the output voltage was measured. 11) Now the capacitan across the Hesiston was co and the filtered output voltage was measured. 11) The nipple voltage was measured.	e input
9	For a full wave nectifien: The given cincuit diagnam was implemented on broad along with a SV peak to peak sinusoidal (AC veltage) broad board itself.	oand Trem

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(Fig - Cincuit diagnam of Half wave nectifier)



(Fig: - Cincuit diagram of full wave Mectifien)

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STUDY OF HALFWAVE AND FULLWAVE RECTIFIER

Aim of the experiencest:To study the half wave and full wave nectifien.

Equipment required:

- i) Digital Multimeten
- ii) 5V AC supply
- iii) CRO
- iv) Bread board
- V) CDS and DSO probes

Components required:

- i) Silicon diodes (Si-4007) 4 NOS.
- ii) Registon 1ks2-1NO
- iii) Capaciton 100 pt 100.

Objects :

- i) To design circuit diagram of half wave and full wave neclifier.
 ii) To take neading and chaw the input and output waveforms
- his Defending the output voltage with biten cincuit across load (R)

Theony -

A nectifien is an electric device that convents alternating current (AC) to direct current (DC).

In half wave nectification, either the positive on negative half of the AC wave is passed, while the other half is blocked. A full wave nectifien convents the whole of the input waveform to one of constant polarity at its output.

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