

Aim: Study of RC coupled common emitter (CE) transistor amplifier & determination of its gain, band width & signal handling capacity.

Apparatus Used: Multimeter, bread board, 12V DC power supply, function generator, CRO Resistor ( $R_1 = 10k\Omega$ ,  $R_2 = 10k\Omega$ ,  $R_C = 1.2k\Omega$ ,  $R_E = 1k\Omega$ ), Capacitor ( $C_1 = C_{in} = 5\mu F$ ,  $C_C = 20\mu F$ ), Transistor  $n-p-n$  BC 546 (BJT).

Theory: An amplifier is a device that is used to amplify input voltage or current. The RC coupled CE arrangement is a popular scheme of cascading low cost amplifiers to achieve the required level of amplification. The inter-stage coupling is done by using a RC network, hence the name. The variation of gain with frequency of an amplifier is called the frequency response characteristic of the amplifier.

The bandwidth of the RC amplifier is the difference between upper cut off frequency & lower cut off frequency. It represents the range of frequencies that the amplifier is most effective in amplifying. The cut off frequency points are determined from the frequency response graph, where the gain (dB) is 3 dB lower than the max gain.

For a transistor to act as an amplifier, it must be properly biased i.e. its emitter base junction must be forward biased & collector base junction must be reverse biased. The transistor is operating in the active region, & verified by measuring the DC bias condition.

Teacher's Signature

### Methodology :-

The components are connected as shown in the figure.

- ① A 50 mV peak to peak signal from the function generator is connected to the input of the amplifier & voltage output is measured in the CRO. Note down the  $V_{in}$ - $V_{out}$  readings for different frequencies keeping the input signal constant.
- ② The voltage gain in dB for each frequency & plot the frequency response curve in a semi-log graph paper. From that find the bandwidth.
- ③ Signal Handling Capacity :- In the mid frequency, select any frequency, go on increasing the input, observing the output in the oscilloscope. At some value of input, the output waveform starts showing distortion. That input value in volts or mV is called the signal handling capacity.

Experimental Results :-

$V_{CC} = 8.28V$ ,  $V_{BE} = 0.67V$ ,  $I_C = 1.40mA$ ,  $V_{AC} = 2V$

$V_{in}$	Frequency (Hz)	$V_{out}$ (in volt)	Gain ( $V_{out}/V_{in}$ )	Gain in dB $= 20 \log (V_{out}/V_{in})$
50 mV $V_{pp}$	50 Hz	0.501	10.08	20.08
	100 Hz	0.948	18.96	29.52
	500 Hz	2.16	43.2	32.80
	1 kHz	2.64	52.8	34.45
	5 kHz	2.82	56.4	35.02
	10 kHz	3.0	60.0	35.56
	20 kHz	3.2	64.0	36.12
	50 kHz	3.3	66.0	36.32
	100 kHz	3.3	66.0	36.32
	200 kHz	3.2	64.0	36.12
	500 kHz	2.96	59.2	35.44
	700 kHz	2.56	51.2	34.13
	1 MHz	2.32	46.4	33.33
	2 MHz	1.77	35.4	29.99
	5 MHz	0.720	14.4	23.14

Teacher's Signature

$f \longrightarrow$

$\uparrow$   
 $dB$

