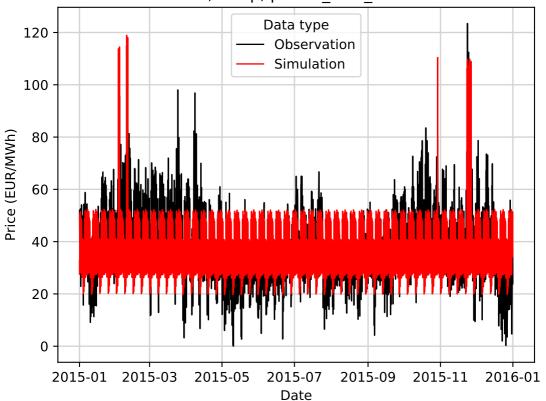
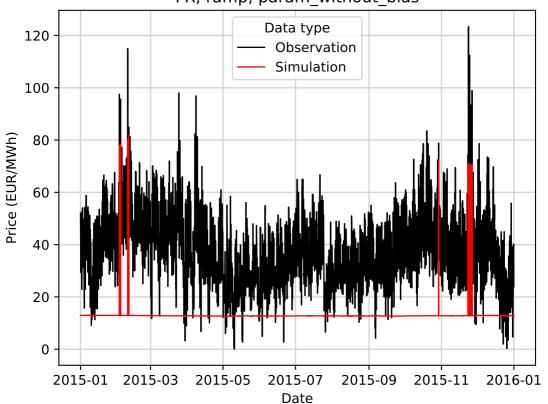
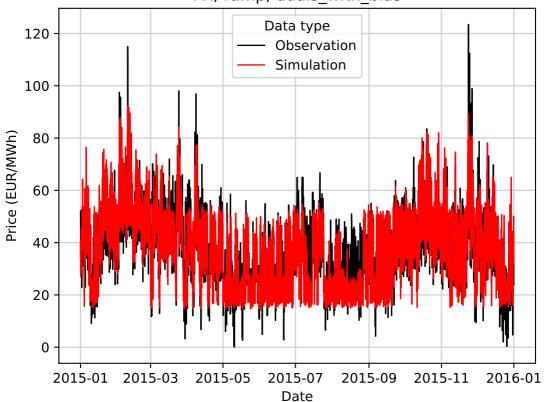
train:2015, test:2015, iter: 0, rmse: 9.4, delta_sd: 3.8 FR, ramp, param with bias



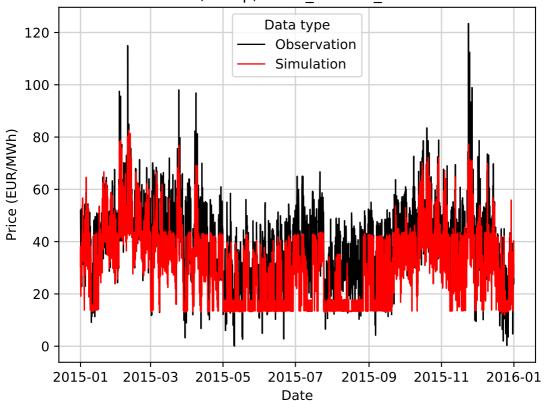
train:2015, test:2015, iter: 0, rmse: 28.5, delta_sd: 10.1 FR, ramp, param_without_bias



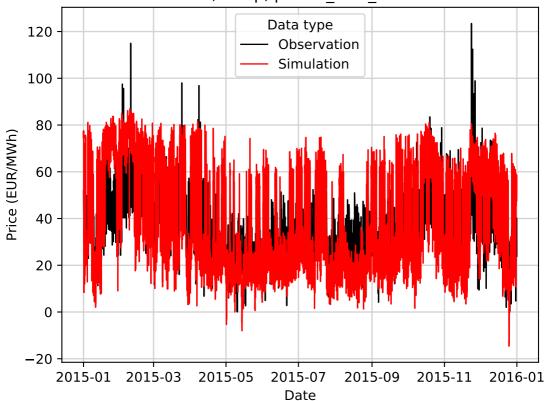
train:2015, test:2015, iter: 0, rmse: 6.5, delta_sd: -1.1 FR, ramp, duals with bias



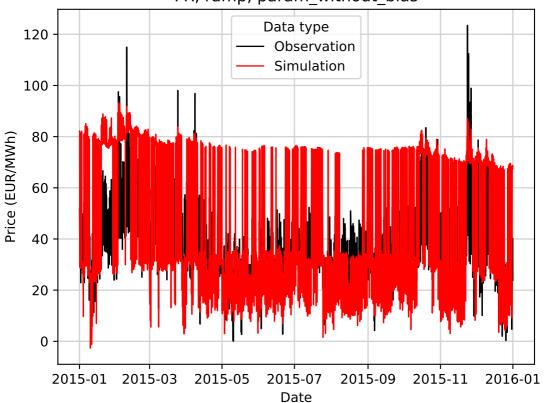
train:2015, test:2015, iter: 0, rmse: 9.3, delta_sd: -0.5 FR, ramp, duals without bias



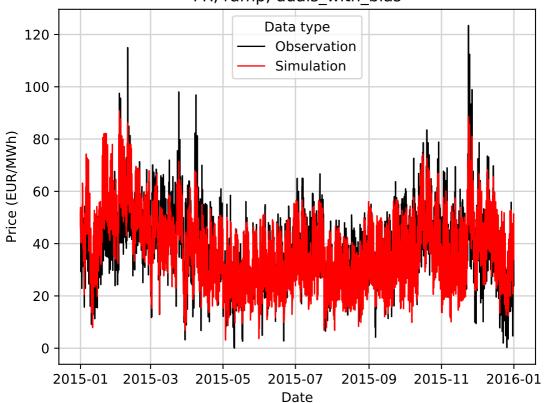
train:2015, test:2015, iter: 1, rmse: 16.6, delta_sd: -9.7 FR, ramp, param with bias



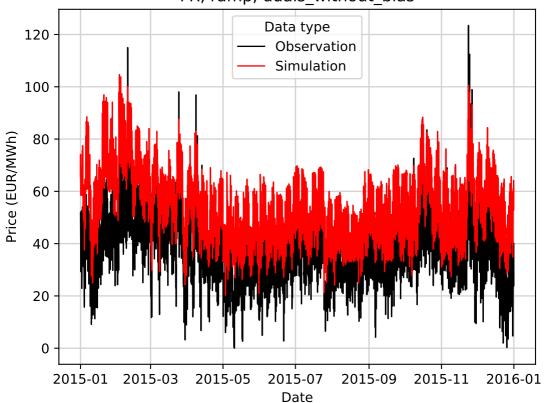
train:2015, test:2015, iter: 1, rmse: 20.1, delta_sd: -13.1 FR, ramp, param without bias



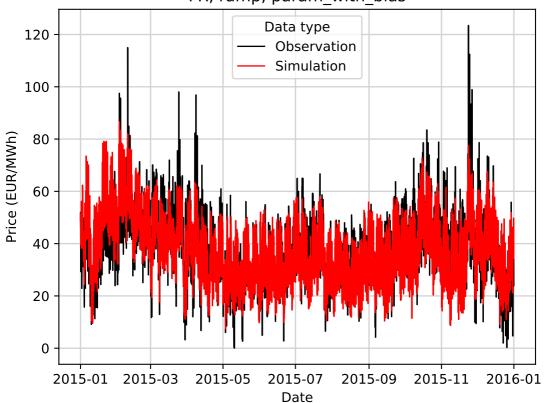
train:2015, test:2015, iter: 1, rmse: 6.8, delta_sd: -0.4 FR, ramp, duals with bias



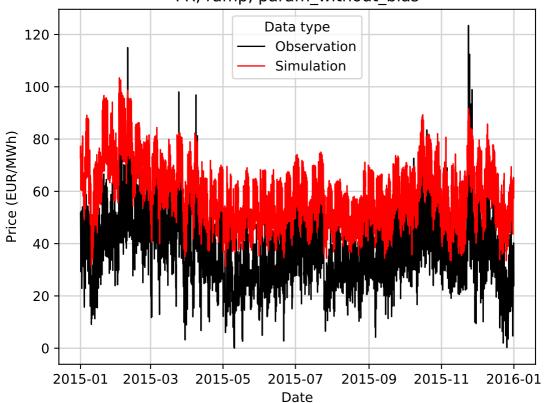
train:2015, test:2015, iter: 1, rmse: 18.5, delta_sd: -0.3 FR, ramp, duals without_bias



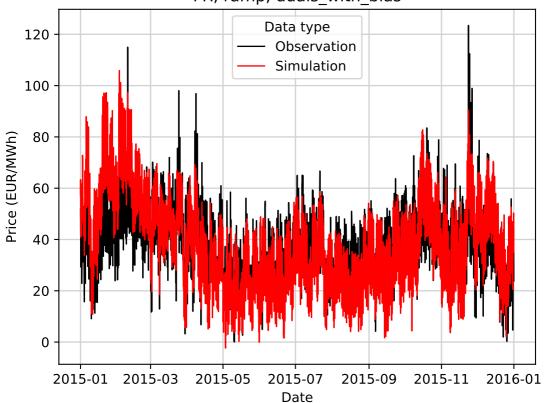
train:2015, test:2015, iter: 2, rmse: 6.5, delta_sd: 0.2 FR, ramp, param_with_bias



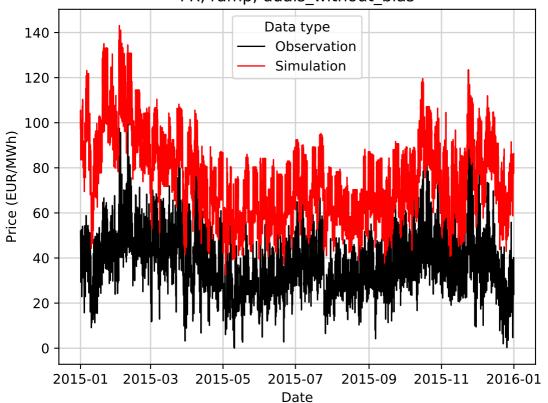
train:2015, test:2015, iter: 2, rmse: 22.8, delta_sd: 0.9 FR, ramp, param without bias



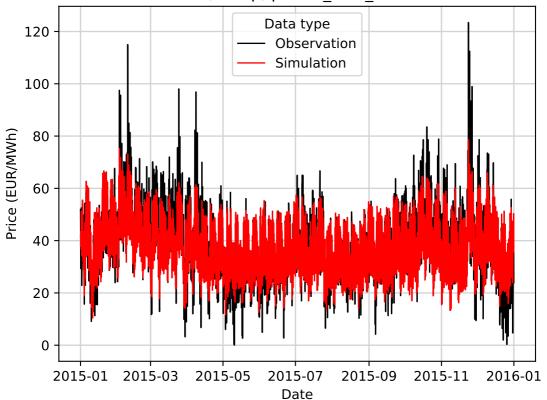
train:2015, test:2015, iter: 2, rmse: 10.3, delta_sd: -4.6 FR, ramp, duals with bias



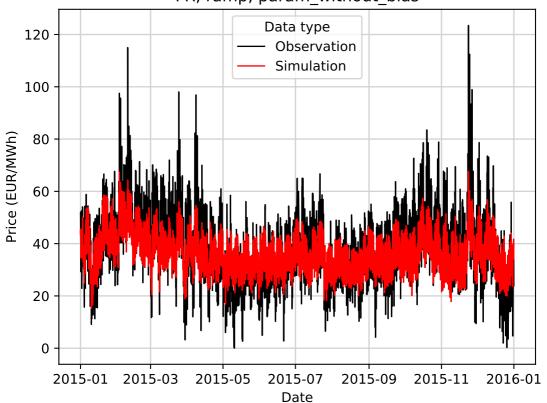
train:2015, test:2015, iter: 2, rmse: 40.3, delta_sd: -5.8 FR, ramp, duals without bias



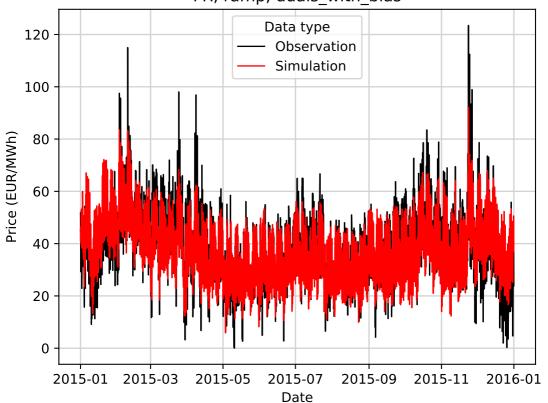
train:2015, test:2015, iter: 3, rmse: 6.3, delta_sd: 2.7 FR, ramp, param_with_bias



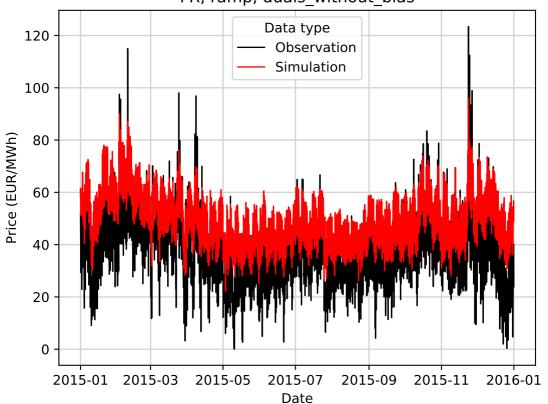
train:2015, test:2015, iter: 3, rmse: 7.6, delta_sd: 5.3 FR, ramp, param_without_bias



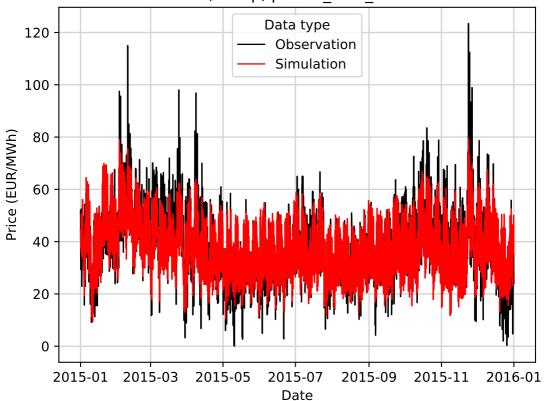
train:2015, test:2015, iter: 3, rmse: 6.0, delta_sd: 1.5 FR, ramp, duals with bias



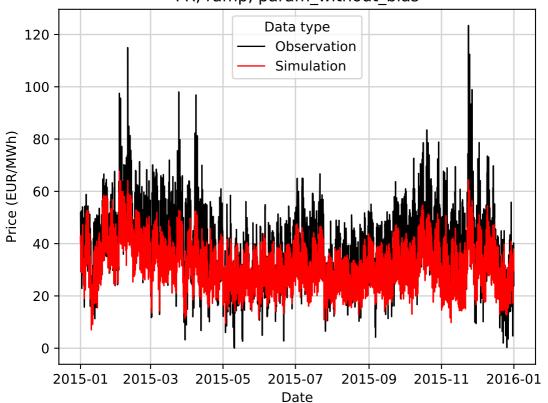
train:2015, test:2015, iter: 3, rmse: 13.4, delta_sd: 3.3 FR, ramp, duals without bias



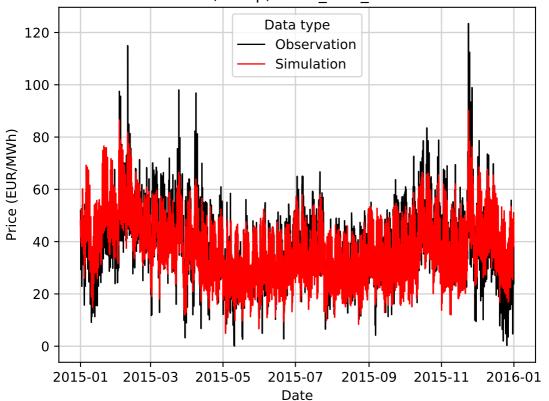
train:2015, test:2015, iter: 4, rmse: 6.2, delta_sd: 2.1 FR, ramp, param_with_bias



train:2015, test:2015, iter: 4, rmse: 9.1, delta_sd: 3.9 FR, ramp, param_without_bias



train:2015, test:2015, iter: 4, rmse: 6.2, delta_sd: 0.9 FR, ramp, duals with bias



train:2015, test:2015, iter: 4, rmse: 15.1, delta_sd: 2.3 FR, ramp, duals without bias

