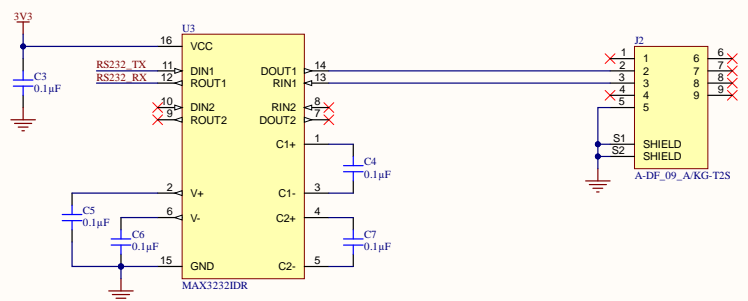
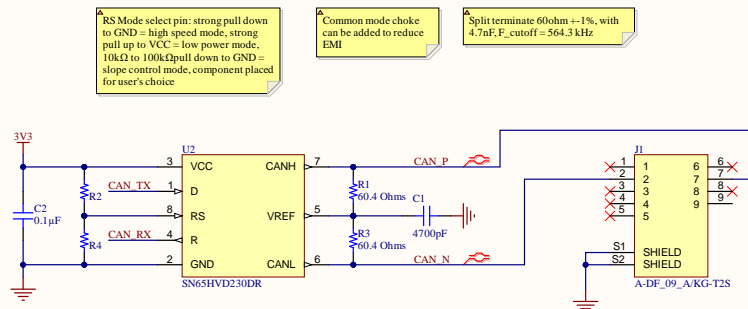
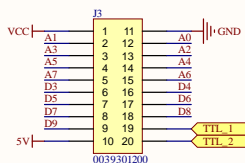


From NUCLEO-32 datasheet, refer to datasheet for more information:
 Limitations on A4 and A5, D4 and D5, related to I2C configuration, are explained in Section 6.10: Solder bridges according to SB16/SB18 setting.
 D7/D8 shared with OSC32_IN/OSC32_OUT.
 No PWM on D11.



Project	DRTC_V2.PrjPcb		
Title	MCU.SchDoc		
Size	Part Number	N/A	Rev
B			1.0
Scale	1:1	Engineer	Ryan Tran
Sheet	* d 1		