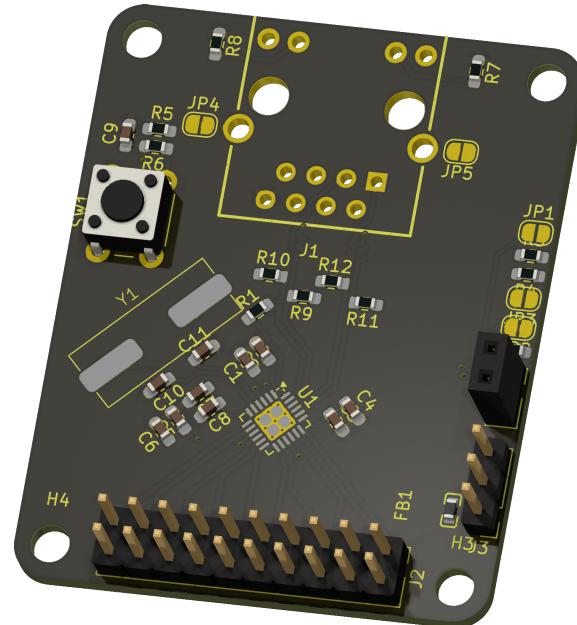


# LAN8742A-breakout-board Assembly Document

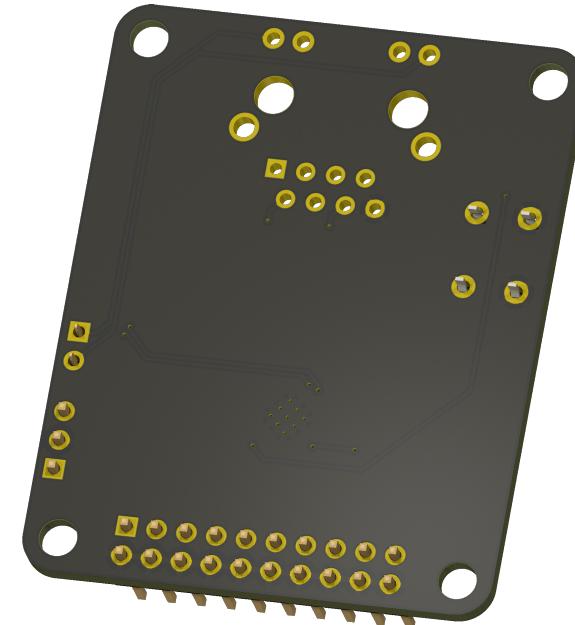
A

**Top View**



B

**Bottom View**



C

		Comments:	Company: PatapimLabs	Variant: PRELIMINARY	Git Hash: f5059ed
		Board Name: <b>LAN8742A-breakout-board</b>		Project Name: <b>LAN8742A Breakout</b>	
		Sheet Title: Top/Bottom View	File Name: LAN8742A-breakout.kicad_pcb	Designer: Tropikej	Date: 2024-04-13 Revision: + (Unreleased)
		Sheet Path:		Reviewer:	Size: <b>A4</b> Sheet: <b>1 of 3</b>

# LAN8742A-breakout-board Assembly Document

A

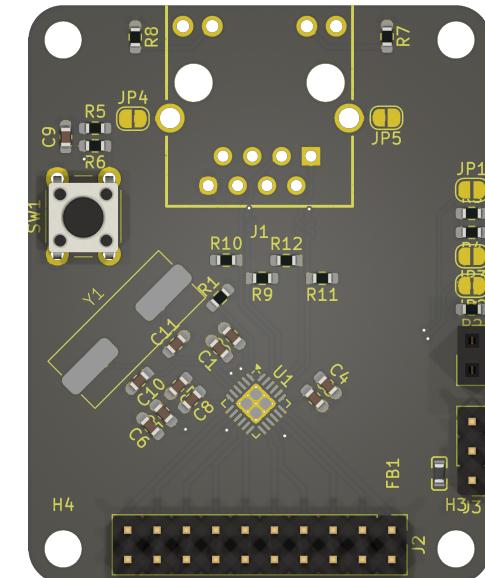
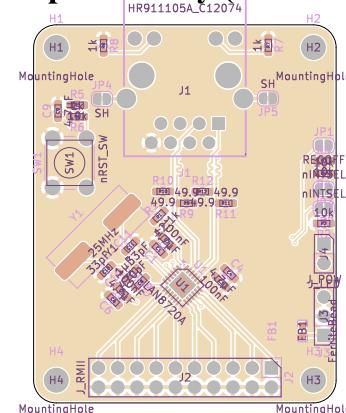
## Components

Type	Front Side	Back Side	Total
THT	5	0	5
SMD	26	0	26
Total	31	0	31

### ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

- 1) DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
- 2) DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
- 3) IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
- 4) CONFORMAL COATING IS NOT REQUIRED.
- 5) DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.

**Top Assembly (Scale 1:1)**



B

A

C

B

D

C

D

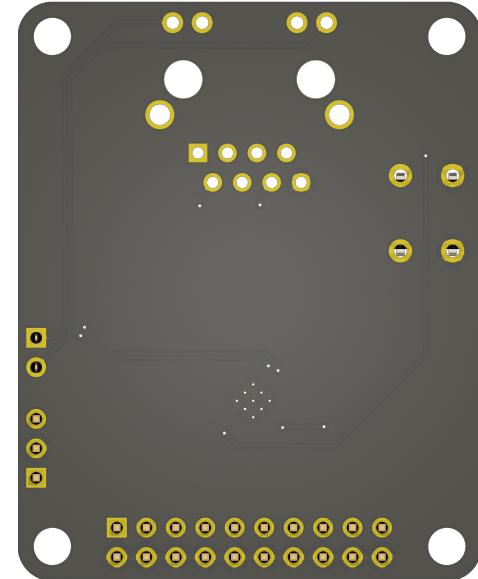
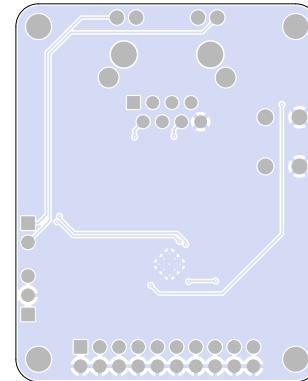
	Comments:	Company: PatapimLabs	Variant: PRELIMINARY	Git Hash: f5059ed
	Board Name: <b>LAN8742A-breakout-board</b>			Project Name: <b>LAN8742A Breakout</b>
	Sheet Title: Top Assembly (Scale 1:1)	File Name: LAN8742A-breakout.kicad_pcb	Designer: Tropikej	Date: 2024-04-13
	Revision: + (Unreleased)		Reviewer:	Size: <b>A4</b>
	Sheet Path: 			Sheet: <b>2</b> of <b>3</b>

# LAN8742A-breakout-board Assembly Document

A

A

**Bottom Assembly (Scale 1:1)**



B

B

C

C

	Comments:	Company: PatapimLabs	Variant: PRELIMINARY	Git Hash: f5059ed
	Board Name: <b>LAN8742A-breakout-board</b>	Project Name: <b>LAN8742A Breakout</b>		
	Sheet Title: Bottom Assembly (Scale 1:1)	File Name: LAN8742A-breakout.kicad_pcb	Designer: Tropikej	Date: 2024-04-13 Revision: + (Unreleased)
	Sheet Path:		Reviewer:	Size: <b>A4</b> Sheet: <b>3</b> of 3