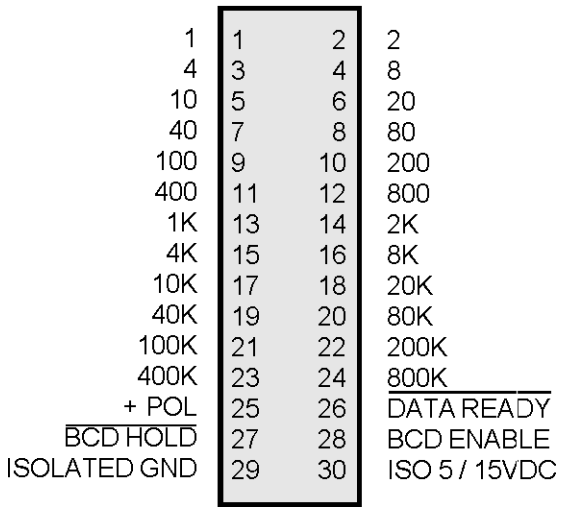
18. PARALLEL BCD OUTPUT OPTION

An optional parallel BCD output board may be connected to the meter main board at plug position P13 (middle position). Once installed, the board is recognized by the meter, which will bring up the appropriate menu items. The BCD board provides isolated, buffered, stored, 3-state parallel outputs that are jumper selectable for either 0-5V logic (LSTTL, CMOS compatible) or 0-15V dc. BCD outputs are positive true. Polarity bit is positive true for +sign.

Logic Level	Jumper
0 to 5 Vdc	b
0 to 15 Vdc	a

BCD OUTPUT



BCD CONTROL SIGNALS

Enable Logic **0** - All outputs go to high impedance state.
Logic **1** - BCD information is available at outputs.

/ BCD Hold Logic **0** - BCD from last update prior to BCD Hold going low is stored.
Logic **1** - BCD information is updated at selected rate.

/ Data Ready Logic **0** - BCD outputs are valid.
Logic **1** - BCD outputs are not valid.

KEYSTROKES FOR SETUP

If the *MENU* ↵ key does not work, see Section 9 "Enabling & Locking Out Menu Items."

MENU ↵ *Press Menu Select Key*	PEAK ▶ *Press Digit Select Key*	RESET ▲ *Press Value Select Key*
SEr 1 Press ↵ until *SEr 1* is displayed.	**0 0** Output filtering	**0** Send unfiltered signal **1** Send filtered signal
	0 0 Output update rate	60 Hz / 50 Hz **0** Line frequency **1** 0.28 sec / 0.34 sec **2** 0.57 sec / 0.68 sec **3** 1.1 sec / 1.4 sec **4** 2.3 sec / 2.7 sec **5** 4.5 sec / 5.4 sec **6** 9.1 sec / 10.9 sec **7** 18.1 sec / 21.8 sec **8** 36.6 sec / 43.5 sec **9** 72.5 sec / 97 sec