



Trusted Firmware Community Project



TrustedFirmware
.org

Trusted Firmware

Open Governance Community Project

Evolution of former Open Source
Arm Trusted Firmware project

Reference implementation of Secure
world software for Armv7 & Armv8
architectures (both A/M-Profiles)

Membership open to all

Governance overseen by a board of
member representatives

Technical direction overseen by TSC

~~Arm Trusted Firmware~~

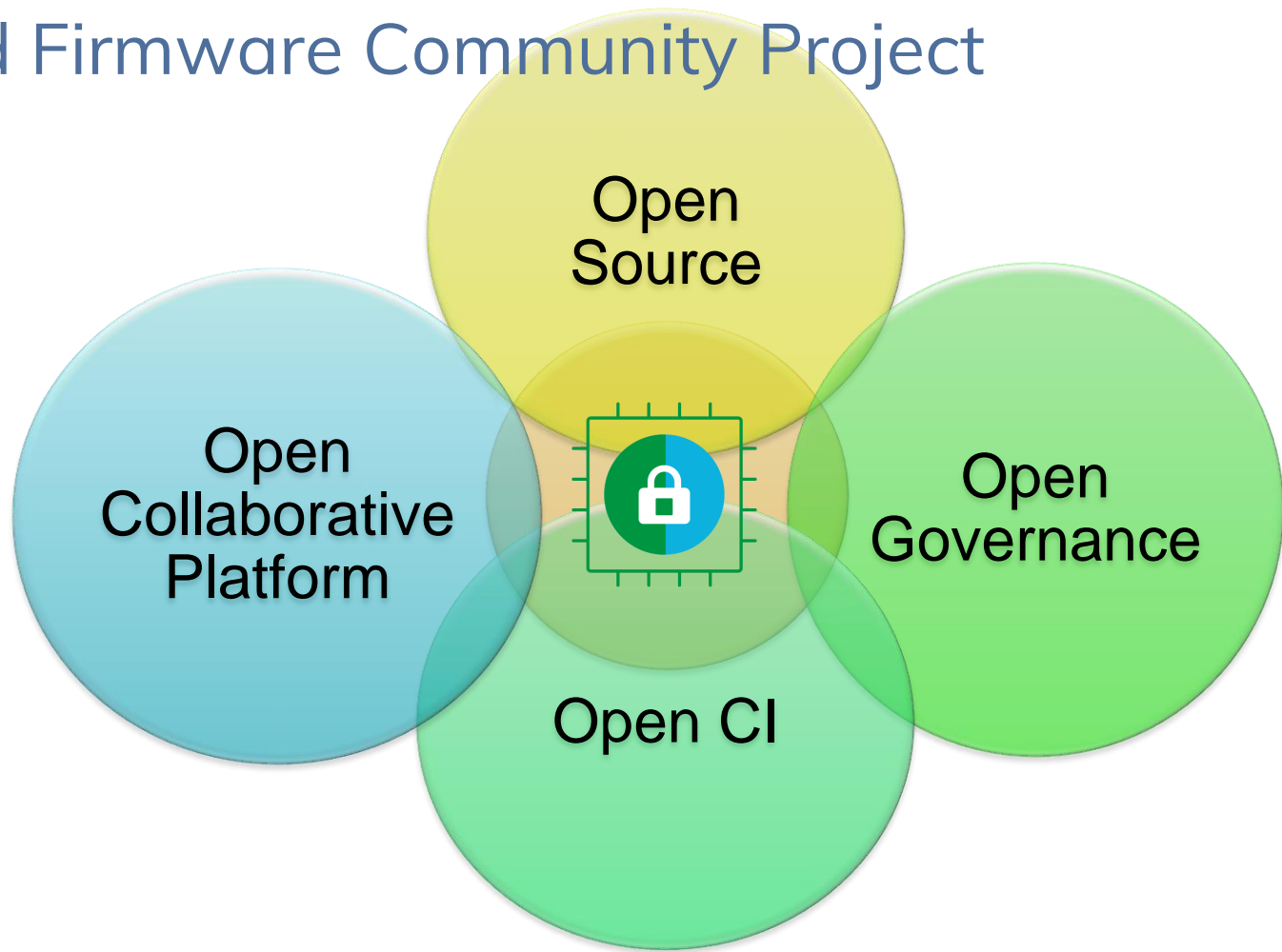


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Trusted Firmware History



A long time ago in a Connect far, far away...



Oct 2013

Arm Trusted Firmware

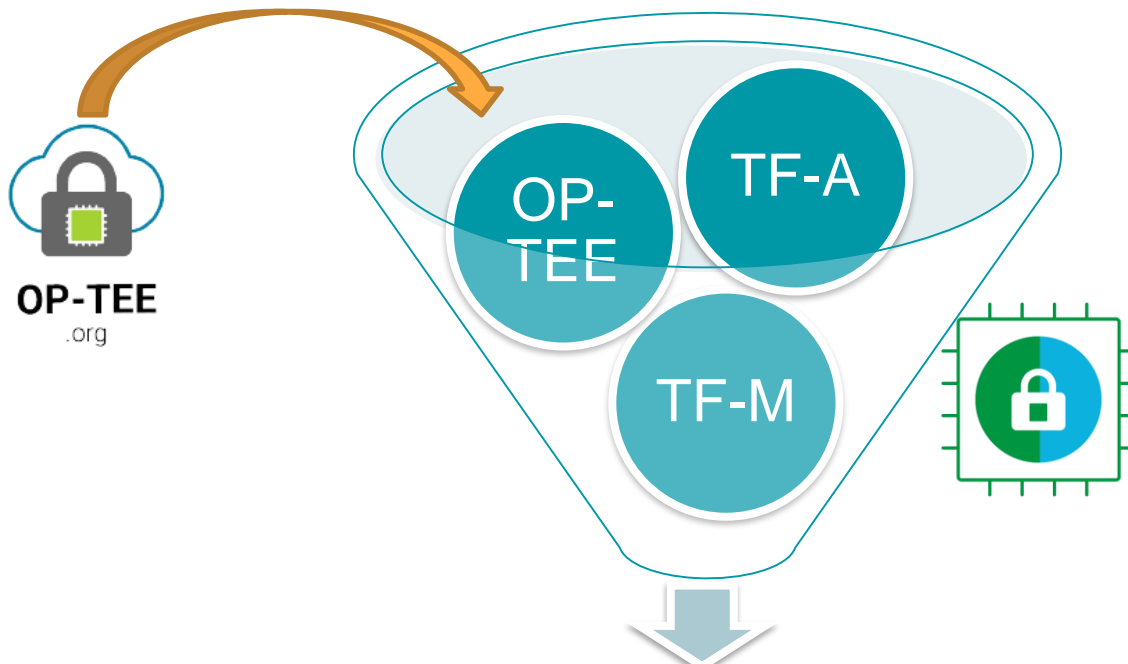
Mar 2018

Trusted Firmware-M
Trusted Firmware-A

Sept 2019
OP-TEE joins
TrustedFirmware
TrustedFirmware.org

Oct 2018

Welcome OP-TEE into TrustedFirmware.org!



**Comprehensive reference implementation of Secure world software and
Secure services for all Arm-based systems**

Easily portable to many other trusted software implementations

<https://www.trustedfirmware.org/news/linaro-transferring-op-tee-to-become-part-of-trusted-firmware/>

Current members

arm

Data iO

 **FUTUREWEI**
Technologies

 **CYPRESS**
EMBEDDED IN TOMORROW™



 Linaro

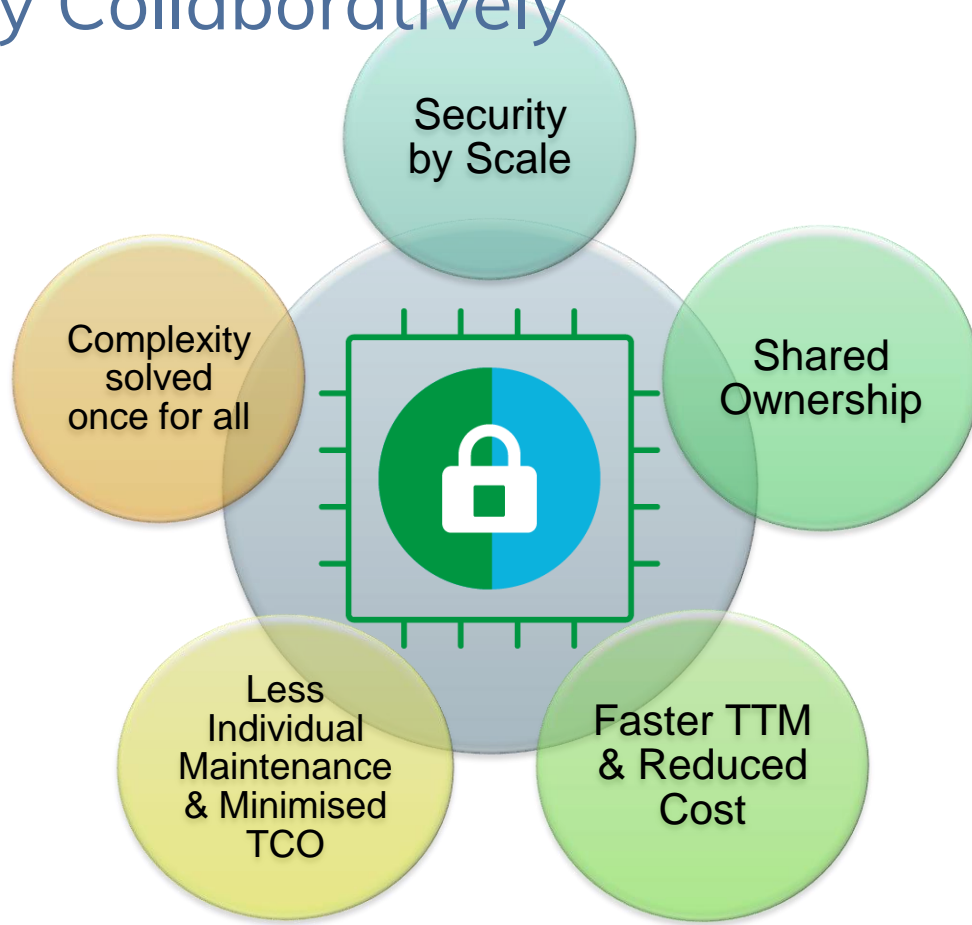
Google

 **TEXAS
INSTRUMENTS**


life.augmented

 **TrustedFirmware**
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Build Security Collaboratively



All market segments

Devices

IoT/Mobile/Auto/Laptop



Embedded
Edge

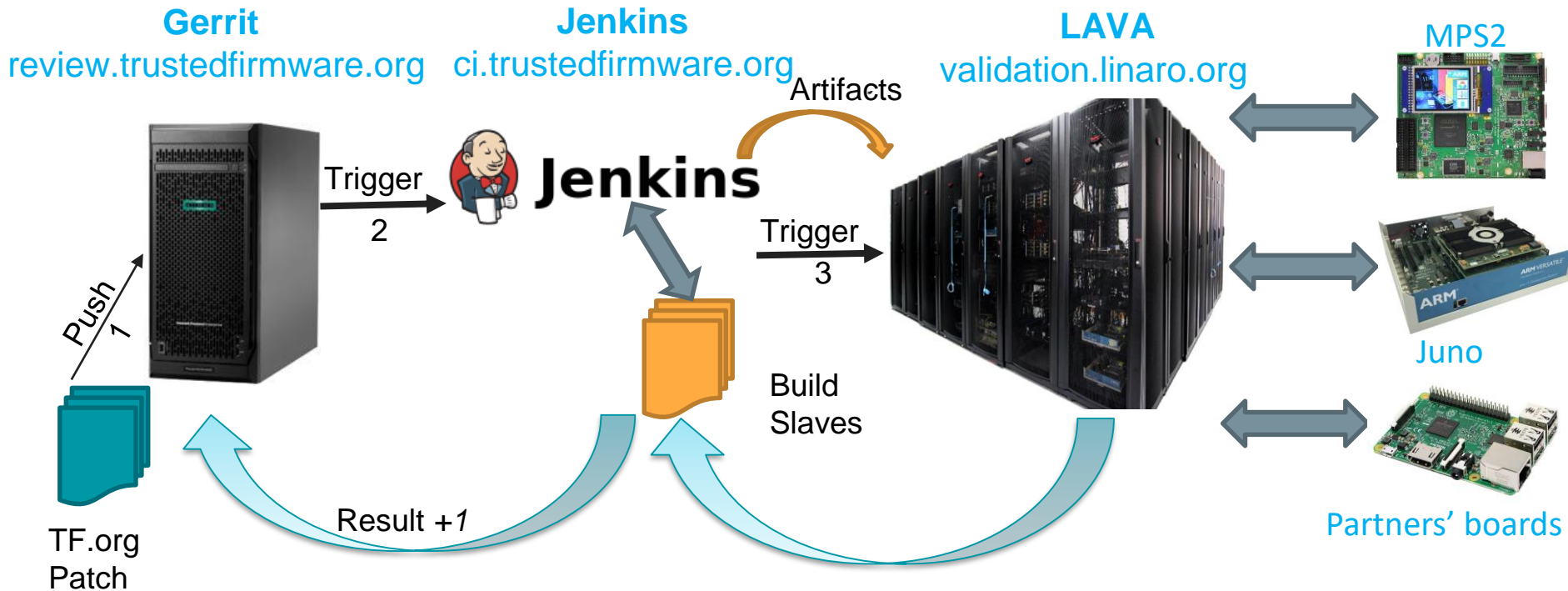


Cloud
Server



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Open CI & Board Farm



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Details & Resources

- Open Source permissive **BSD 3-clause license**
- All contributions accepted under the terms of **DCO**
- Project [mailing lists](#) for technical discussions
- [Git](#) & [Gerrit](#) for open reviews
- [Monthly project status updates](#)
- [Board meeting minutes](#)
- [Project Charter](#)



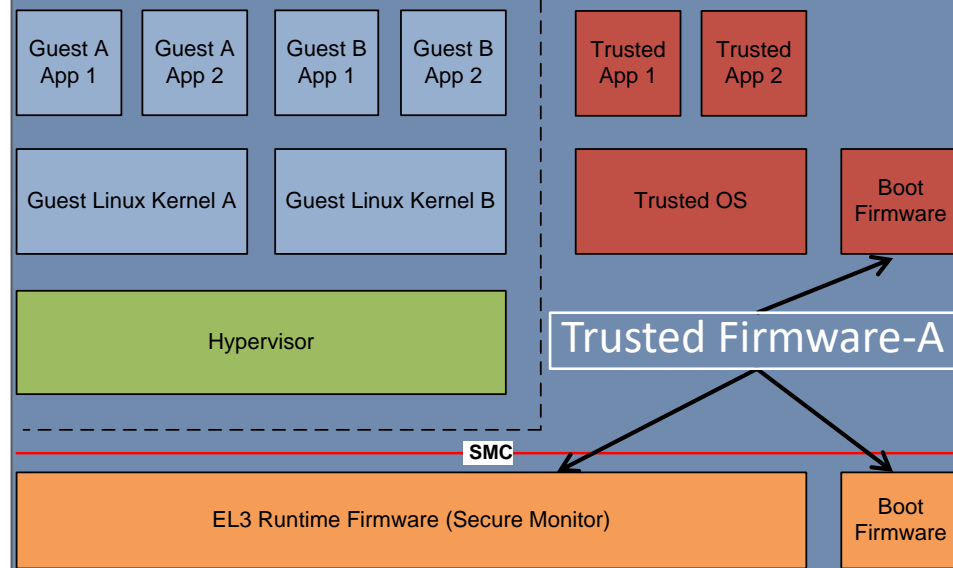
Trusted Firmware-A

Secure world reference software for all Arm Cortex-A & Neoverse processors across all market segments.

Trusted boot flow and runtime firmware providing standard implementation of Arm specifications:

- SMCCC (SMC Calling Convention)
- TBBR (Trusted Board Boot Requirements)
- PSCI (Power State Coordination Interface)
- SCMI (System Control & Management Interface)
- SPCI (Secure Partitions Client Interface)

Cortex-A/Neoverse

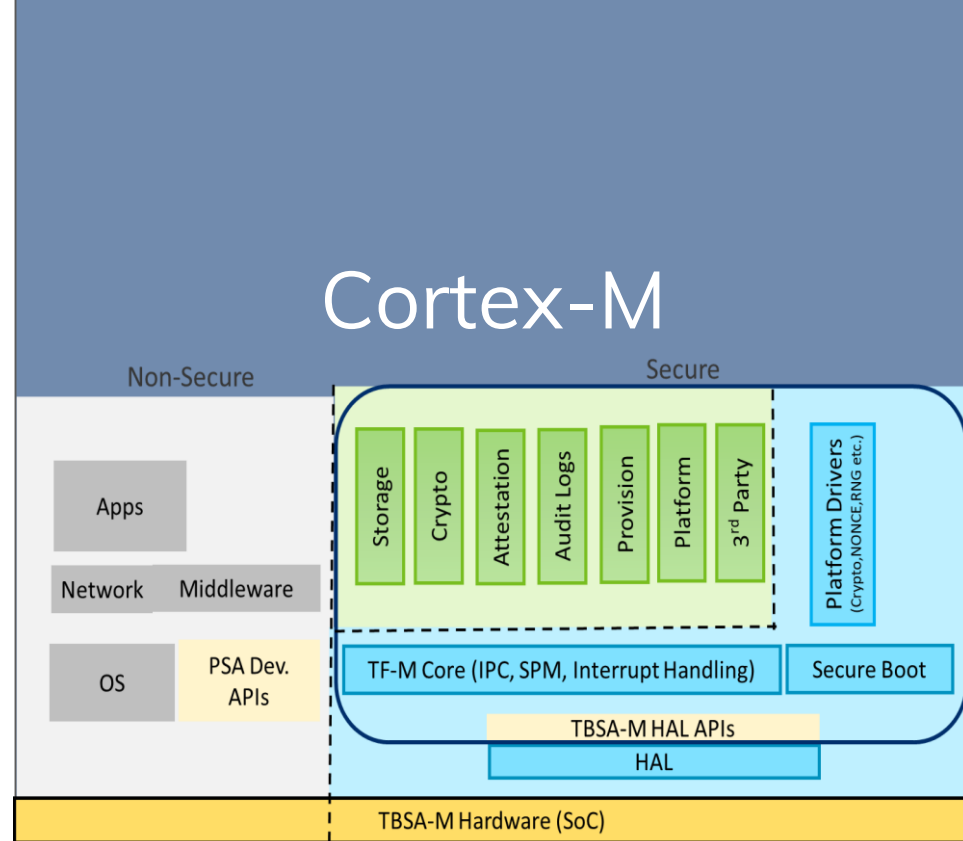


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Trusted Firmware-M

Reference implementation of Arm Platform Security Architecture (PSA)
It provides Trusted Execution Environment for Arm Cortex-M processors.

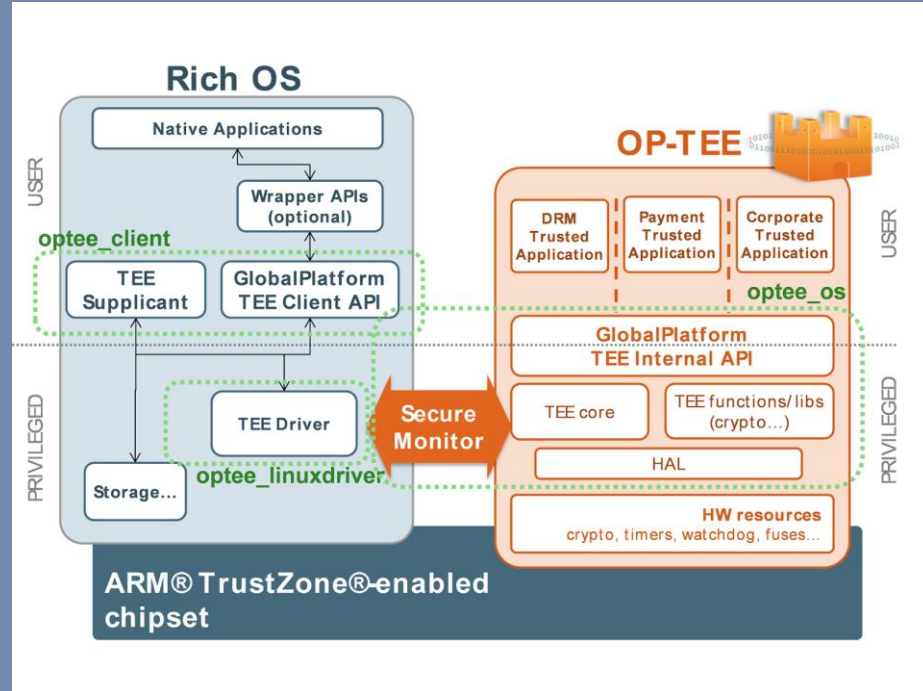
It consists of Secure Boot and a set of Secure Services such as Secure Storage, Crypto etc. for Applications accessible via PSA Developer APIs.



OP-TEE

A reference implementation of a Trusted Execution Environment (TEE), designed as companion to a non-secure Linux kernel running on Arm Cortex-A cores using the TrustZone technology.

Implements [TEE Internal Core API](#) v1.1.x and the [TEE Client API](#) v1.0, as defined in the [GlobalPlatform API](#) specifications.



How to Get Involved

Become a project member

Platinum Board members define the mission and strategy: \$50K/year

General members receive project updates, make requests to the board and have joint representation at Board meetings: \$2.5-25K*/year

Read the project [Charter](#)

* Fee according to company size and type

Contact:

enquiries@TrustedFirmware.org

for more information



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Thank you

Adopt Trusted Firmware to build your next secure platform

Visit www.TrustedFirmware.org or email
enquiries@trustedfirmware.org for more information