

An Introduction of Network Switches

Yaxuan Qi

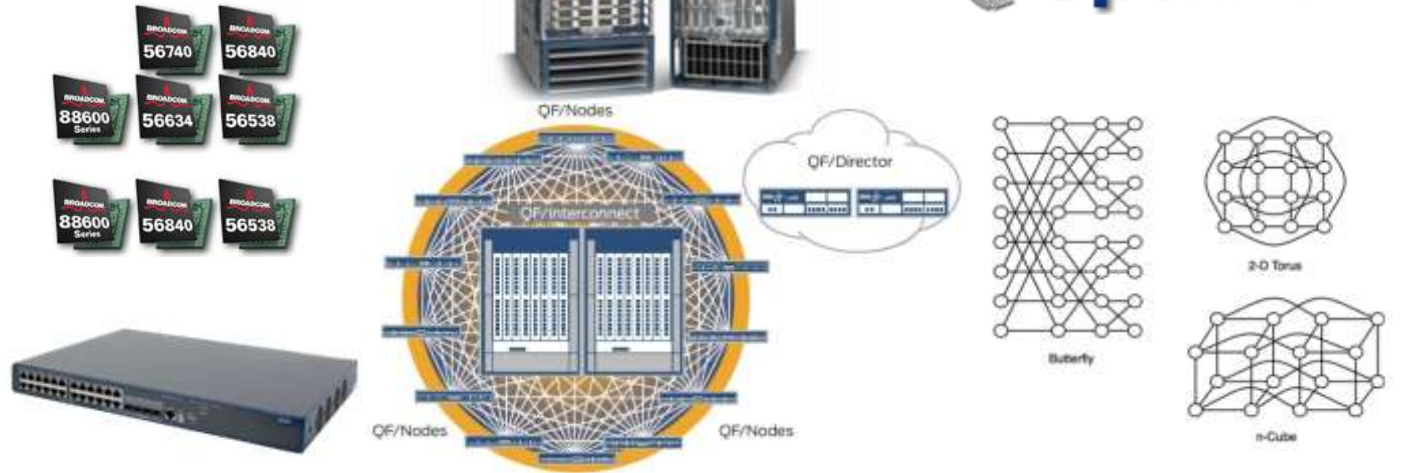
Venus Team, NSLab

RIIT, Tsinghua Univ.

In courtesy of Juniper, NEC, ONF



Outline



Fundamentals

Chips

Boxes

State-of-the-Art

Chassis

Fabrics

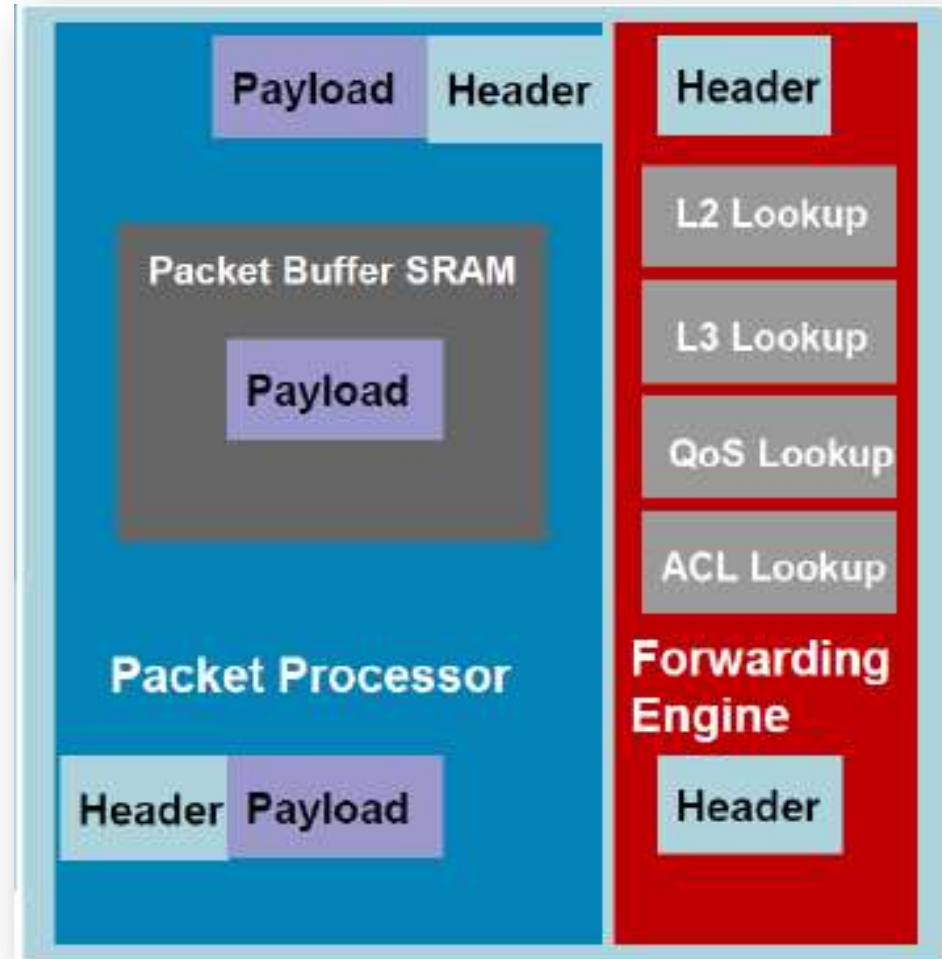
Future

OpenFlow

SDN



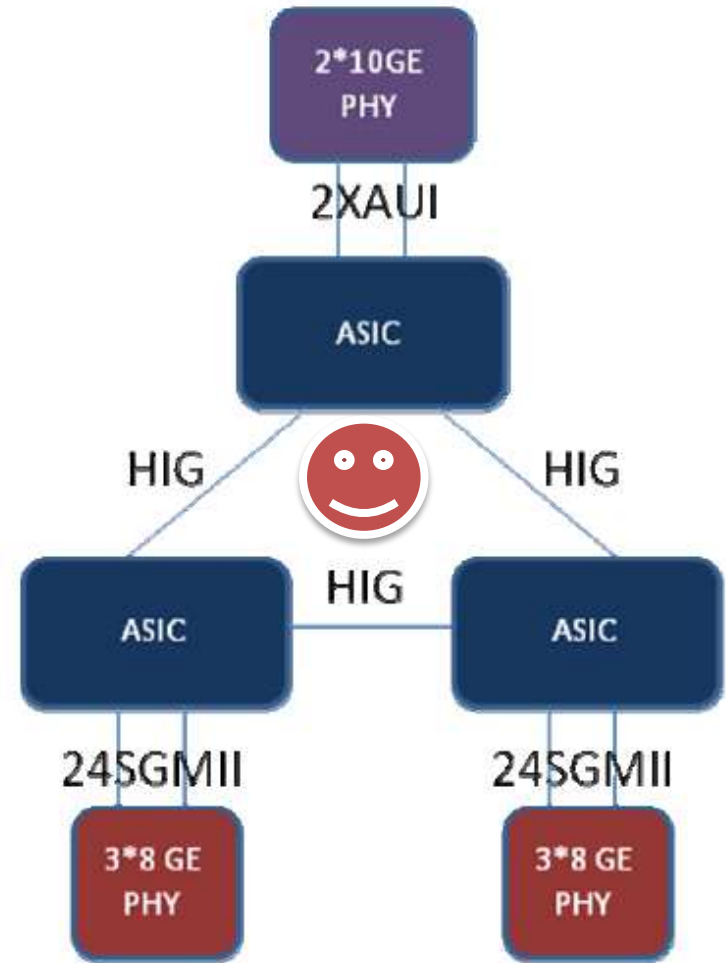
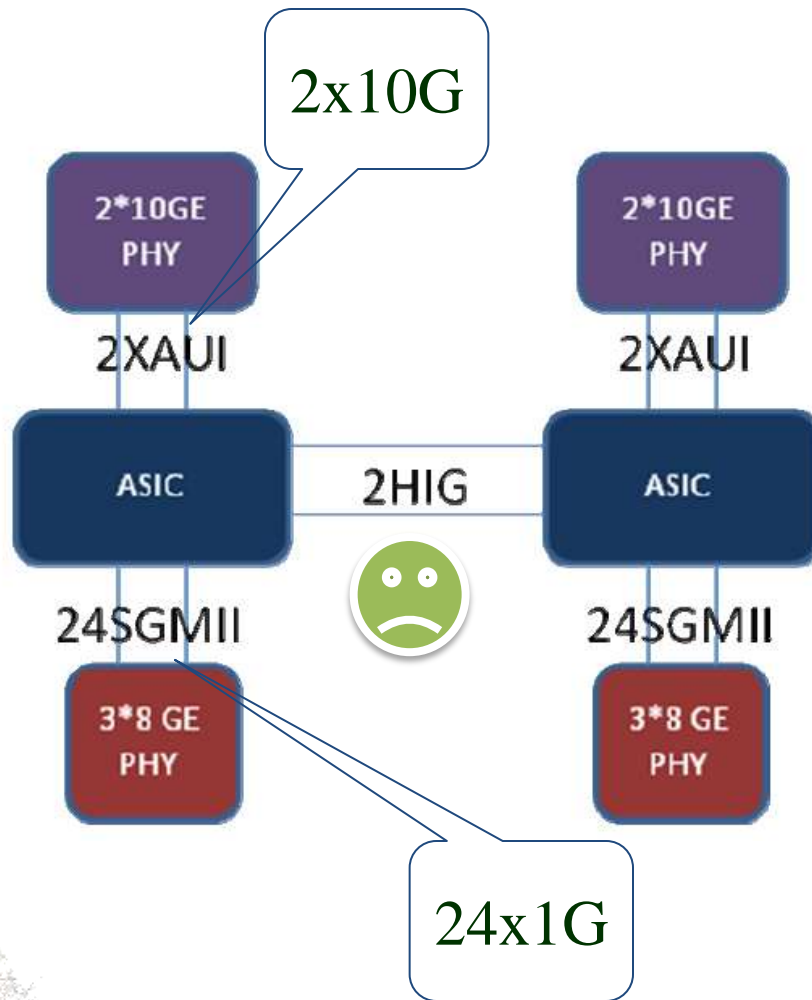
A Single Switch Chip



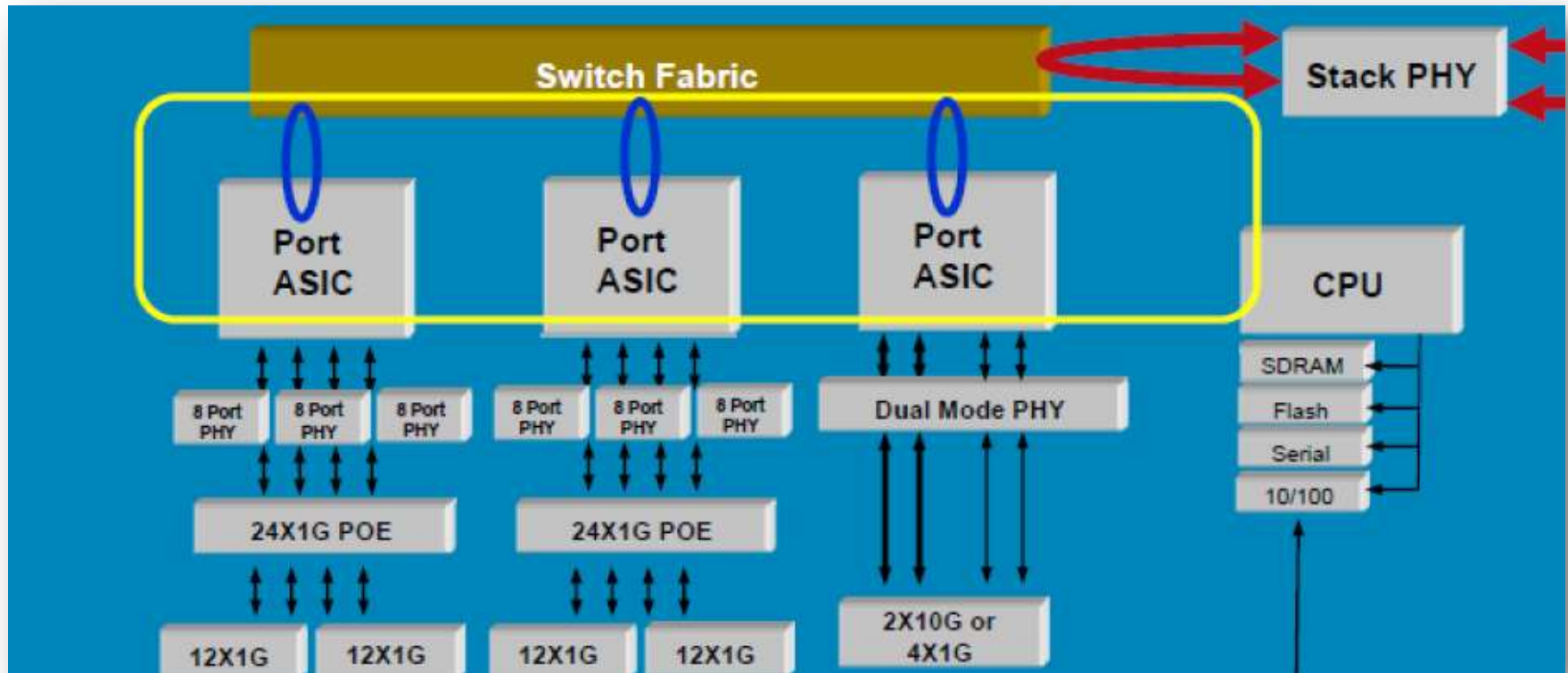
switch processing



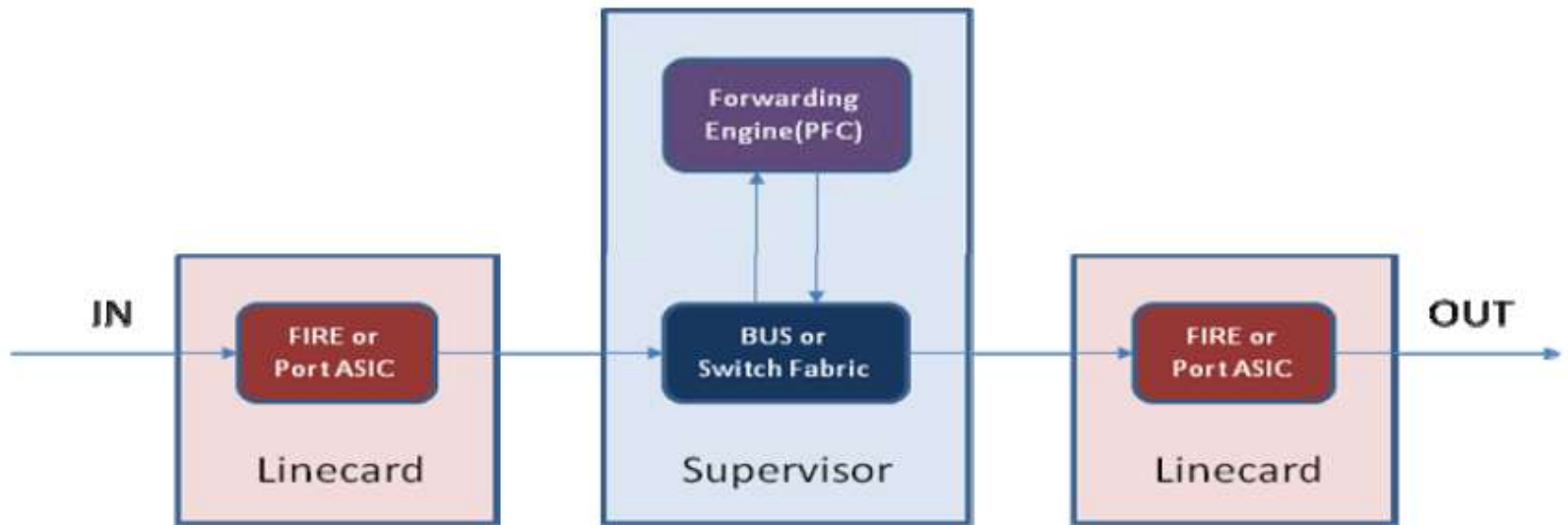
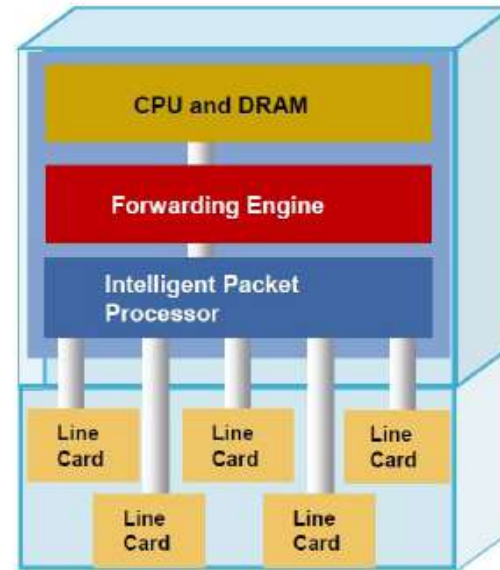
Switch Chips



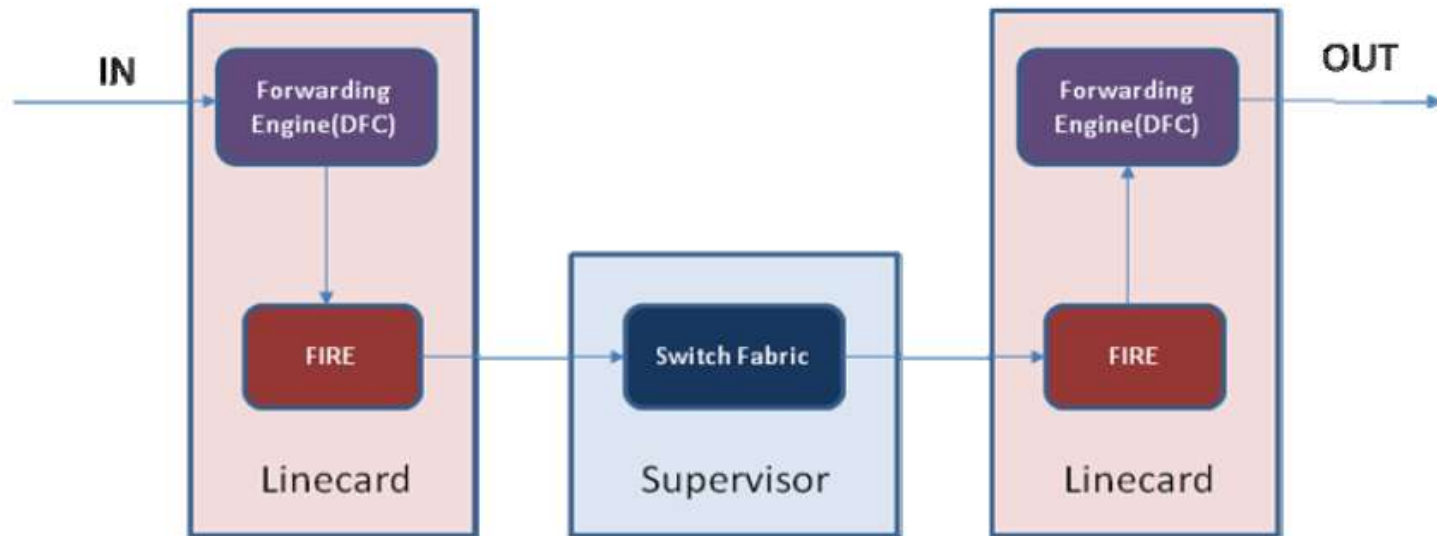
Chips in a Box



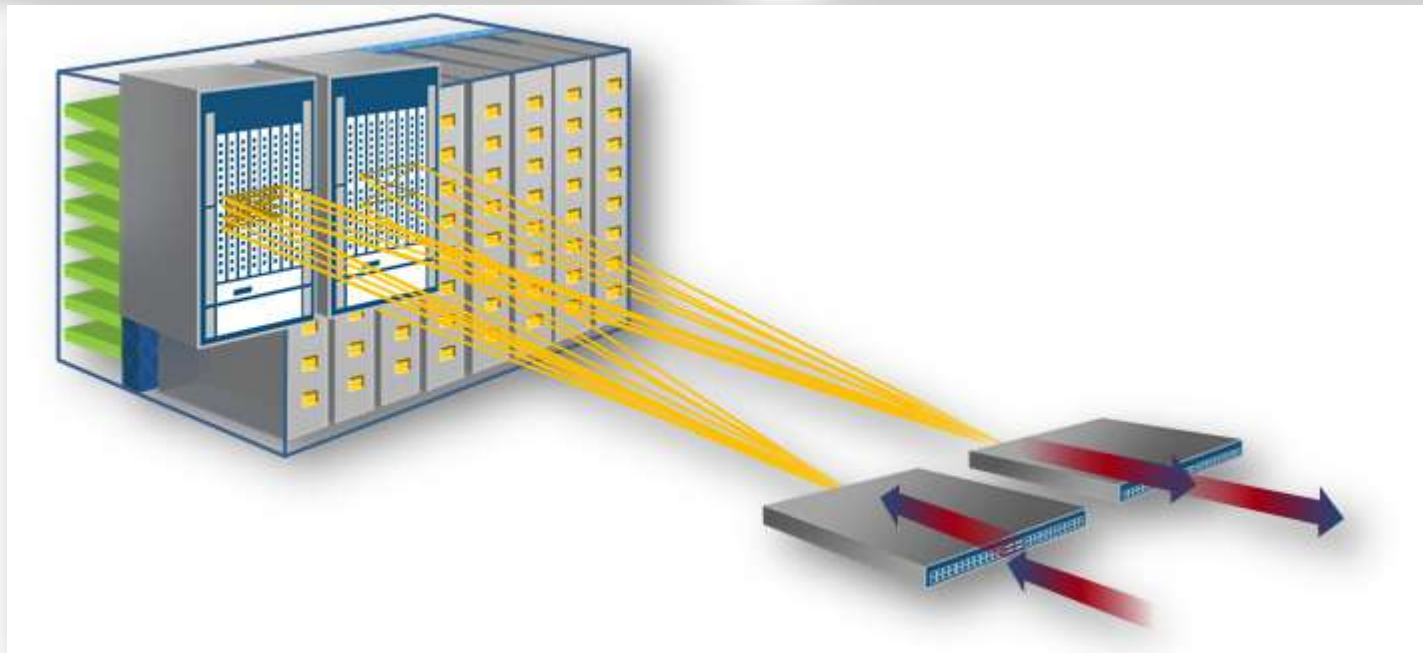
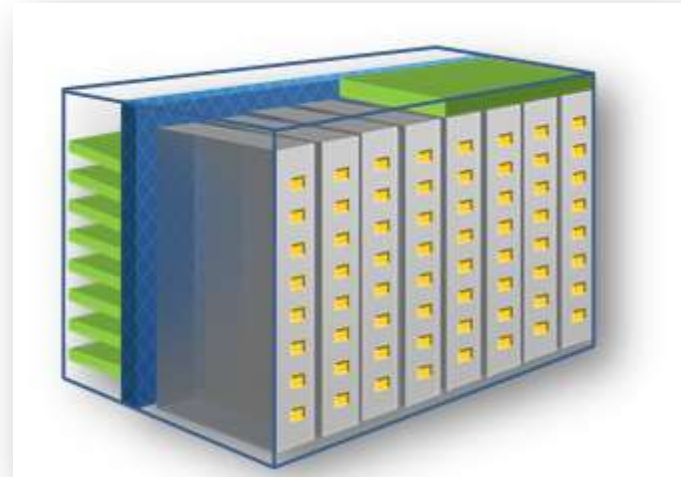
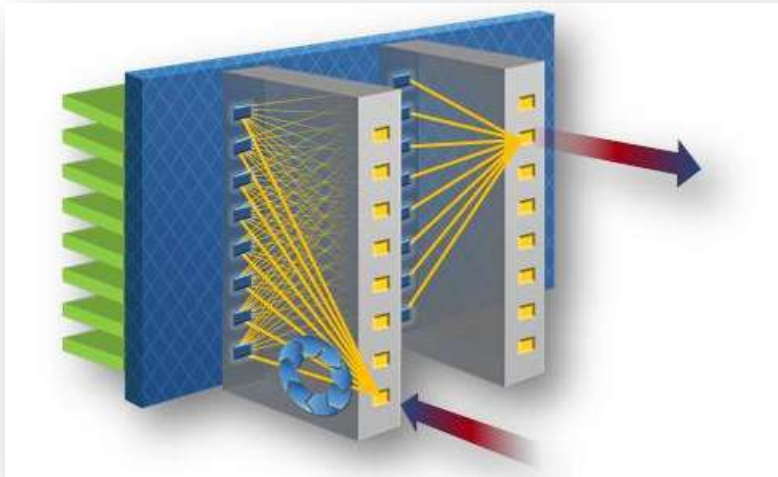
Chassis: Cisco Catalyst 65xx



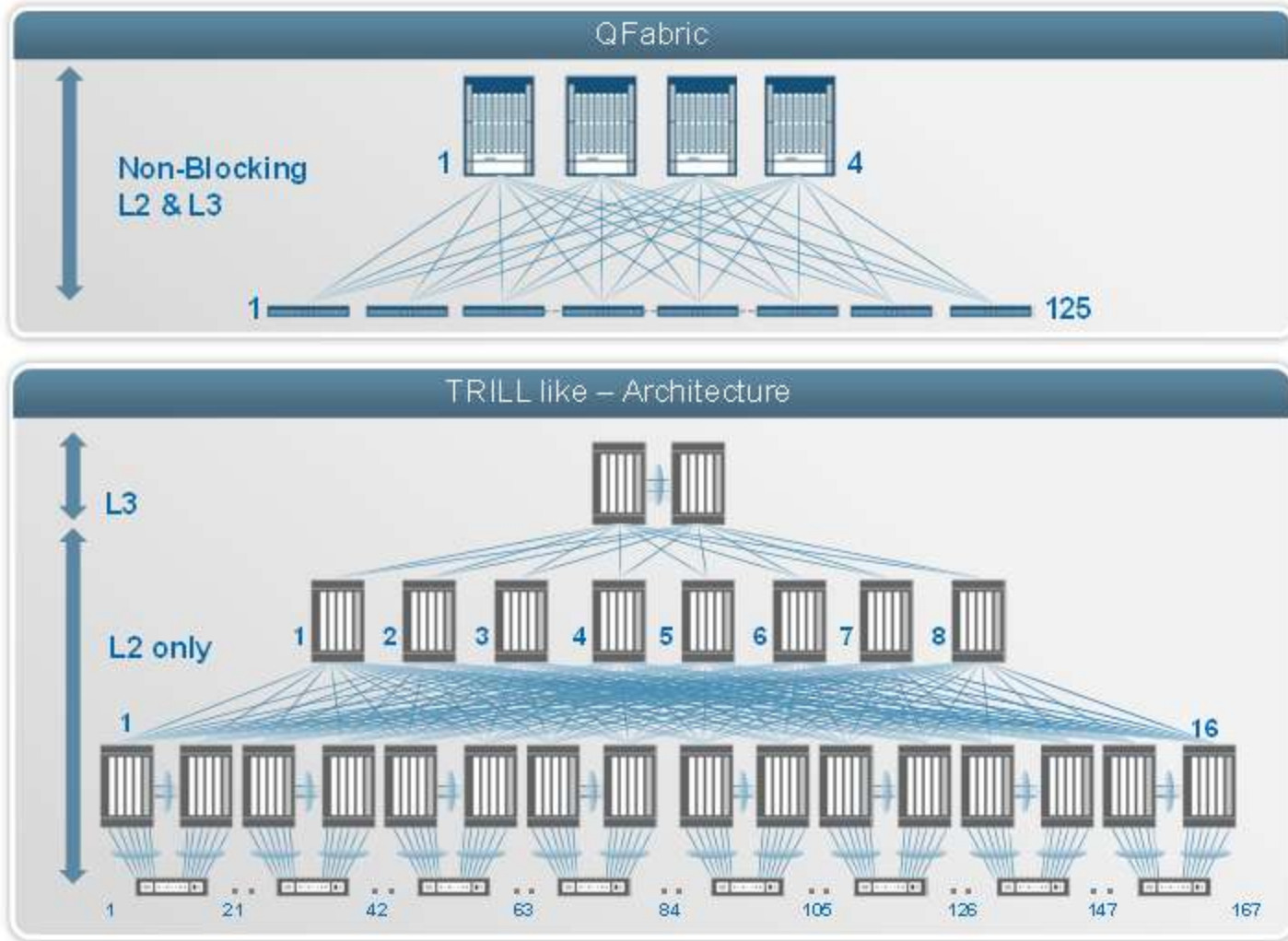
Chassis: Cisco Nexus 70xx



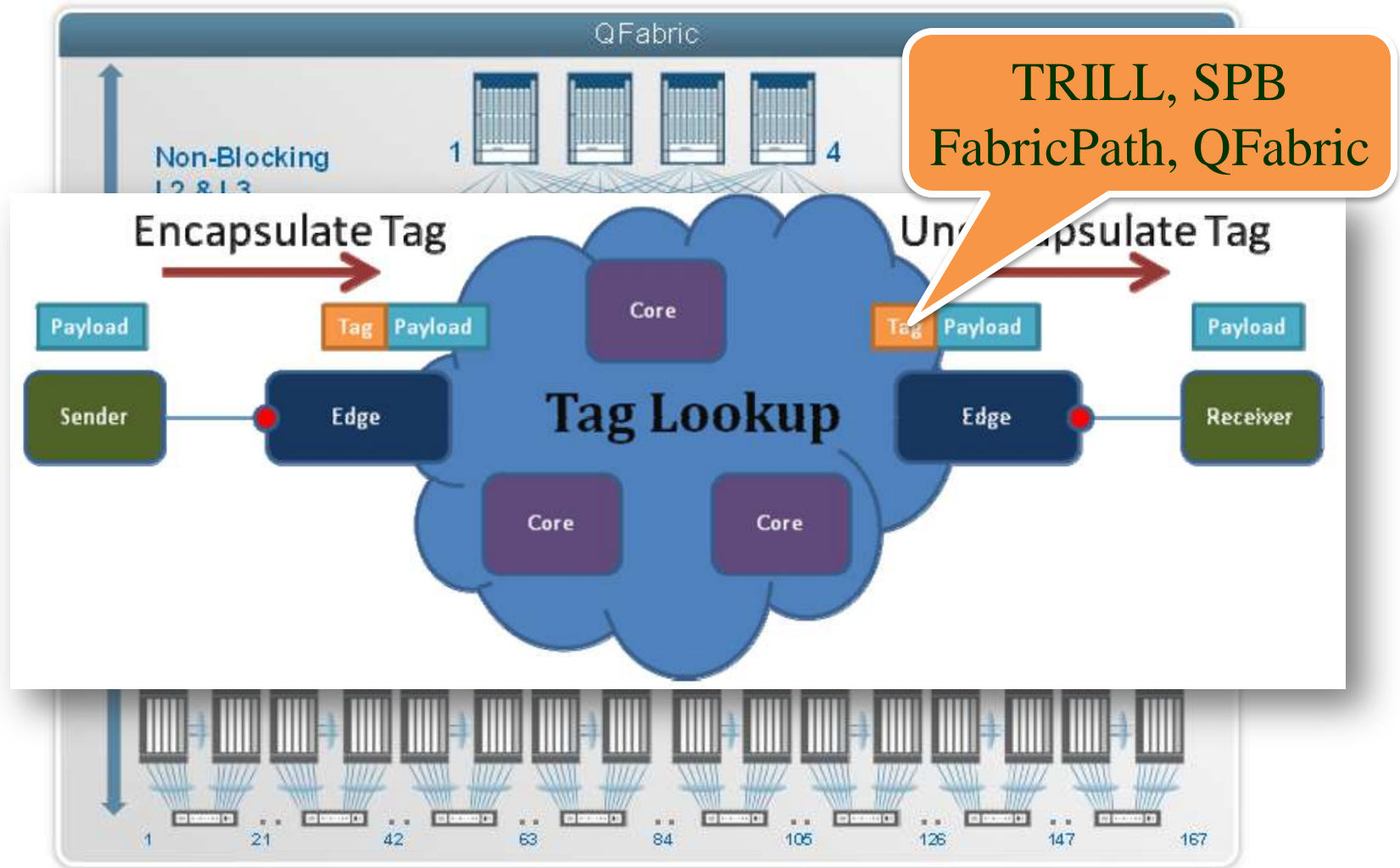
From Switch to Fabrics



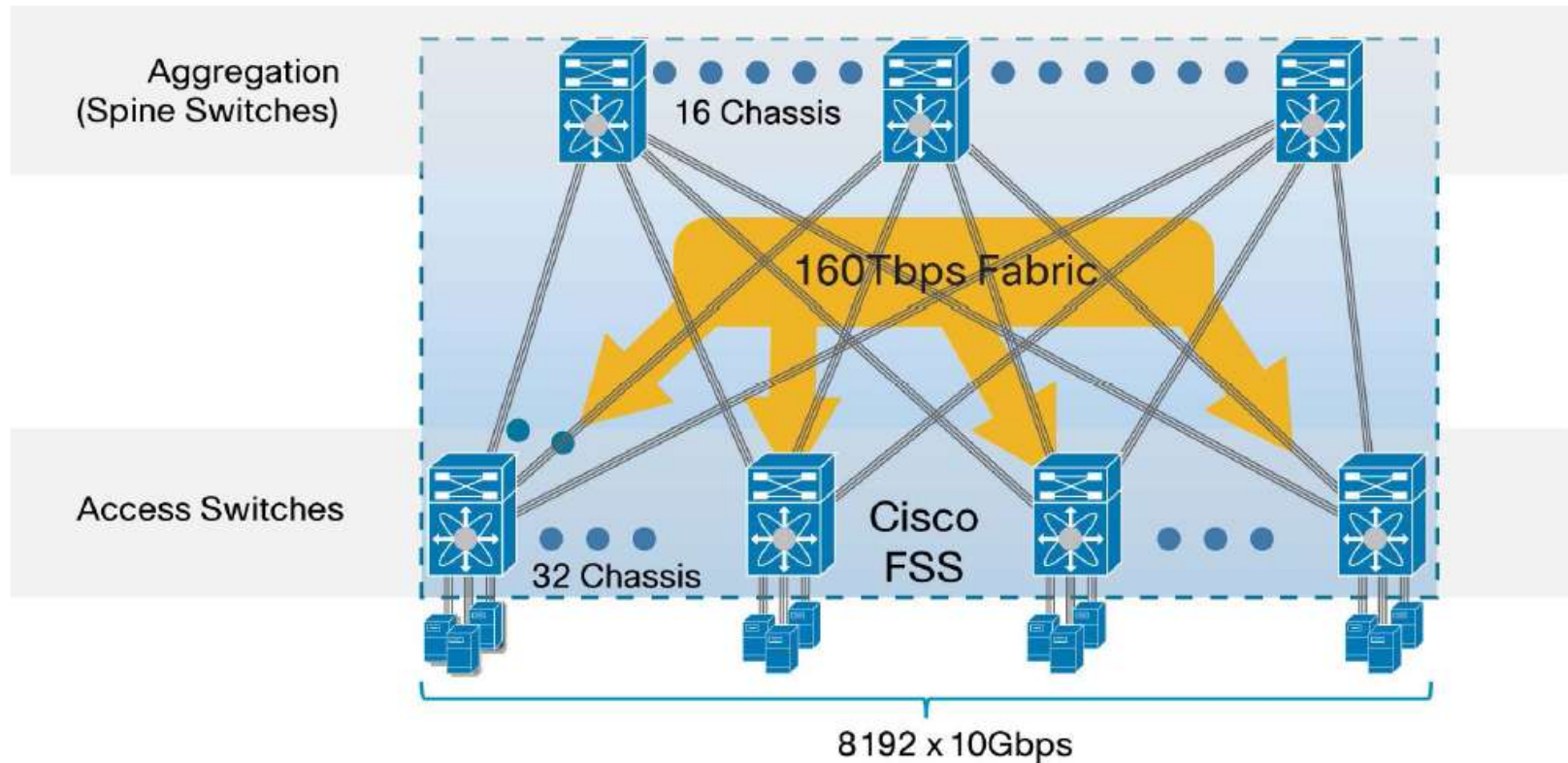
State of the Art Network Fabric



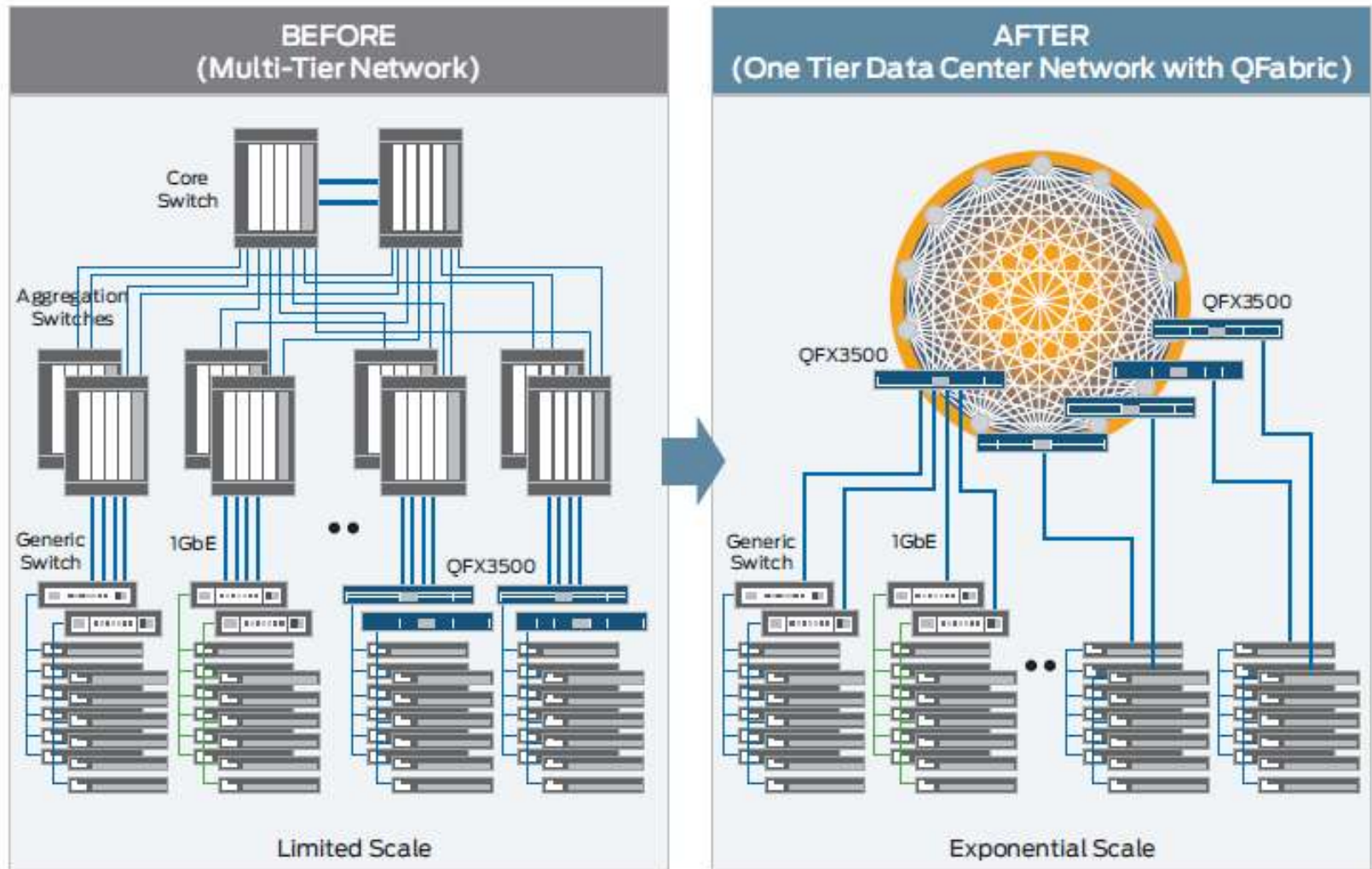
State of the Art Network Fabric



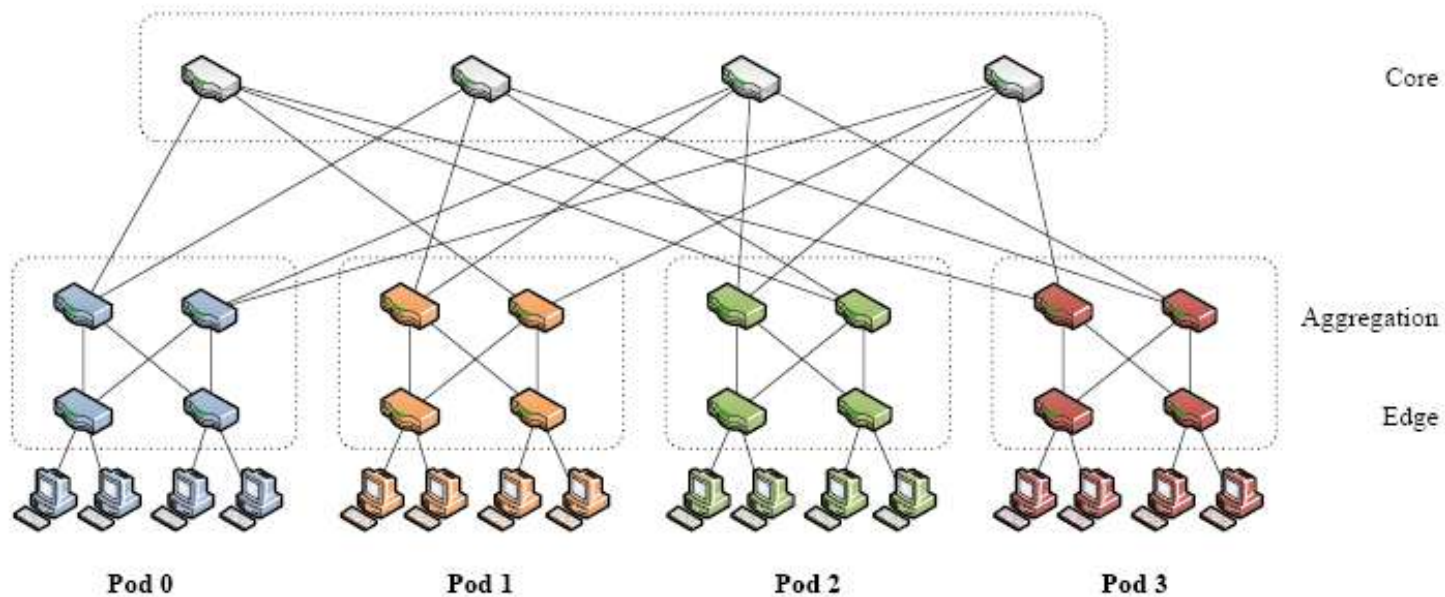
Cisco FabricPath



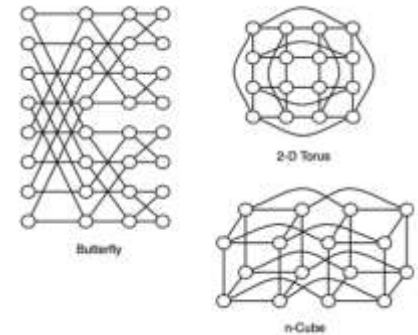
Juniper QFabric



Innovation with Switches



Innovation with Switches



Switching Fabric
(TRILL, SPB, FabricPath, QFabric)

SDN layer
(Open vSwitch, OpenFlow)

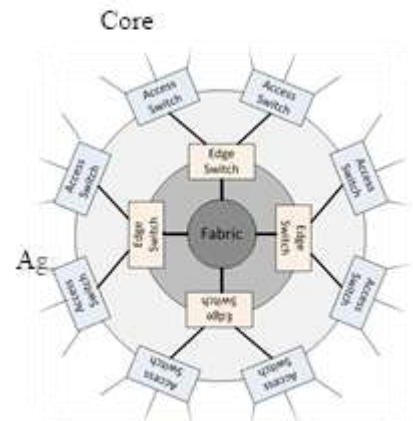
Virtualized Resource (VMware, Citrix, XEN, KVM)

Pod 0

Pod 1

Pod 2

Pod 3



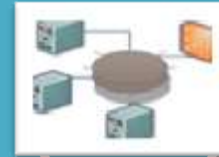
NSLab LiveCloud



Venus Network



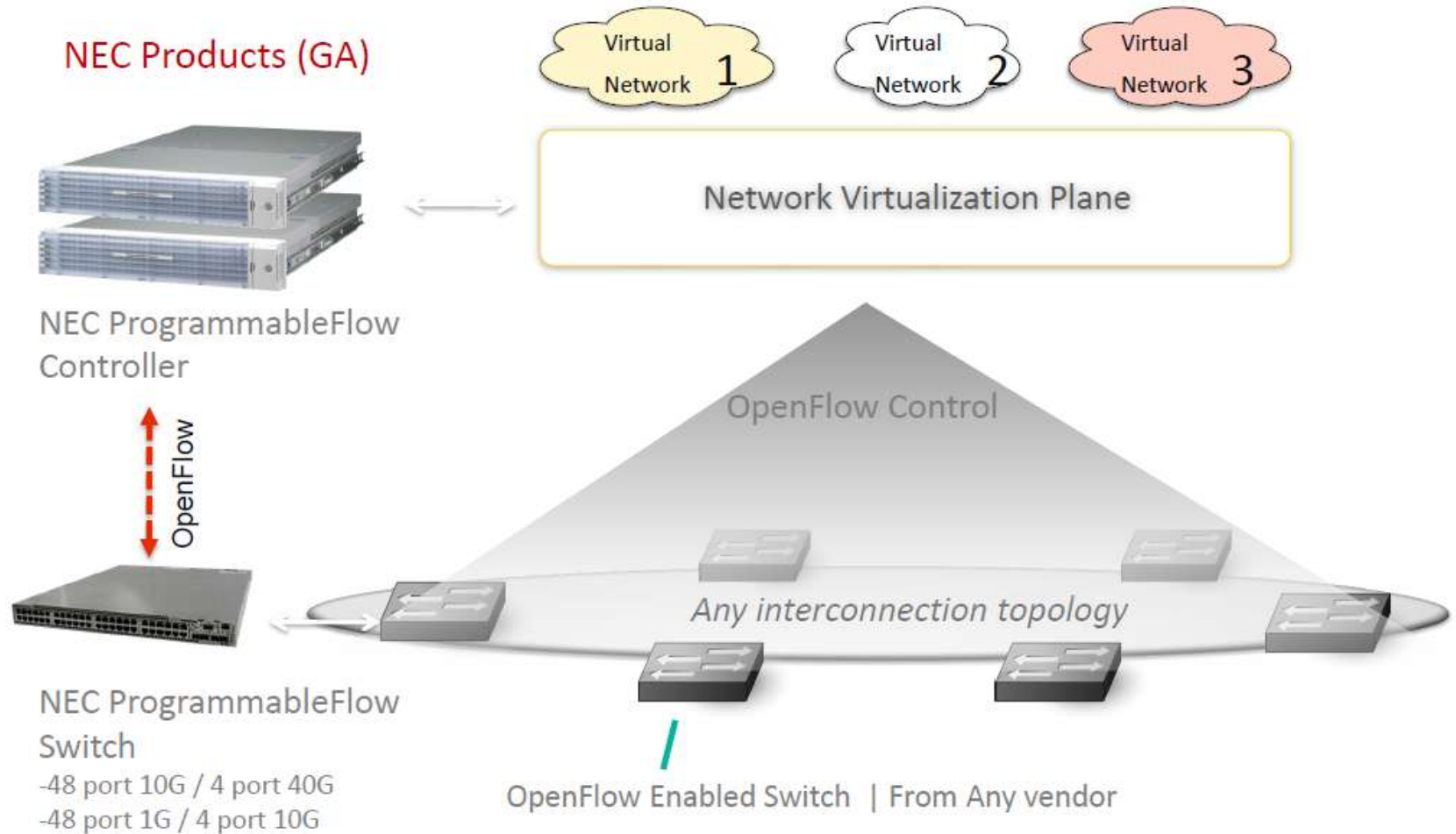
Mars Network



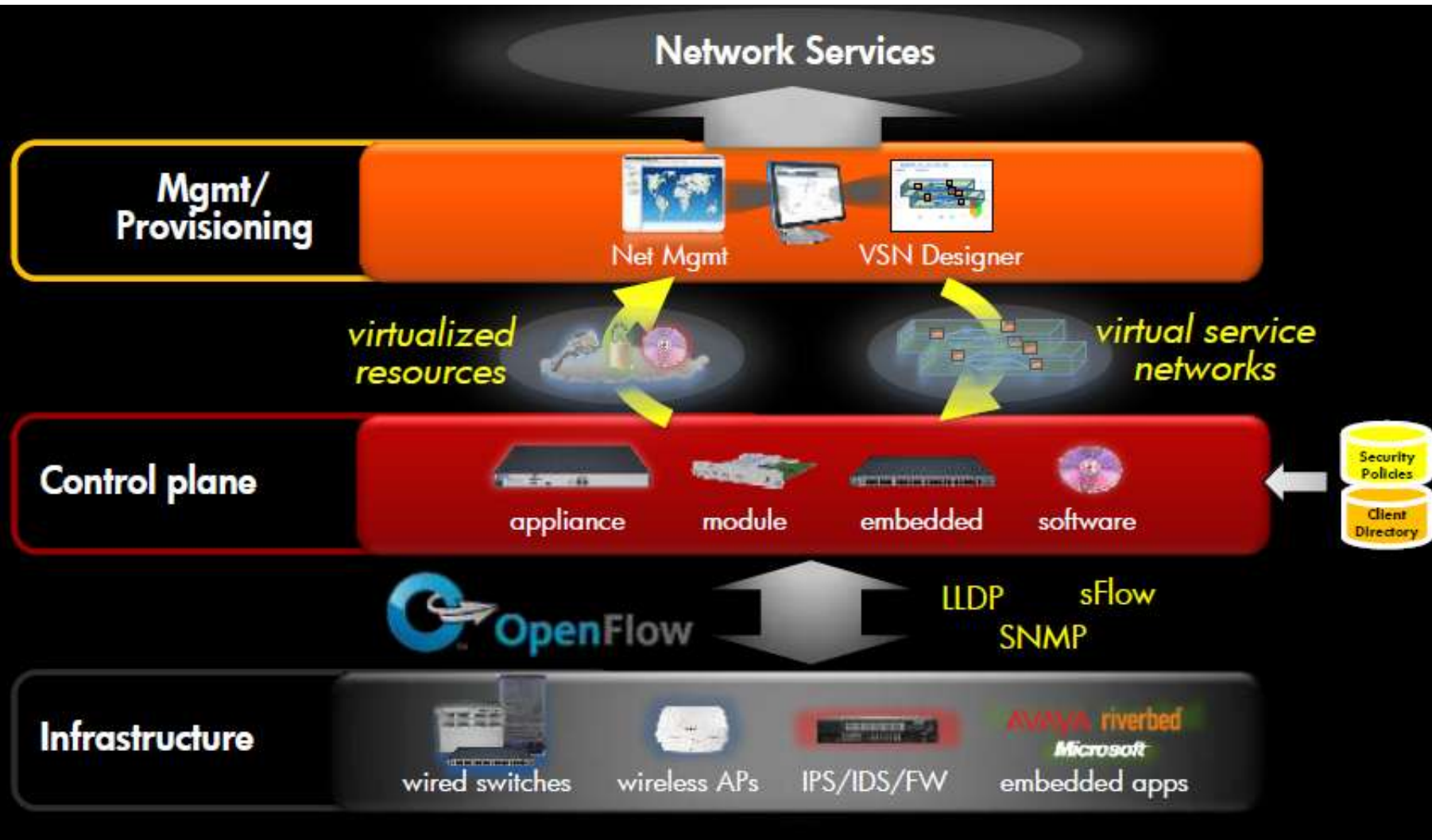
Saturn Network



NEC ProgrammableFlow



The Future



Innovation or Renovation



OpenFlow and Software Defined Networking

Word of caution: techtonic re-imaginings will mean a few steps back before moving forward

