

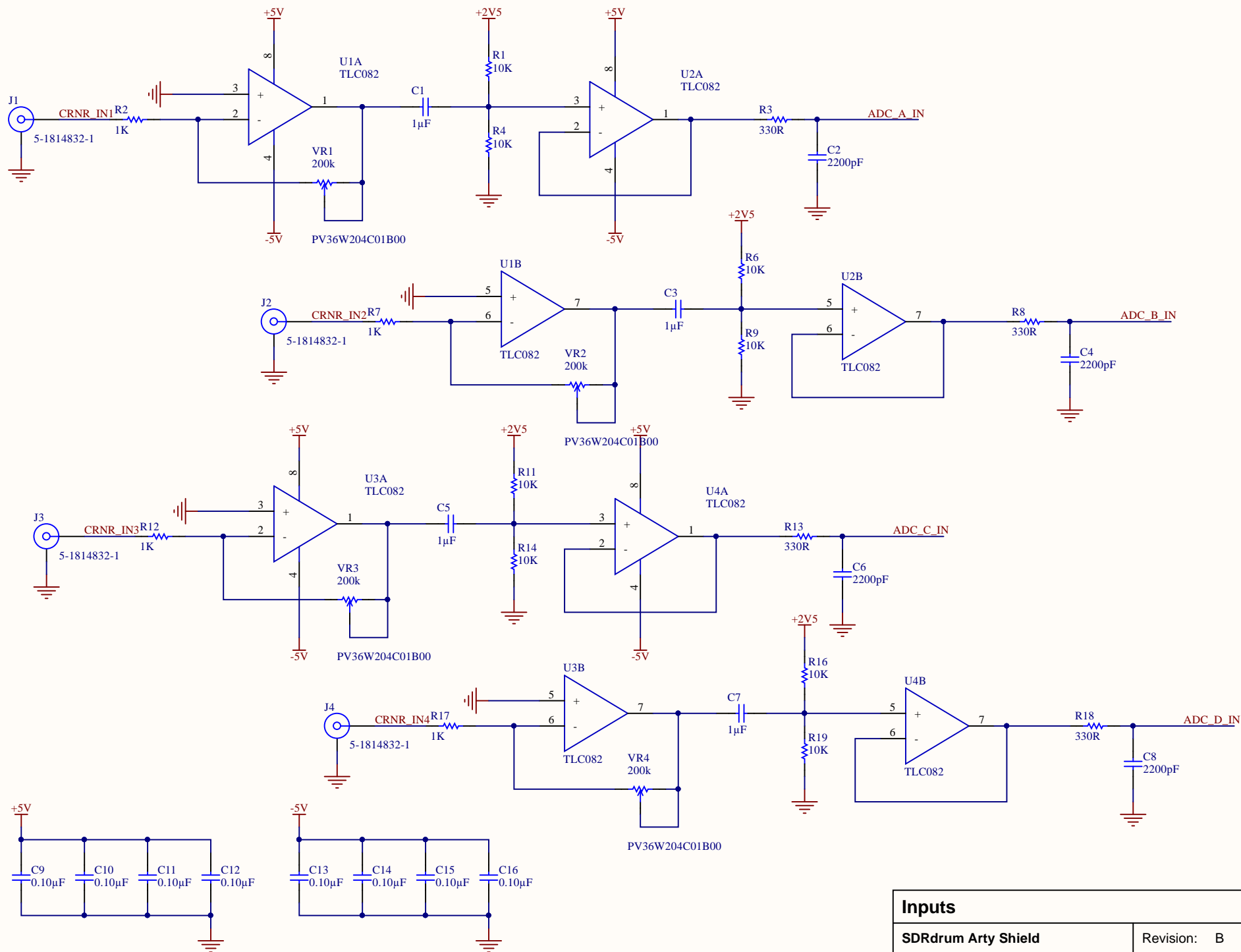
SDRdrum

Arty Shield - Rev B

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Revision	Date	Remarks
A	2017-01-08	Initial revision
B	2019-07-18	Fixed input gain adjustment, added DAC reconstruction filter capacitors, fixed PCB bugs

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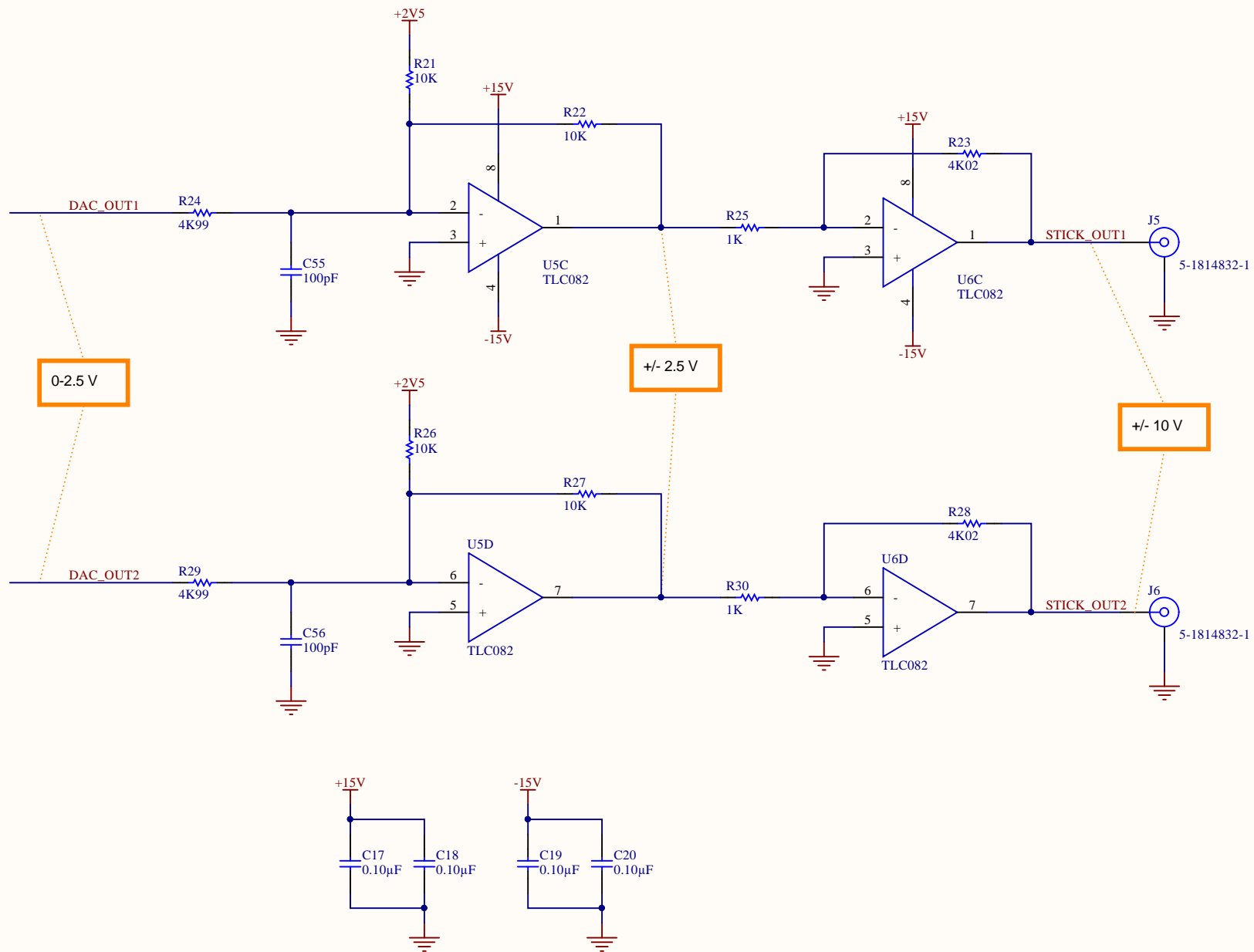
Inputs

SDRdrum Arty Shield

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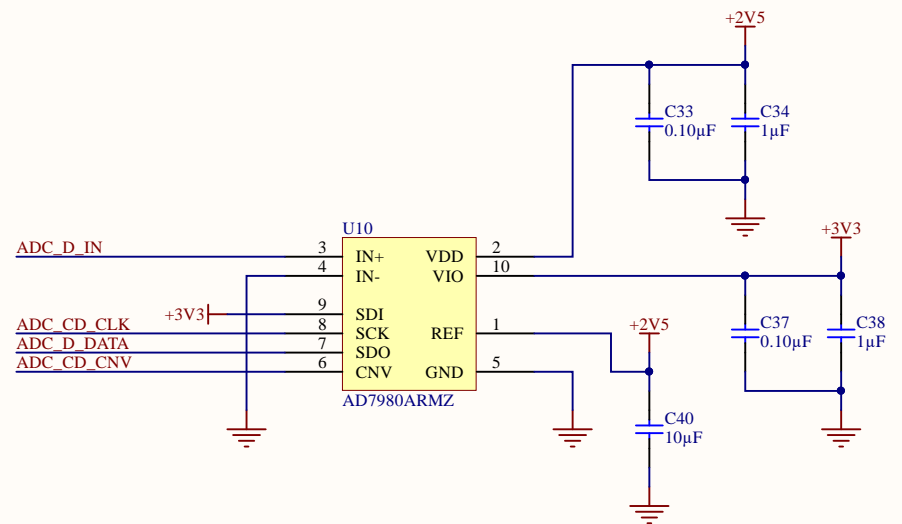
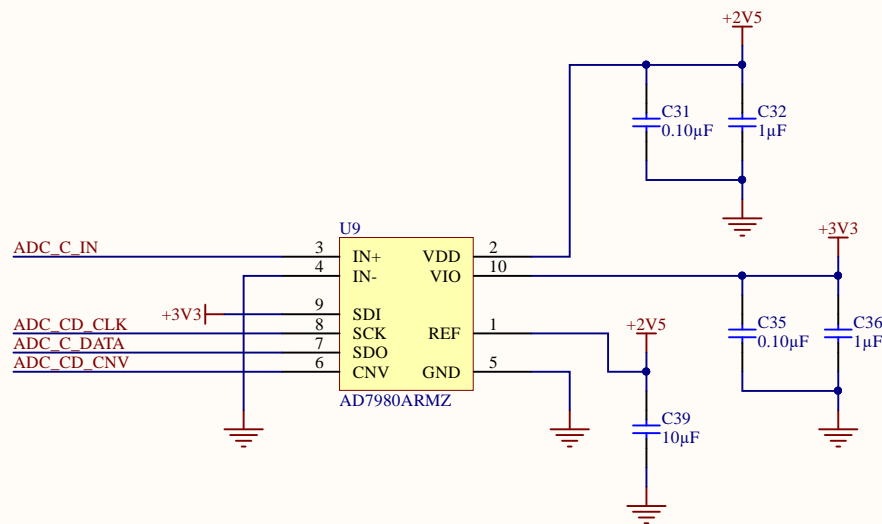
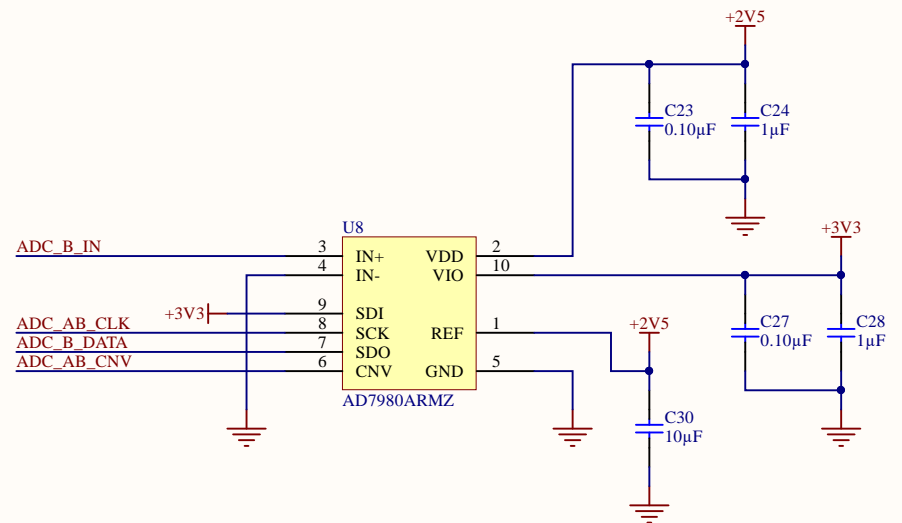
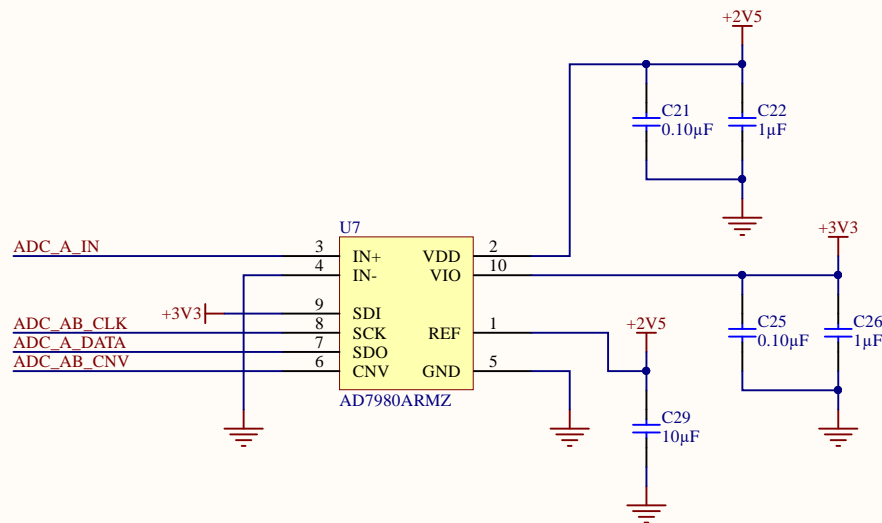
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Outputs

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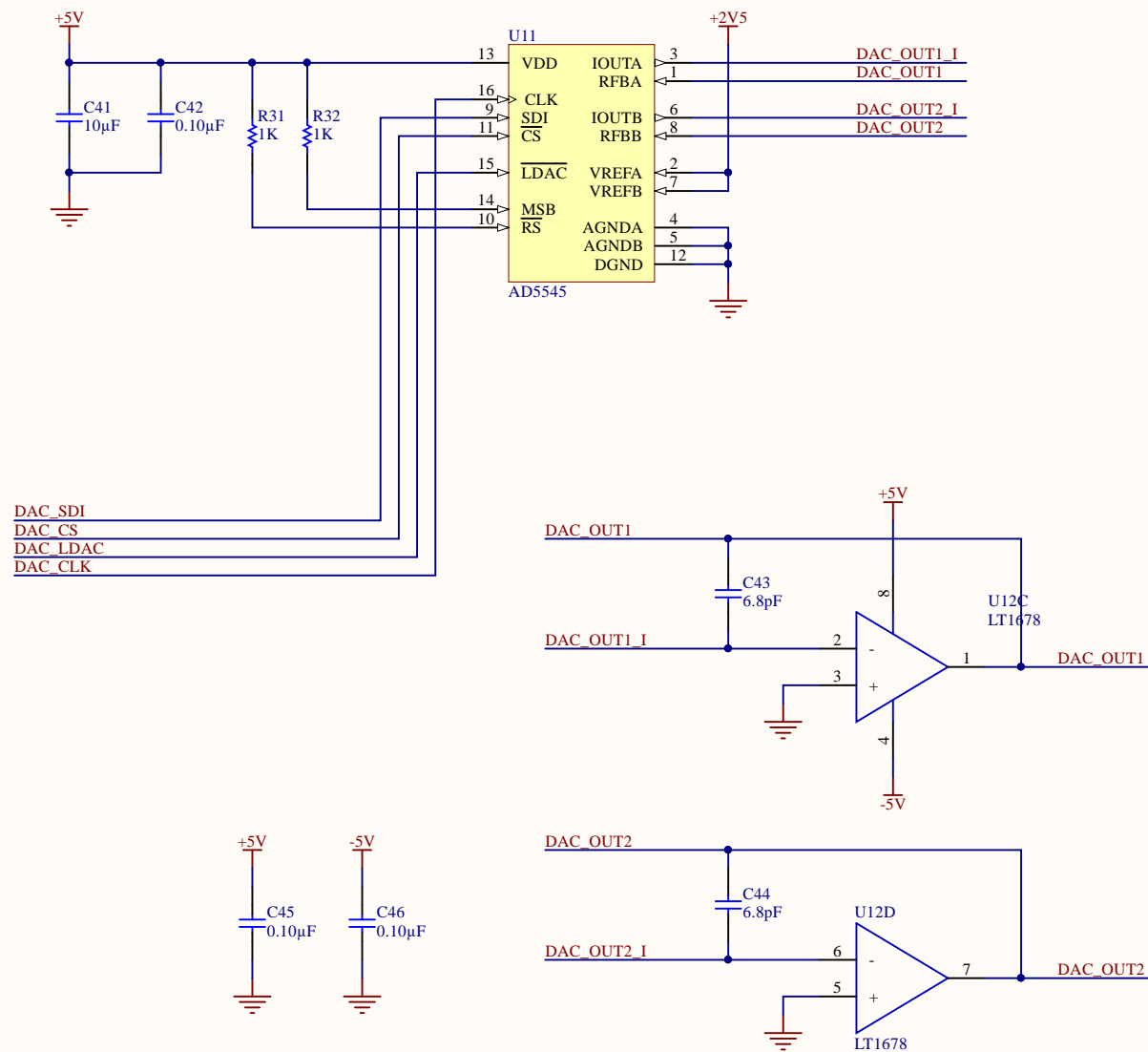
ADCs

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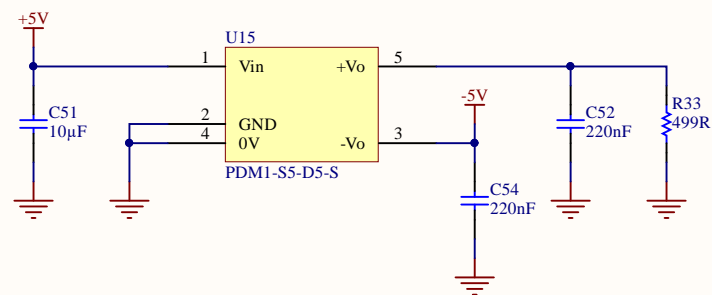
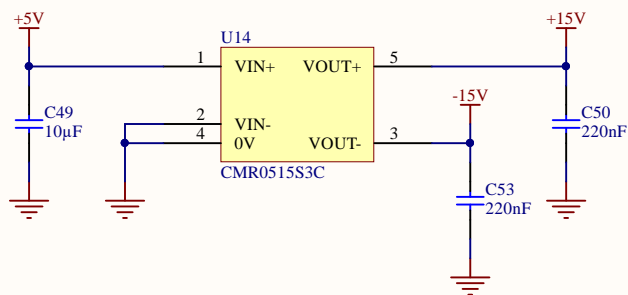
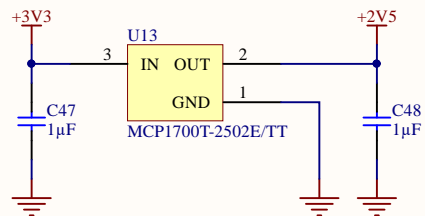
DACs

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DESIGN NOTE:
PDM1-S5-D5-S requires a minimum load of 10 mA per channel. R? provides this minimum load on the unused 5 V output.

Power Supplies

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Connectors and Mechanical	
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