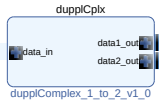
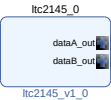
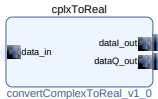
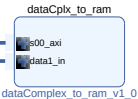


ADC



AXI

PL->PS



DAC

