

Semnale de Control

MIPS Ciclu Unic -Păvălău Tudor-Ștefan

	Opcode	regdst	extop	alusrc	branch	bgez	jump	aluop	memwrite	memtoreg	regwrite
R	000000	1	X	00	0	0	0	000	0	0	1
ADDI	001000	0	1	01	0	0	0	001	0	0	1
LW	100011	0	1	01	0	0	0	001	0	1	1
SW	101011	X	1	01	0	0	0	001	1	X	0
BEQ	000100	X	1	01	1	0	0	010	0	X	0
BGEZ	000001	X	1	10	0	1	0	010	0	X	0
J	000010	X	X	XX	X	X	1	XXX	0	X	0
ANDI	001100	0	1	10	0	0	0	100	0	0	1
ORI	001101	0	1	10	0	0	0	011	0	0	1