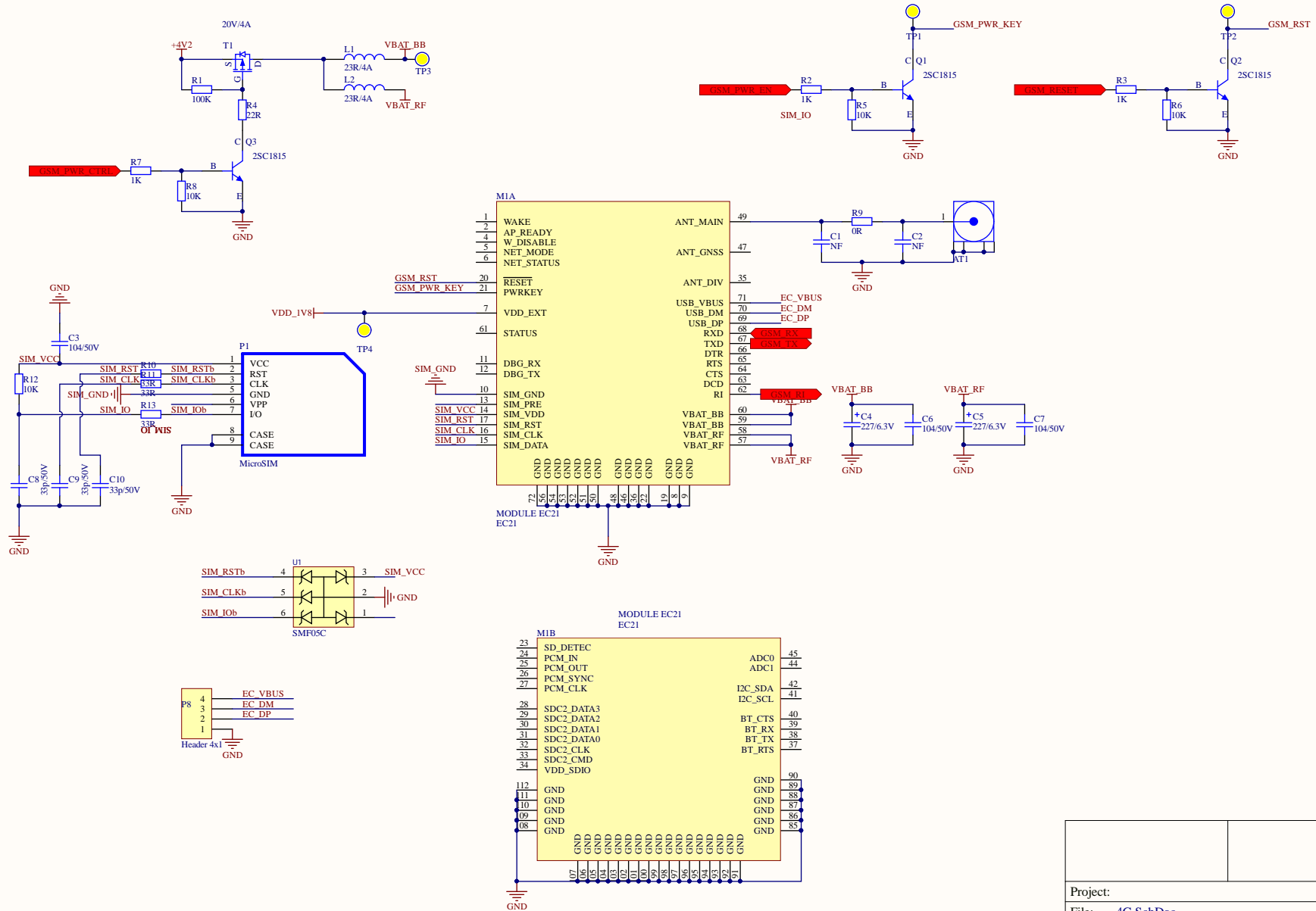


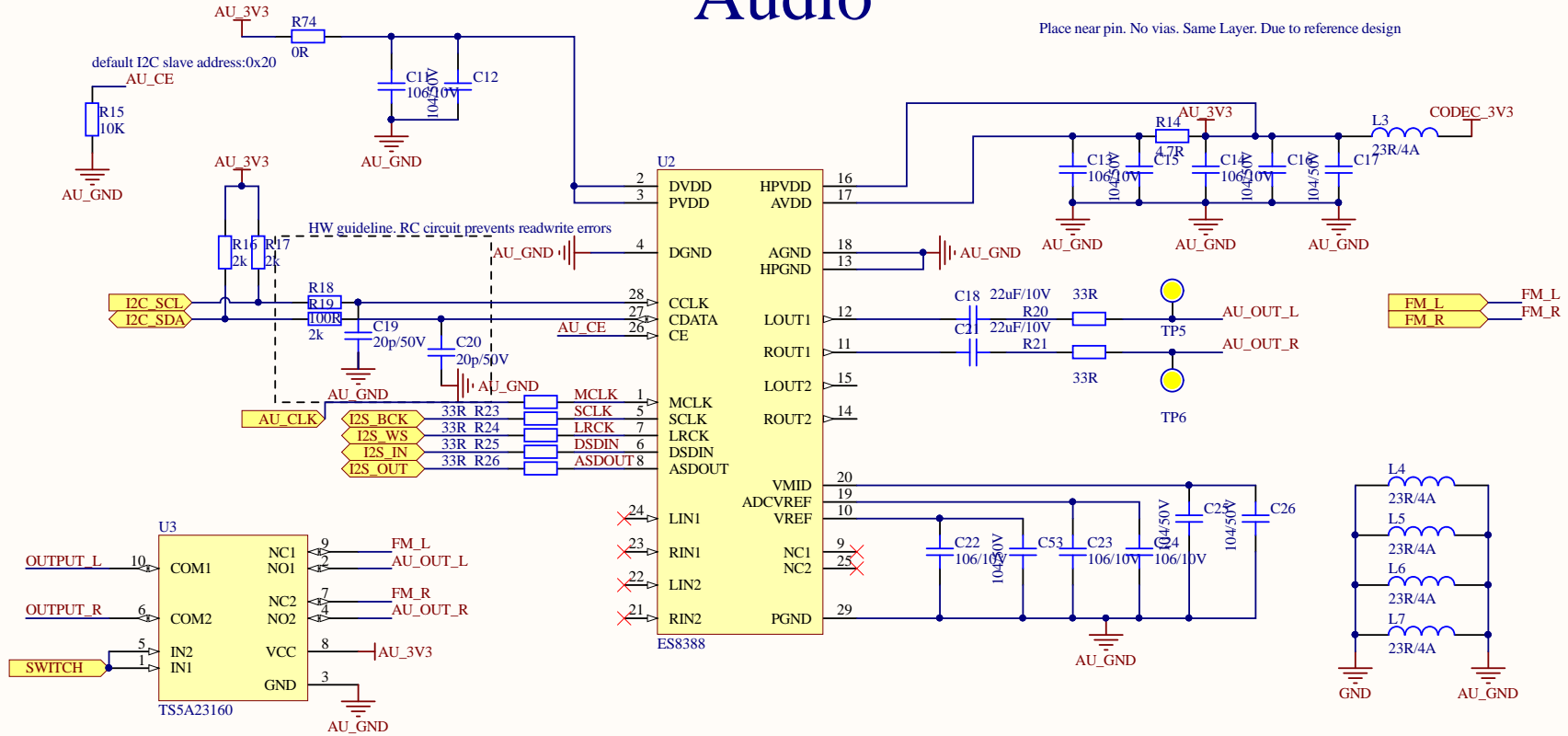
4G



Project:			
File: 4G.SchDoc			
Rev v1.0A	Sheet /	Drawn by: *	
Size A3	5/29/2022	Checked by: *	
DRAFT		Approved by: *	

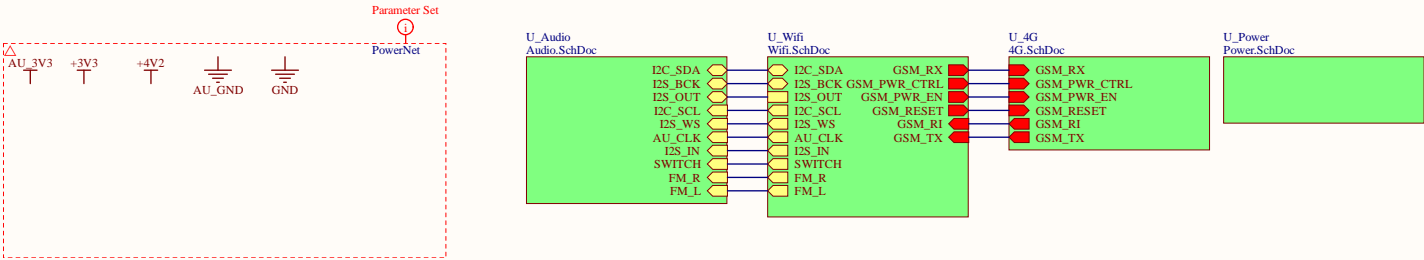
Audio

Place near pin. No vias. Same Layer. Due to reference design



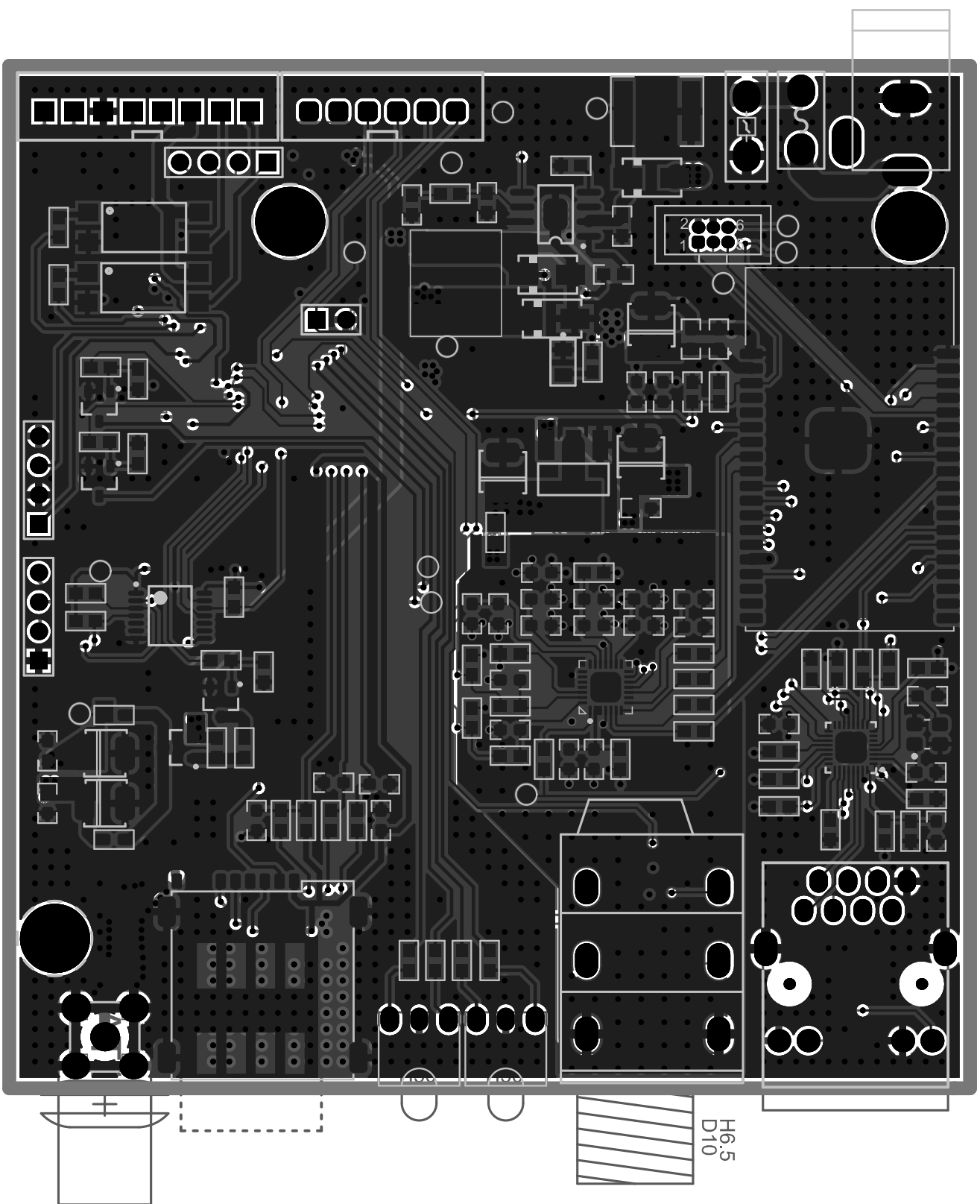
Project:			
File:		Audio.SchDoc	
Rev v1.0A	Sheet * / *	Drawn by: TueTD	
Size A4	5/29/2022	Checked by: *	
DRAFT		Approved by: *	

(Block Diagram)

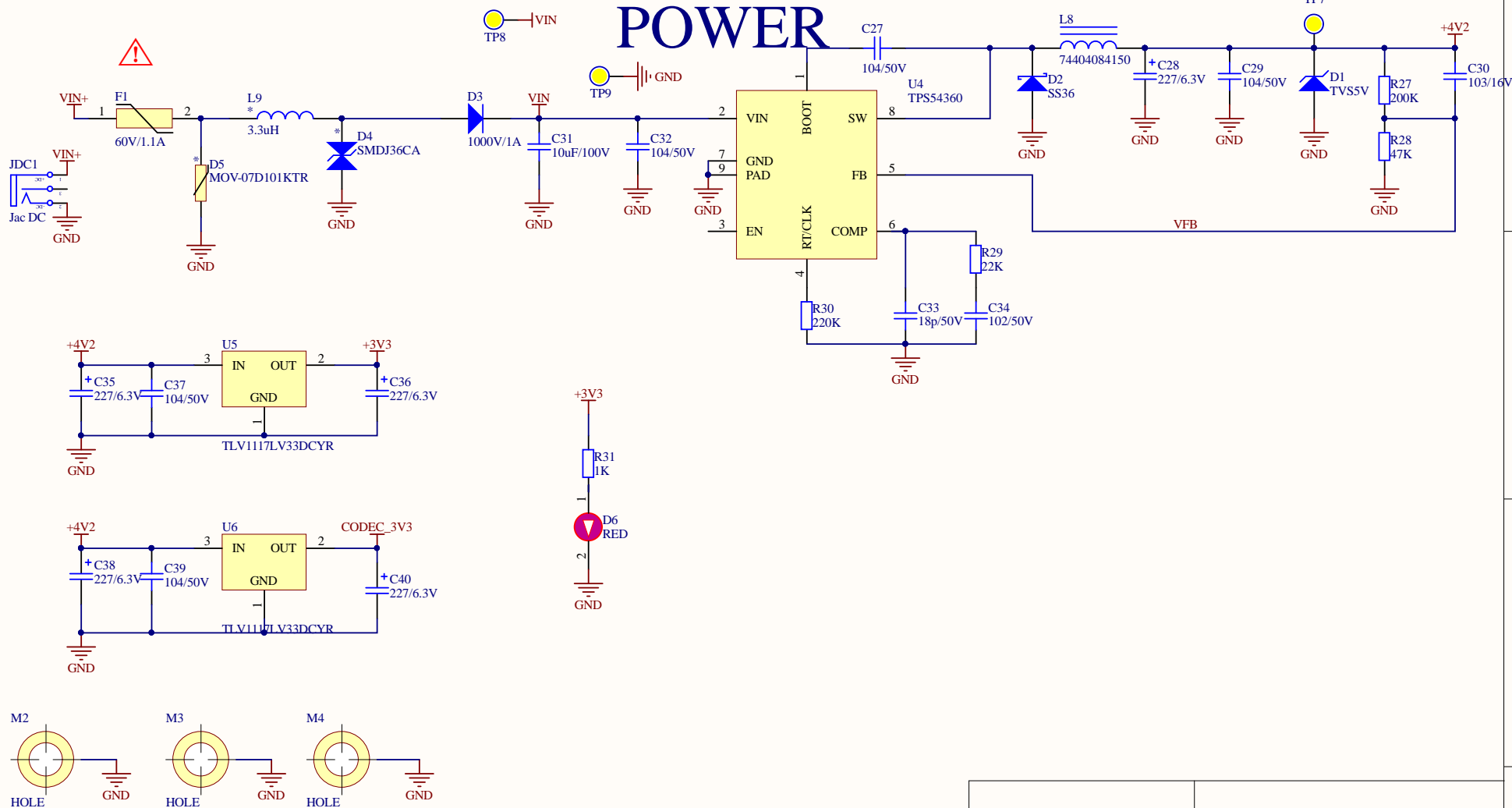


0000281001.X(2015)

Project:					
File: block diagram.SchDoc					
Rev v1.0A	Sheet 3 / 11	Drawn by: *			
Size A3	5/29/2022	Checked by: *			
DRAFT		Approved by: *			



POWER



Project:	
File: Power.SchDoc	
Rev v1.0A	Sheet /
Size A4	5/29/2022
DRAFT	
Drawn by: *	
Checked by: *	
Approved by: *	

PROJECT DIAGRAM

Designator
cover.SchDoc

Designator
block_diagram.SchDoc

- Kiểm tra change log trước khi start project
- Đóng gói Project (Project Packager) trước khi tăng phiên bản project hoặc mỗi khi release cho sản xuất.

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for
informational
design notes.

DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

TEMPLATE NOTES

Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Company, Project and Version/Revision

Mark Not Fitted Components as

NF

Net Class Example



Differential signal example



TITLE Examples (You can change the color to reflect your company color)

PAGE TITLE

Peripheral / Group of component title

Smaller Title

Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

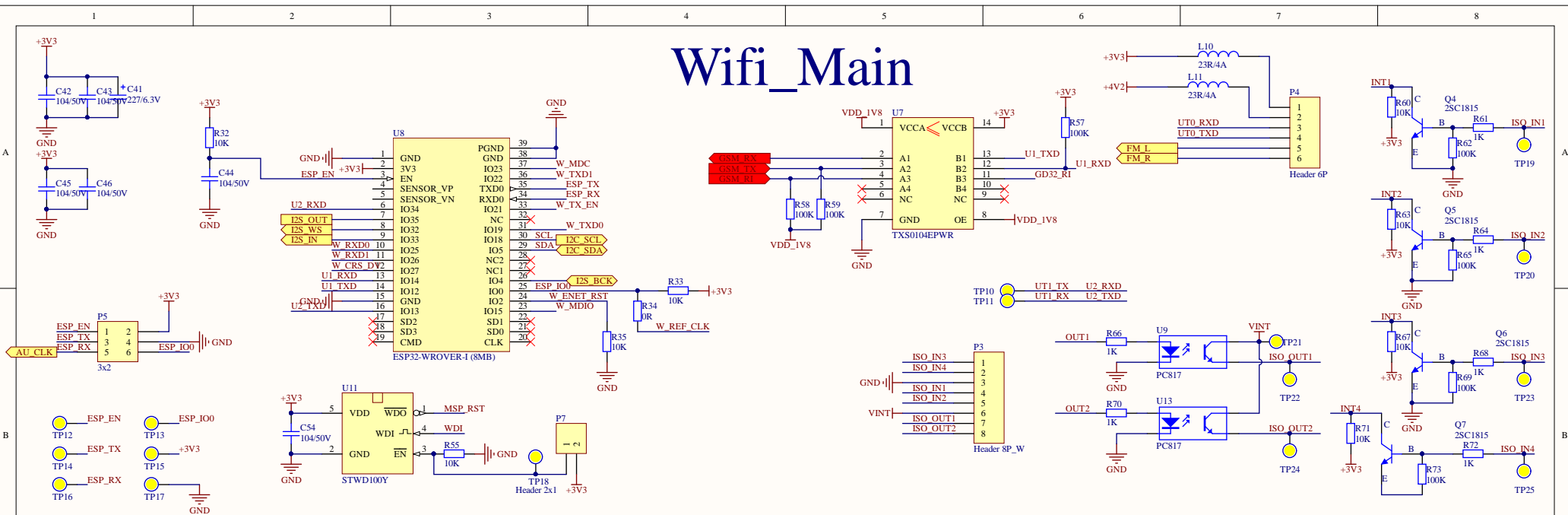
PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

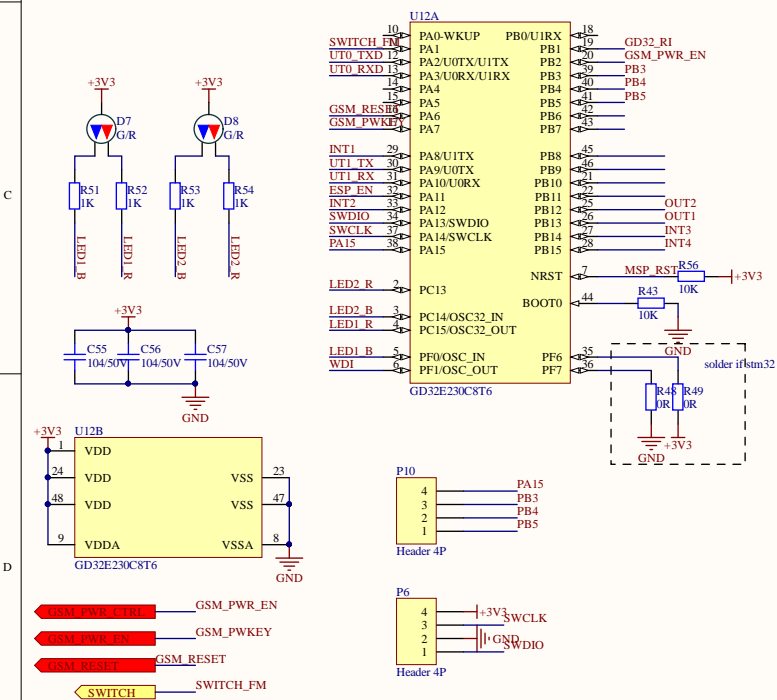
RELEASED - A board with this schematic has been sent to production.

Project:					
File: root.SchDoc					
Rev	v1.0A	Sheet	1 / 11	Drawn by:	BienNB
Size	A2	5/29/2022		Checked by:	---
DRAFT				Approved by:	---

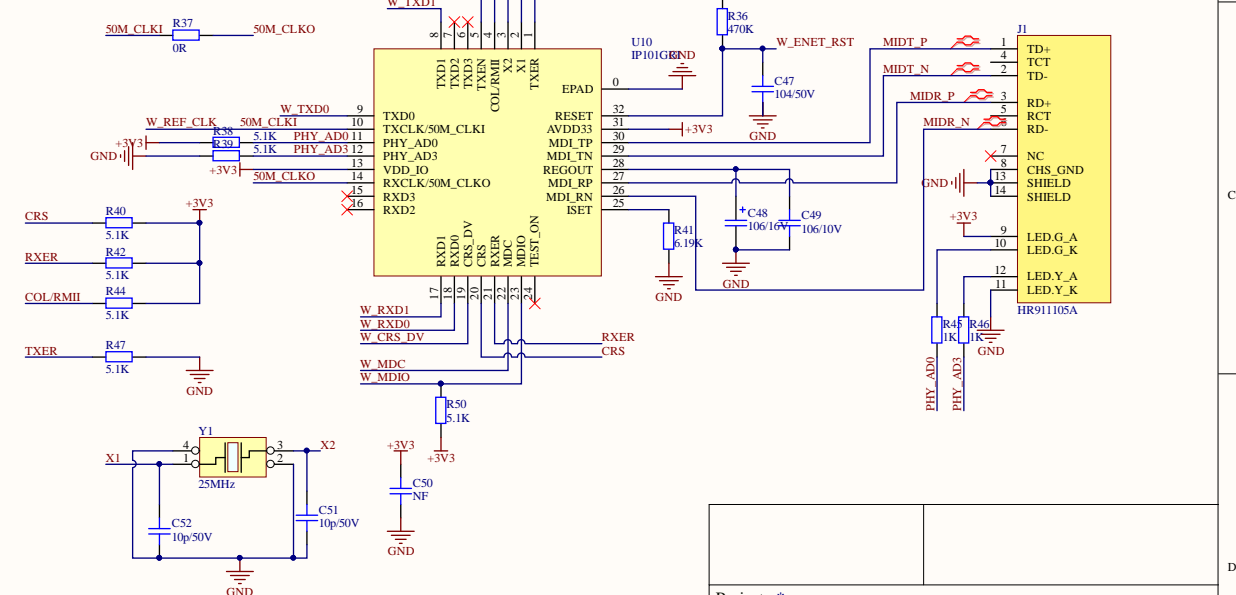
Wifi_Main



IO EXPEND



ETHERNET



Project: *			
File: Wifi.SchDoc			
Rev v1.0A	Sheet 1 / *	Drawn by: TueTD	
Size A3	5/29/2022	Checked by: *	
DRAFT		Approved by: *	