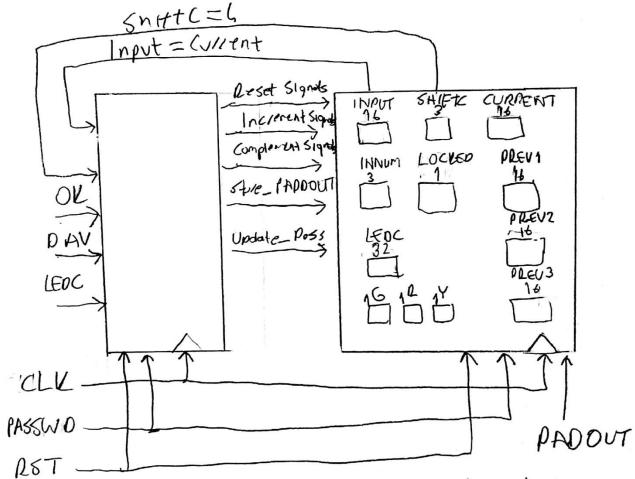
1. Block Diagram:



Input: Register to store the output of keypod

Cullent: Cullent possessord

Preview Volves to check

Innum, ShiftC, LeolC: Countris for the control system

ShiftC LeolC: Countris for the control system

ShiftC LeolC: Countris for the control system

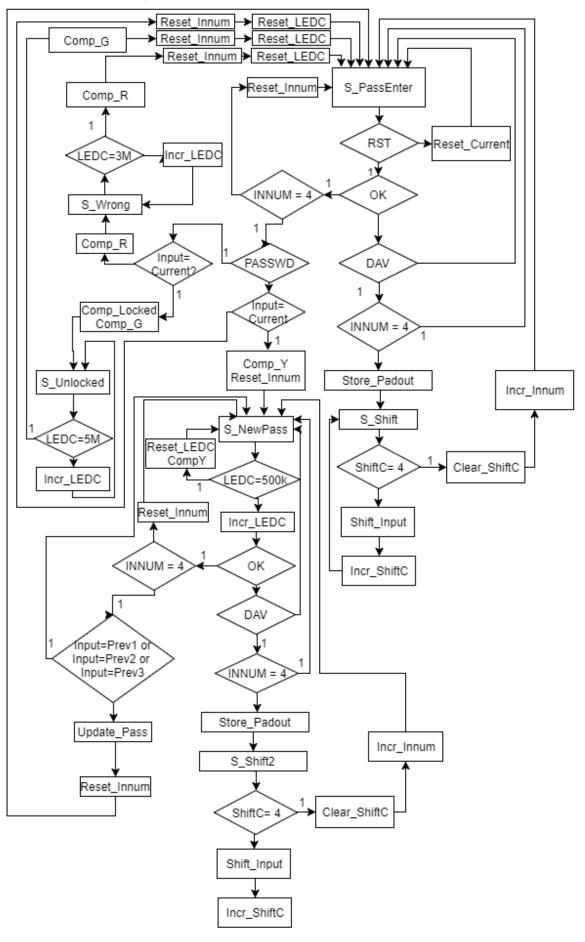
ShiftC=4, Input=Cullent: Comparison signals to moke decisions

LEO Count comparisons were omitted.

All signals of a confequery are snown together to

avoid clutter.

3: ASM Chart of the system

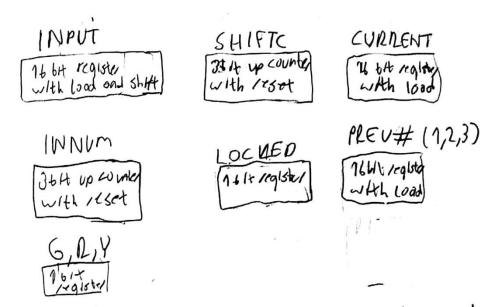


State T	ه الم	e:	6	105	ן עם	Possud	VAQ	ا۱۱۷۱ میمیلم	SHIFTC=4	Input = Culled	Next Stofe Gr B1 Go
5- POSSENIEL	0	0	0	1	X	X	X	X	×	, Х	000
S_ Poss Entry	0	0	-	10	1	0.	()X7	0	×	1	0 0 0
5_PossEnter	0	0	0	0	1	0	X	1	X	1	000
5 - PossEnter	0	0	0	0	1	1	X	0	X	1	000
5_ Ross Enter	6	0,	6	O	1	1	X	1	X	4	7 000
5_ POSSEM	0	0	0	O	0	X	O	X	X	X	0 10 0
5_PossEnter	0	0	0	0	0	X	1	0	X	X	0111
5- Pass Enter	0	0	0	0	0	X	1	1	X	X	0 0 00
5_ PossEnter		0	0	0	1	0	X	X	X	0	000
S_ PassEnter	0	O	O	0	1	1	X	X	χ.	0	000
5_ Unlocked	0	0	1	X	人	X	×	X	X	X	000
5_Wrong	0	1	0	X	X	×	X	X	X	X	000
Sushitt	0	1	1	X	X	X	×	×	0	X	011
5-5414	C) 1	1	X	×	Х.	X	X	1	X	000
S_NewPoss	1	0	0	X	0	X	0	X	×	X	700
5 - New Poss	1	O	O	X	0	X	1	0	X	X	1 0 1
5- New Poss	1	0	0	X	0	1 X	1	1	×	X	100
5_ New Poss	1	0	0	X	1	×		Ō	X	X	700
5_ New Poss	1	0	0	X	1	X		7	X	X	101
5-541ft 2	1	0	1	X	X	X	×	×	0	×	101
5-8h1++2	7	0	1	X	×	×	×	X	1 1	X	100

LEDC signals were omtted, they only effect the timing of the system. The electron paths all traction the some witout them.

Implementation of system;

The system for controller part is very complex to alrow by hand so, only datapath is gluen.



The signal connections are also rother complex so please thethe flow chook and source code to indiretion what is connected to what, please also note that, there are '6 comparators to produce that, there are '6 comparators to produce the control signal "Input = Current "linnum=4" the control signal "LEDC = X" signals.

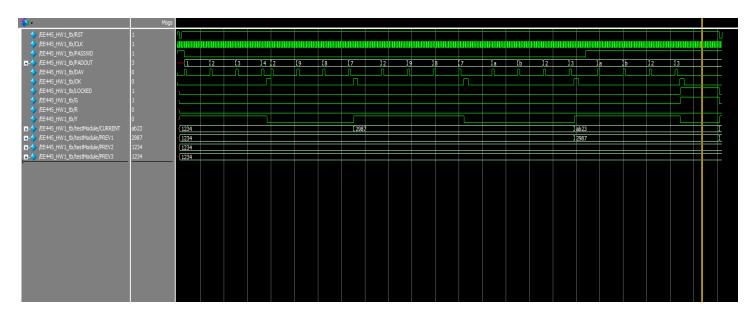
Implementation Details:

On the Pass Enter state the system waits for inputs from the keypad and when a button is pressed number of inputs after the last okay is check and if the number is less than 4 the new input is stored on INPUT register. If the number is more than 4, the input is ignored. After an input is stored on the register the system goes to Shift state and shifts the input register left 4 times to make space for the next input. The shifting is not done for the 4th input since it is already in place.

When OK button is pressed if less than 4 inputs came to system since the last OK the input is ignored, and the system stays in the Pass Enter state to wait for the next input. If the INPUT is not the same as CURRENT password if PASSWD switch is off, then the system goes to Wrong state. If PASSWD switch is on the input is just ignored. If the input is correct PASSWD switch is checked and the state is changed to New Pass if the switch is on and Correct state if it is off.

On the Correct and Wrong states, the corresponding LED is lit for the amount noted and then the system returns to Pass Enter. If the system is on the New Pass state then a new 4 digit input is waited like before and if it is correct the INPUT is compared with previous passwords and if it differs it is set to CURRENT and the previous inputs are updated.

Simulation results:



Reset signal is sent to initialize the system. Password switch is on to enter the new password.

The old password is entered first (1234) and then new password is entered (2987)

Please note that since Y should blink every 0.5 seconds but when the simulation time exceeds second it takes too much time to wait for the waveforms to show so we see Y as low only during password change but after the password is entered we observe the change pack to Y=1.

After the password change we keep password switch on and enter another password to observe the changes on the previous password values. After doing the same steps as above we clearly see the change on the CURRENT value (2987 to AB23) and PREV takes the old value of CURRENT (2987). The password switch is back off and the system returns to usual operation.

When the correct password is entered LOCKED becomes 1 to indicate the lock opens and G becomes 1 while Y becomes 0.