

# Lab 05

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1. Read through the assignment handout carefully and make sure you understand the requirements. Ask your lab demonstrator, or post to the discussion board if you have any questions.
2. Examine the first stage of the assignment:
  - 2.1. List the major components you believe you will need to build to achieve this.

**Components: JK Flip Flop, AND Gate, Pin, Clock, D Flip Flop**

- 2.2. What resources (lecture slides, previous labs, online links etc) do you have that are relevant to these components?

## Common Clock Lecture

3. Start sketching your solution - either free-hand drawings or Logisim circuits are fine. You might want to focus on specific components and consider their design in isolation. Discuss with your lab mates, and your demonstrator (but remember your work has to be your own!)

I have finished Step 1 in the assignment

