

## **IO-Link Block AOI Test Report**

Date of Test:March	21, 2024
Name of Test Engineer:	Jose Sonchez
<b>Product Description of Test</b>	ted Product: TBEN-L4-810L + A01
Product Firmware Rev:	3.4.2.0
PLC used to Test Product:	1769 - L30 EBMS
Version of StudioLogix used	d to Test Product: V32 (Mini)
Name of AOI File Tested:	TBEW-Lx-810L-A01-V1-1.LGX
Version of AOI File Tested:	1.1

**Test Notes:** 

Generic ADI does not work K -In words not transferring From Common data to ADI in words.

Test Passed: Yes / No



## **Test Plan**

Import L5K into Studio, create catalog ACD, verify all IO-Link Blocks are present in project.
Create Blank ACD Project for your Test PLC of choice.
Drag over IO-Link Block Device and corresponding Block AOI from Catalog to Project
☑ Verify AOI Version matches Rev file and Revision Notes from txt file in github.
☐ Verify AOI Description matches txt file in github.
Configure IP Address of local block.
☑ Bring copy of 8IOL AOI into Logic
Create tag instances of AOI, Common Data, Read MSG, and Write MSG
Fie TBEN I, O, and C Data to AOI (Charge the order in AOI) I/O/C to C/# 10
Setup SEND MSG instructions with correct PATH, Service Code 4b, Class 67, Send and
Receive Arrays to Common Data Send/Receive Arrays
Setup Write MSG instructions the same but change Class to 68 4 Service code 40%
Download Program to PLC (Power Cycle block ?)
Check all DIs with signal (Note: All IO-Link Pins will not show LED but will show change of
Signal state, this is because IO-Link is still active just not throwing a diagnostic error)  Dr.
Plug IO-Link device into each port in turn and verify the right IOLDevice Output turns on and correct VID/DID shows up in the Common Data. Checking the VID/DID verifies the
Read MSG was setup and working correctly.
✓ Trigger each IO-Link Port to DI setting and make sure the correct port is goes to DI.
Inigger each DXP Disable Setting and make sure the correct port is disabled.
Import Universal Device Info and point it at a port that has a device.
Write the App Tag to the device. This tests the Write MSG is working correctly.
output. This will verify the Port Process data mapping.
Genera AOZ not working properly.