

EEE 302: Microprocessors & Interfacing
Semester: Spring 2022
Course Instructor: FMA
Date: March 20, 2022
Section-1
QUIZ-2

Time: 25 minutes

Total Marks: 20

Answer all the questions. Do not use notes, books and mobiles

1. Suppose, we have the following values: [6 x 2=12 marks] [CO2/APPLY]

For registers,

[DS] = 2A00h [ES] = 23B0h [CS] = 48D0h [SS] = 38C0h

[BX] = 089Ah [SI] = 05ADh

[DI] = 09ECh [BP] = 1BA4h.

Generate 20-bit physical address for the following commands and mention if it is aligned or misaligned data transfer for each case.

- (a) MOV CL, 0Eh [BX+SI]
- (b) MOV DX, 32Eh [BP]
- (c) MOV AX, CS: [BP] [SI] 11h
- (d) MOV [BX+DI], AX
- (e) MOV DX, SS: [BX] 1033h
- (f) MOV AX, 342h [SI]

2. Mention the reasons of the following invalid commands: [CO2/APPLY]

[4 x 2 = 8 marks]

- a) MOV AX, [CX]
- b) MOV SS, DS
- c) MOV AX, 10000h
- d) MOV [0902h], [BP]