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| **East West University**  **Department of EEE**  **EEE 302: Microprocessors & Interfacing**  **Semester: Summer 2022**  **Course Instructor: FMA**  **Date: June 26, 2022**  **Section-1**  **QUIZ-1** | |  | | --- | |  | |

**Time: 25 minutes Total Marks: 40**

Answer all the questions. Do not use notes, books, and mobiles

1. In the following diagram, you have the internal architecture of the Intel 8086 microprocessor. Answer the following questions refer to this diagram.

Diagram

Description automatically generated

1. What is the difference between memory segments and segment registers?
2. Why is the address generation circuit located in the bus interface unit? Justify your answer.
3. Why do Intel 8086 generate 20-bit physical address before fetching instructions from memory?
4. What can be stored in memory by the Intel 8086 microprocessor? Break down the following in the power of 2: 64MB, 32GB.