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| **East West University**  **Department of EEE**  **EEE 302: Microprocessors & Interfacing**  **Semester: Spring 2022**  **Quiz-3** | |  | | --- | |  | |

1. In the following diagram, you have the **minimum mode** configuration. Explain the generation of control signals preferably via a truth table. What will happen to the unexploited pins for IC 74138?

