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| **East West University**  **Department of EEE**  **EEE 302: Microprocessors & Interfacing**  **Semester: Fall 2021**  **Course Instructor: FMA**  **Date: November 7, 2021**  **Section-1**  **QUIZ-1** | |  | | --- | |  | |

**Time: 30 minutes Total Marks: 40**

Answer all the questions. Do not use notes, books, and mobiles

1. In the following diagram, you have the internal architecture of the Intel 8086 microprocessor. Answer the following questions refer to this diagram.

Diagram

Description automatically generated

1. Define register. How many registers are there in the BIU unit?
2. What is 6- byte prefetch queue? How does it enhance the total system performance?
3. Why is the address generation circuit located in the bus interface unit? Justify your answer.
4. What can be stored in memory by the Intel 8086 microprocessor? Break down the following in the power of 2: 64MB, 16KB, 32GB.