

VHDL 7: WRITE A VHDL CODE FOR MULTIPLEXER.

VHDL 8: WRITE A VHDL CODE FOR DECODER.

The screenshot shows the EDA playground interface with the following details:

- Top Bar:** playground, New, Run, Save*, KnowHow WEBINARS, AXI based Design & Verification, FREE • 1 Hour, NOV 7, 2025, REGISTER NOW.
- Left Sidebar:** Bring you to by DOULOS, Languages & Libraries (selected), Testbench + Design (selected), VHDL, Libraries (None, OVL, SV), Top entity (tb_Decoder2to4), Enable VUnit (unchecked), Tools & Simulators (GHDLS 3.0.0, Import Options, Make Options, Run Options, Simulator Options, Simulator Shell (unchecked)), Use run.bash shell script (unchecked), Open EPwave after run (checked), Show output file after run (unchecked), Download files after run (unchecked).
- Middle Left Panel:** testbench.vhd (content: VHDL code for tb_Decoder2to4 testbench).
- Middle Right Panel:** design.vhd (content: VHDL code for Decoder2to4 entity).
- Bottom Panel:** Log (content: EPwave log output for running ghd1 -m tb_Decoder2to4 --vcd=dump.vcd, showing simulation results for A=0, B=0 to A=1, B=1, and finding dump.vcd).

