Christian Bush, Spencer Callicott, Will Carroll,
Landon Casey, Jack Fletcher

Schematic Errata

Component R6 needs a jumper connection from the signal side to LCD V0 or opposing pin of R7. There is no connection from R6 to R7 as evidenced by a continuity test. This connection should be a junction between R6, R7 and the LCD V0 pin.

SDA and SCL need a jumper connection to the microcontroller. They are not currently connected to anything other than the pullup resistors.

A jumper needs to be wired from MCLR (pin 1) on the serial header, to RTS# (pin 6) on the FTDI header.