编译原理第一次实验测试用例:目录

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1 A组测试用例

本组测试用例共9个,每个仅包含单个的词法或者语法错误。除特殊说明外,不可多报。多报、漏报错误,或者打印语法树都会导致扣分。错误编号和行号之后的说明文字不要求与给出的输出完全一致,仅供助教理解使用,不作为评分依据。

1.1 A-1

输入

```
struct 1Person {
   int id;
};

int main() {
   return get_id(x);
}
```

输出

```
Error type B at Line 1: Illegal identifier "1Persion"
```

说明:错误类型也可以是 A 类,或者一个 A 一个 B,但是只能在第 1 行。这里有一个非法标识符 1_Person。

1.2 A-2

输入

```
struct if {
   int cond;
};

int abc() {
   return 123;
}
```

输出

```
Error type B at Line 1: Syntax error
```

说明:关键词if不可以作为标识符。

1.3 A-3

输入

```
int a, a, a;
float a;

struct a {};

int a = 1;

int a() {}
```

输出

```
Error type B at Line 6: Cannot initialize global variable "a"
```

说明:全局变量的声明不可以初始化。

1.4 A-4

输入

```
int main() {

struct A a[10];

a[0].a = 13;

a[1].b = d;

int b = a;

return c;

}
```

输出

```
Error type B at Line 5: Illegal declaration
```

说明:变量声明只允许出现在语句块头部。

1.5 A-5

输入

```
int a;
int;

struct;
struct A;

float b() {
   return 1.0;
}
```

输出

```
Error type B at Line 4: Unexpected ";"
```

说明:第4行缺少结构体名。

1.6 A-6

输入

```
int main() {
  float a = 1.3, b = 0.000000003, c = .3;
  int b = a, c = b, s;
  return 0.4;
}
```

输出

```
Error type A at Line 2: Illegal FLOAT ".3"
```

说明: ".3"不是合法的浮点数(附录要求浮点数的小数点前后必须出现数字),这里也可以 报成语法错误。

1.7 A-7

```
int BEIJING;
  int NEWYORK;
  int MOSCOW;
  struct WorldMap {
    struct {
      int capital;
      float lat, lon, hei;
8
    } countries[1000];
  } ;
10
  int main() {
12
     struct WorldMap map;
13
    BEIJING = 1;
14
    NEWYORK = 2;
    MOSCOW = 3;
16
    map.countries[1].capital = BEIJING;
17
    map.countries[2].capital = NEWYORK;
18
    map.countries[3].capital = BEIJING, MOSCOW;
     return map;
20
  }
21
```

```
Error type B at Line 19: Unexpected ","
```

说明: C-不允许使用","操作符。

1.8 A-8

```
struct A {} a[100];
struct B {};
```

```
int eq(struct A a, struct B b) {
    return a == b;
6
  int main() {
    struct B b[100];
9
    if (eq(A, B)) {
10
      return a[1];
11
    } else
12
      return b[200 * 30];
13
    return A[d[b[(1 * ((5) + (b - a))) / 3 + a[A]]] * d.b[]];
15
```

```
Error type B at Line 14: Empty dimension
```

说明: 第14行,数组使用d.b[]缺少维度。

1.9 A-9

```
int main()
2
       int i = 0, j, t, a[5];
      while(i < 5)
       {
5
          a[i] = read();
          i = i + 1;
       i = 0;
9
      while(i < 4)
10
          j = i + 1;
12
          while (j < 5)
13
```

```
{
14
                 if(a[i] > a[j])
16
                      int d[2.2];
17
                      t = a[i];
18
                      a[i] = a[j];
                      a[j] = t;
20
21
                 j = j + 1;
22
23
            }
            i = i + 1;
25
        i = 0;
26
       while(i < 5)
27
28
            write(a[i]);
29
            i = i + 1;
30
31
        return 0;
32
33
```

Error type B at Line 17: Definition of an array only accepts an INT dimension

说明: 第17行数组维度不可使用浮点数。

2 B组测试用例

本组测试用例共2个,每个用例包含多处不同的错误。除特殊说明外,漏报、多报错误或者打印语法树都会导致扣分。

2.1 B-1

```
int change(int m) {
     int n[2] = m;
2
     int j = 0;
3
     while (j < (n) {
      n = n - 2;
       j = j + 1;
     return n;
8
9
10
  int main() {
     int a[5];
12
     int i = 0;
13
     struct A { int a; };
14
     a[i] = 100;
     while (i < 4) {
16
       i = i + 1;
17
       a[i] = change(a[i-1]);
18
       write(a[i]);
20
     return 0;
21
22
```

```
Error type B at Line 4: Missing ")"

Error type B at Line 14: Unexpected ";"
```

说明:第4行括号不匹配,第14行结构体定义后缺少变量名(注意,语句块内定义的结构体需立即使用)。

2.2 B-2

```
int big(int x, int y) {
    if (x >= y ) return 0;
2
    return 1;
  int main(){
    int a[2], b[2], c[4.4];
    int ia, ib, ic;
8
    ia = 0;
    while (ia < 2) {
10
      a[ia] = read();
     ia = ia + 1;
12
13
    ib = 0;
14
    while (ib < 2) {
15
     b[ib] = read();
16
     ib = ib + 1;
17
18
    int a[4];
     ic = 0;
20
     ia = 0;
21
     ib = 0;
22
    while (ic < = 4) {
23
      if (ia >= 2) {
24
        c[ic] = b[ib];
25
        ib = ib + 1;
26
       } else if (ib >= 2) {
        c[ic] = a[ia];
         ia = ia + 1;
29
       } else if (big(a[ia], b[ib])==0) {
30
         c[ic] = a[ia];
31
         ia = ia + 1;
```

```
} else {
33
         c[ic] = b[ib];
34
         ib = ib + 1;
35
       }
36
       ic = ic + 1;
37
     }
     ic = 0
     while (ic < 4) {
40
       write(c[ic]);
41
       ic = ic + 1;
42
     return 0;
44
45
```

```
Error type B at Line 7: Defition of an array only accepts an INT dimension

Error type B at Line 19: Illegal declaration

Error type B at Line 23: Unexpected "="

Error type B at Line 39: Missing ";"
```

说明: 第7行维度不允许浮点数; 第19行变量声明仅允许出现在语句块头部; 第23行< =多了一个空格; 第39行缺少分号。

3 C组测试用例

本组测试用例共2个,不包含任何错误,需要输出正确的语法树。除特殊说明外,应与给出的语法树完全相同。语法树打印错误酌情扣分。

3.1 C-1

```
int look_and_say(int x) {
  int _1[32], i = 0;
```

```
int 2[64], j = 0, k = 0;
3
     int count = 0;
4
5
     if (x < 0) {
6
     x = ---x;
7
     }
     i = 0;
10
     while (x > 0) {
11
      _{1[i]} = lsd(x);
12
      x = x / 10;
13
     i = i + 1;
14
     }
15
16
     i = i - 1;
17
     j = 0;
18
     while (i >= 0) {
19
       if (i == 0) {
20
         _{2[j]} = 1;
21
         _{2[j + 1]} = _{1[0]};
22
         j = j + 2;
23
         i = -1;
24
       } else {
25
         count = 1;
26
         while (i > 0 && _1[i - 1] == _1[i]) {
27
          count = count + 1;
28
          i = i - 1;
29
         }
30
         2[j] = count;
31
         2[j + 1] = 1[i];
32
         i = i - 1;
33
         j = j + 2;
```

```
}
35
     }
37
     k = 0;
38
     while (k < j) {
39
       write(_2[k]);
40
       k = k + 1;
41
42
43
     return 0;
44
```

```
Program (1)
     ExtDefList (1)
2
       ExtDef (1)
3
         Specifier (1)
            TYPE: int
5
         FunDec (1)
6
            ID: look_and_say
           LΡ
           VarList (1)
9
              ParamDec (1)
10
                Specifier (1)
11
                  TYPE: int
12
                VarDec (1)
13
                  ID: x
14
            RP
15
         CompSt (1)
16
            LC
17
            DefList (2)
18
              Def (2)
19
                Specifier (2)
```

```
TYPE: int
21
                DecList (2)
22
                   Dec (2)
23
                    VarDec (2)
24
                       VarDec (2)
25
                       ID: _1
                      LB
27
                       INT: 32
28
                      RB
29
                  COMMA
30
                   DecList (2)
31
                     Dec (2)
32
                      VarDec (2)
33
                         ID: i
34
                      ASSIGNOP
35
                      Exp (2)
36
                        INT: 0
37
                SEMI
38
              DefList (3)
                Def (3)
40
                  Specifier (3)
41
                    TYPE: int
42
                  DecList (3)
43
                     Dec (3)
44
                       VarDec (3)
45
                         VarDec (3)
46
                          ID: _2
47
                         LB
48
                         INT: 64
49
                         RB
50
                     COMMA
51
                     DecList (3)
```

```
Dec (3)
53
                          VarDec (3)
54
                            ID: j
55
                          ASSIGNOP
56
                          Exp (3)
57
                           INT: 0
                       COMMA
                        DecList (3)
60
                          Dec (3)
61
                            VarDec (3)
62
                              ID: k
63
                            ASSIGNOP
64
                            Exp (3)
65
                              INT: 0
66
                   SEMI
67
                 DefList (4)
68
                   Def (4)
69
                     Specifier (4)
70
                       TYPE: int
                     DecList (4)
72
                       Dec (4)
73
                         VarDec (4)
74
                            ID: count
75
                          ASSIGNOP
76
                         Exp (4)
77
                            INT: 0
78
                     SEMI
            StmtList (6)
80
              Stmt (6)
81
                 ΙF
82
                 LP
83
                Exp (6)
```

```
Exp (6)
85
                      ID: x
                    RELOP
87
                    Exp (6)
88
                      INT: 0
89
                  RP
                  Stmt (6)
                    CompSt (6)
92
                       LC
93
                       StmtList (7)
94
                         Stmt (7)
                            Exp (7)
96
                              Exp (7)
97
                                ID: x
98
                              ASSIGNOP
                              Exp (7)
100
                                MINUS
101
                                Exp (7)
102
                                   MINUS
103
                                   Exp (7)
104
                                     MINUS
105
                                     Exp (7)
106
                                       ID: x
107
                            SEMI
108
                       RC
109
               StmtList (10)
110
                  Stmt (10)
111
                    Exp (10)
112
                      Exp (10)
113
                        ID: i
114
                       ASSIGNOP
115
                       Exp (10)
```

116

| 117 | INT: 0 | |
|-----|---------------|--|
| 118 | SEMI | |
| 119 | StmtList (11) | |
| 120 | Stmt (11) | |
| 121 | WHILE | |
| 122 | LP | |
| 123 | Exp (11) | |
| 124 | Exp (11) | |
| 125 | ID: x | |
| 126 | RELOP | |
| 127 | Exp (11) | |
| 128 | INT: 0 | |
| 129 | RP | |
| 130 | Stmt (11) | |
| 131 | CompSt (11) | |
| 132 | LC | |
| 133 | StmtList (12) | |
| 134 | Stmt (12) | |
| 135 | Exp (12) | |
| 136 | Exp (12) | |
| 137 | Exp (12) | |
| 138 | ID: _1 | |
| 139 | LB | |
| 140 | Exp (12) | |
| 141 | ID: i | |
| 142 | RB | |
| 143 | ASSIGNOP | |
| 144 | Exp (12) | |
| 145 | ID: lsd | |
| 146 | LP | |
| 147 | Args (12) | |
| 148 | Exp (12) | |

| 149 | ID: x |
|-----|---------------|
| 150 | RP |
| 151 | SEMI |
| 152 | StmtList (13) |
| 153 | Stmt (13) |
| 154 | Exp (13) |
| 155 | Exp (13) |
| 156 | ID: x |
| 157 | ASSIGNOP |
| 158 | Exp (13) |
| 159 | Exp (13) |
| 160 | ID: x |
| 161 | DIV |
| 162 | Exp (13) |
| 163 | INT: 10 |
| 164 | SEMI |
| 165 | StmtList (14) |
| 166 | Stmt (14) |
| 167 | Exp (14) |
| 168 | Exp (14) |
| 169 | ID: i |
| 170 | ASSIGNOP |
| 171 | Exp (14) |
| 172 | Exp (14) |
| 173 | ID: i |
| 174 | PLUS |
| 175 | Exp (14) |
| 176 | INT: 1 |
| 177 | SEMI |
| 178 | RC |
| 179 | StmtList (17) |
| 180 | Stmt (17) |

| EXP (17) EXP (18) EXP | | |
|--|-----|---------------|
| TD: i ASSIGNOP Exp (17) Exp (17) Exp (17) TD: i MINUS Exp (17) INT: 1 SEMI SEMI StmtList (18) Exp (18) Exp (18) Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI StmtList (19) WHILE LP Exp (19) | 181 | Exp (17) |
| ASSIGNOP Exp (17) Exp (17) ID: i MINUS Exp (17) INT: 1 SEMI SEMI StmtList (18) Exp (19) | 182 | Exp (17) |
| Exp (17) Exp (18) Exp (19) Exp (19) Exp (19) | 183 | ID: i |
| Exp (17) ID: i ID: i MINUS Exp (17) INT: 1 SEMI SEMI StmtList (18) Stmt (18) Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI StmtList (19) SEMI StmtList (19) SEMI SEMI SEMI SEMI Exp (18) | 184 | ASSIGNOP |
| ID: i MINUS Exp (17) INT: 1 SEMI SEMI StmtList (18) Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI SEMI SEMI SEMI LY SEMI WHILE LP Exp (19) | 185 | Exp (17) |
| MINUS Exp (17) INT: 1 INT: 1 SEMI StmtList (18) Stmt (18) Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI StmtList (19) StmtList (19) WHILE LP Exp (19) | 186 | Exp (17) |
| Exp (17) INT: 1 SEMI SEMI StmtList (18) Exp (18) Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI SEMI StmtList (19) StmtList (19) WHILE LP Exp (19) | 187 | ID: i |
| INT: 1 SEMI SEMI StmtList (18) Stmt (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI SEMI Stmt (19) Exp (19) Exp (19) | 188 | MINUS |
| SEMI SEMI StmtList (18) Stmt (18) Stmt (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI SEMI SEMI SUBJECT OF SEMISOR OF SE | 189 | Exp (17) |
| StmtList (18) Stmt (18) Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI StmtList (19) StmtList (19) WHILE LP Exp (19) | 190 | INT: 1 |
| Stmt (18) Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) INT: 0 SEMI StmtList (19) Stmt (19) WHILE LP Exp (19) | 191 | SEMI |
| Exp (18) Exp (18) Exp (18) ID: j ASSIGNOP Exp (18) Exp (18) INT: 0 SEMI StmtList (19) Stmt (19) WHILE LP Exp (19) | 192 | StmtList (18) |
| Exp (18) ID: j ASSIGNOP Exp (18) Exp (18) INT: 0 SEMI StmtList (19) Stmt (19) WHILE LP Exp (19) | 193 | Stmt (18) |
| ID: j ASSIGNOP Exp (18) INT: 0 SEMI StmtList (19) Stmt (19) WHILE LP Exp (19) | 194 | Exp (18) |
| ASSIGNOP Exp (18) INT: 0 SEMI SIMI StmtList (19) Stmt (19) WHILE LP Exp (19) | 195 | Exp (18) |
| Exp (18) INT: 0 SEMI StmtList (19) Stmt (19) WHILE LP Exp (18) Exp (18) INT: 0 Exp (19) | 196 | ID: j |
| INT: 0 SEMI StmtList (19) Stmt (19) WHILE LP Exp (19) | 197 | ASSIGNOP |
| 200 SEMI 201 StmtList (19) 202 Stmt (19) 203 WHILE 204 LP 205 Exp (19) | 198 | Exp (18) |
| 201 StmtList (19) 202 Stmt (19) 203 WHILE 204 LP 205 Exp (19) | 199 | INT: 0 |
| 202 Stmt (19) 203 WHILE 204 LP 205 Exp (19) | 200 | SEMI |
| 203 WHILE 204 LP 205 Exp (19) | 201 | StmtList (19) |
| 204 LP 205 Exp (19) | 202 | Stmt (19) |
| 205 Exp (19) | 203 | WHILE |
| | 204 | LP |
| 206 Exp (19) | 205 | Exp (19) |
| | 206 | Exp (19) |
| 207 ID: i | 207 | ID: i |
| 208 RELOP | 208 | RELOP |
| 209 Exp (19) | 209 | Exp (19) |
| 210 INT: 0 | 210 | INT: 0 |
| 211 RP | 211 | RP |
| 212 Stmt (19) | 212 | Stmt (19) |

| 213 | CompSt (19) |
|-----|---------------|
| 214 | LC |
| 215 | StmtList (20) |
| 216 | Stmt (20) |
| 217 | IF |
| 218 | LP |
| 219 | Exp (20) |
| 220 | Exp (20) |
| 221 | ID: i |
| 222 | RELOP |
| 223 | Exp (20) |
| 224 | INT: 0 |
| 225 | RP |
| 226 | Stmt (20) |
| 227 | CompSt (20) |
| 228 | LC |
| 229 | StmtList (21) |
| 230 | Stmt (21) |
| 231 | Exp (21) |
| 232 | Exp (21) |
| 233 | Exp (21) |
| 234 | ID: _2 |
| 235 | LB |
| 236 | Exp (21) |
| 237 | ID: j |
| 238 | RB |
| 239 | ASSIGNOP |
| 240 | Exp (21) |
| 241 | INT: 1 |
| 242 | SEMI |
| 243 | StmtList (22) |
| 244 | Stmt (22) |

| 245 | Exp (22) |
|-----|---------------|
| 246 | Exp (22) |
| 247 | Exp (22) |
| 248 | ID: _2 |
| 249 | LB |
| 250 | Exp (22) |
| 251 | Exp (22) |
| 252 | ID: j |
| 253 | PLUS |
| 254 | Exp (22) |
| 255 | INT: 1 |
| 256 | RB |
| 257 | ASSIGNOP |
| 258 | Exp (22) |
| 259 | Exp (22) |
| 260 | ID: _1 |
| 261 | LB |
| 262 | Exp (22) |
| 263 | INT: 0 |
| 264 | RB |
| 265 | SEMI |
| 266 | StmtList (23) |
| 267 | Stmt (23) |
| 268 | Exp (23) |
| 269 | Exp (23) |
| 270 | ID: j |
| 271 | ASSIGNOP |
| 272 | Exp (23) |
| 273 | Exp (23) |
| 274 | ID: j |
| 275 | PLUS |
| 276 | Exp (23) |

| 277 | INT: 2 |
|-----|---------------|
| 278 | SEMI |
| 279 | StmtList (24) |
| 280 | Stmt (24) |
| 281 | Exp (24) |
| 282 | Exp (24) |
| 283 | ID: i |
| 284 | ASSIGNOP |
| 285 | Exp (24) |
| 286 | MINUS |
| 287 | Exp (24) |
| 288 | INT: 1 |
| 289 | SEMI |
| 290 | RC |
| 291 | ELSE |
| 292 | Stmt (25) |
| 293 | CompSt (25) |
| 294 | LC |
| 295 | StmtList (26) |
| 296 | Stmt (26) |
| 297 | Exp (26) |
| 298 | Exp (26) |
| 299 | ID: count |
| 300 | ASSIGNOP |
| 301 | Exp (26) |
| 302 | INT: 1 |
| 303 | SEMI |
| 304 | StmtList (27) |
| 305 | Stmt (27) |
| 306 | WHILE |
| 307 | LP |
| 308 | Exp (27) |

| 309 | Exp (27) |
|-----|---------------|
| 310 | Exp (27) |
| 311 | ID: i |
| 312 | RELOP |
| 313 | Exp (27) |
| 314 | INT: 0 |
| 315 | AND |
| 316 | Exp (27) |
| 317 | Exp (27) |
| 318 | Exp (27) |
| 319 | ID: _1 |
| 320 | LB |
| 321 | Exp (27) |
| 322 | Exp (27) |
| 323 | ID: i |
| 324 | MINUS |
| 325 | Exp (27) |
| 326 | INT: 1 |
| 327 | RB |
| 328 | RELOP |
| 329 | Exp (27) |
| 330 | Exp (27) |
| 331 | ID: _1 |
| 332 | LB |
| 333 | Exp (27) |
| 334 | ID: i |
| 335 | RB |
| 336 | RP |
| 337 | Stmt (27) |
| 338 | CompSt (27) |
| 339 | LC |
| 340 | StmtList (28) |

| 341 | Stmt (28) |
|-----|---------------|
| 342 | Exp (28) |
| 343 | Exp (28) |
| 344 | ID: count |
| 345 | ASSIGNOP |
| 346 | Exp (28) |
| 347 | Exp (28) |
| 348 | ID: count |
| 349 | PLUS |
| 350 | Exp (28) |
| 351 | INT: 1 |
| 352 | SEMI |
| 353 | StmtList (29) |
| 354 | Stmt (29) |
| 355 | Exp (29) |
| 356 | Exp (29) |
| 357 | ID: i |
| 358 | ASSIGNOP |
| 359 | Exp (29) |
| 360 | Exp (29) |
| 361 | ID: i |
| 362 | MINUS |
| 363 | Exp (29) |
| 364 | INT: 1 |
| 365 | SEMI |
| 366 | RC |
| 367 | StmtList (31) |
| 368 | Stmt (31) |
| 369 | Exp (31) |
| 370 | Exp (31) |
| 371 | Exp (31) |
| 372 | ID: _2 |
| | |

| 373 | LB |
|-----|---------------|
| 374 | Exp (31) |
| 375 | ID: j |
| 376 | RB |
| 377 | ASSIGNOP |
| 378 | Exp (31) |
| 379 | ID: count |
| 380 | SEMI |
| 381 | StmtList (32) |
| 382 | Stmt (32) |
| 383 | Exp (32) |
| 384 | Exp (32) |
| 385 | Exp (32) |
| 386 | ID: _2 |
| 387 | LB |
| 388 | Exp (32) |
| 389 | Exp (32) |
| 390 | ID: j |
| 391 | PLUS |
| 392 | Exp (32) |
| 393 | INT: 1 |
| 394 | RB |
| 395 | ASSIGNOP |
| 396 | Exp (32) |
| 397 | Exp (32) |
| 398 | ID: _1 |
| 399 | LB |
| 400 | Exp (32) |
| 401 | ID: i |
| 402 | RB |
| 403 | SEMI |
| 404 | StmtList (33) |

| 405 | Stmt (33) |
|-----|---------------|
| 406 | Exp (33) |
| 407 | Exp (33) |
| 408 | ID: i |
| 409 | ASSIGNOP |
| 410 | Exp (33) |
| 411 | Exp (33) |
| 412 | ID: i |
| 413 | MINUS |
| 414 | Exp (33) |
| 415 | INT: 1 |
| 416 | SEMI |
| 417 | StmtList (34) |
| 418 | Stmt (34) |
| 419 | Exp (34) |
| 420 | Exp (34) |
| 421 | ID: j |
| 422 | ASSIGNOP |
| 423 | Exp (34) |
| 424 | Exp (34) |
| 425 | ID: j |
| 426 | PLUS |
| 427 | Exp (34) |
| 428 | INT: 2 |
| 429 | SEMI |
| 430 | RC |
| 431 | RC |
| 432 | StmtList (38) |
| 433 | Stmt (38) |
| 434 | Exp (38) |
| 435 | Exp (38) |
| 436 | ID: k |

| 437 | ASSIGNOP |
|-----|---------------|
| 438 | Exp (38) |
| 439 | INT: 0 |
| 440 | SEMI |
| 441 | StmtList (39) |
| 442 | Stmt (39) |
| 443 | WHILE |
| 444 | LP |
| 445 | Exp (39) |
| 446 | Exp (39) |
| 447 | ID: k |
| 448 | RELOP |
| 449 | Exp (39) |
| 450 | ID: j |
| 451 | RP |
| 452 | Stmt (39) |
| 453 | CompSt (39) |
| 454 | LC |
| 455 | StmtList (40) |
| 456 | Stmt (40) |
| 457 | Exp (40) |
| 458 | ID: write |
| 459 | LP |
| 460 | Args (40) |
| 461 | Exp (40) |
| 462 | Exp (40) |
| 463 | ID: _2 |
| 464 | LB |
| 465 | Exp (40) |
| 466 | ID: k |
| 467 | RB |
| 468 | RP |

```
SEMI
469
                                             StmtList (41)
470
                                                Stmt (41)
471
                                                  Exp (41)
472
                                                     Exp (41)
473
                                                        ID: k
474
                                                     ASSIGNOP
475
                                                     Exp (41)
476
                                                        Exp (41)
477
                                                          ID: k
478
                                                        PLUS
                                                        Exp (41)
480
                                                          INT: 1
481
                                                   SEMI
482
                                          RC
483
                                   StmtList (44)
484
                                     Stmt (44)
485
                                        RETURN
486
                                        Exp (44)
                                           INT: 0
488
                                        SEMI
489
              RC
490
```

说明:使用的空格可以用 Tab 替换,注意缩进

3.2 C-2

```
int min = 0, max = intrev32ifbe(is.length)-1, mid = -1;
       struct int64 t cur = -1;
8
       if (intrev32ifbe(is.length) == 0) {
           if (pos) pos = 0;
10
           return 0;
       } else {
12
           if (value > intset_get(is,max)) {
13
                if (pos) pos = intrev32ifbe(is.length);
14
                return 0;
15
           } else if (value < intset get(is,0)) {</pre>
16
                if (pos) pos = 0;
17
                return 0;
18
           }
19
       }
20
21
       while (max >= min) {
22
           mid = shr((min + max), 1);
23
           cur = intset_get(is, mid);
           if (value > cur) {
25
                min = mid + 1;
26
           } else if (value < cur) {</pre>
27
                max = mid-1;
28
           } else {
29
                break;
30
           }
31
       }
33
       if (value == cur) {
34
           if (pos) pos = mid;
35
           return 1;
       } else {
```

```
if (pos) pos = min;
return 0;

40  }
41 }
```

```
Program (1)
     ExtDefList (1)
2
       ExtDef (1)
3
          Specifier (1)
            StructSpecifier (1)
5
              STRUCT
              Tag (1)
                ID: intset
8
         SEMI
       ExtDefList (2)
10
         ExtDef (2)
            Specifier (2)
12
              StructSpecifier (2)
13
                STRUCT
14
                Tag (2)
15
                   ID: uint8_t
16
            SEMI
17
         ExtDefList (3)
18
            ExtDef (3)
              Specifier (3)
20
                StructSpecifier (3)
21
                   STRUCT
22
                   Tag (3)
23
                     ID: int64_t
24
              SEMI
25
            ExtDefList (5)
26
              ExtDef (5)
```

```
Specifier (5)
28
                   StructSpecifier (5)
29
                     STRUCT
30
                     Tag (5)
31
                        ID: uint8 t
32
                FunDec (5)
                   ID: intset_search
34
                   LΡ
35
                   VarList (5)
36
                     ParamDec (5)
37
                        Specifier (5)
38
                          StructSpecifier (5)
39
                            STRUCT
40
                            Tag (5)
41
                              ID: intset
42
                       VarDec (5)
43
                          ID: is
44
                     COMMA
45
                     VarList (5)
                        ParamDec (5)
47
                          Specifier (5)
48
                            StructSpecifier (5)
49
                              STRUCT
50
                              Tag (5)
51
                                 ID: int64_t
52
                          VarDec (5)
53
                            ID: value
                        COMMA
55
                        VarList (5)
56
                          ParamDec (5)
57
                            Specifier (5)
                              StructSpecifier (5)
```

```
STRUCT
60
                                 Tag (5)
                                    ID: uint32_t
62
                             VarDec (5)
63
                               ID: pos
64
                   RP
                 CompSt (5)
66
                   LC
67
                   DefList (6)
68
                      Def (6)
                        Specifier (6)
70
                          TYPE: int
71
                        DecList (6)
72
                          Dec (6)
73
                            VarDec (6)
74
                               ID: min
75
                             ASSIGNOP
76
                            Exp (6)
77
                               INT: 0
78
                          COMMA
79
                          DecList (6)
80
                             Dec (6)
81
                               VarDec (6)
82
                                 ID: max
83
                               ASSIGNOP
84
                               Exp (6)
85
                                 Exp (6)
                                    ID: intrev32ifbe
87
                                    LΡ
88
                                    Args (6)
89
                                      Exp (6)
90
                                         Exp (6)
```

| ı | |
|-----|---------------------|
| 92 | ID: is |
| 93 | DOT |
| 94 | ID: length |
| 95 | RP |
| 96 | MINUS |
| 97 | Exp (6) |
| 98 | INT: 1 |
| 99 | COMMA |
| 100 | DecList (6) |
| 101 | Dec (6) |
| 102 | VarDec (6) |
| 103 | ID: mid |
| 104 | ASSIGNOP |
| 105 | Exp (6) |
| 106 | MINUS |
| 107 | Exp (6) |
| 108 | INT: 1 |
| 109 | SEMI |
| 110 | DefList (7) |
| 111 | Def (7) |
| 112 | Specifier (7) |
| 113 | StructSpecifier (7) |
| 114 | STRUCT |
| 115 | Tag (7) |
| 116 | ID: int64_t |
| 117 | DecList (7) |
| 118 | Dec (7) |
| 119 | VarDec (7) |
| 120 | ID: cur |
| 121 | ASSIGNOP |
| 122 | Exp (7) |
| 123 | MINUS |

| 124 | Exp (7) |
|-----|------------------|
| 124 | INT: 1 |
| 125 | SEMI |
| 126 | |
| 127 | StmtList (9) |
| 128 | Stmt (9) |
| 129 | IF |
| 130 | LP |
| 131 | Exp (9) |
| 132 | Exp (9) |
| 133 | ID: intrev32ifbe |
| 134 | LP |
| 135 | Args (9) |
| 136 | Exp (9) |
| 137 | Exp (9) |
| 138 | ID: is |
| 139 | DOT |
| 140 | ID: length |
| 141 | RP |
| 142 | RELOP |
| 143 | Exp (9) |
| 144 | INT: 0 |
| 145 | RP |
| 146 | Stmt (9) |
| 147 | CompSt (9) |
| 148 | LC |
| 149 | StmtList (10) |
| 150 | Stmt (10) |
| 151 | IF |
| 152 | LP |
| 153 | Exp (10) |
| 154 | ID: pos |
| 155 | RP |

```
Stmt (10)
156
                                       Exp (10)
157
                                          Exp (10)
158
                                            ID: pos
159
                                         ASSIGNOP
160
                                         Exp (10)
161
                                            INT: 0
162
                                       SEMI
163
                                  StmtList (11)
164
                                     Stmt (11)
165
                                       RETURN
166
                                       Exp (11)
167
                                          INT: 0
168
                                       SEMI
169
                               RC
170
                          ELSE
171
                          Stmt (12)
172
                             CompSt (12)
173
                                LC
174
                                StmtList (13)
175
                                  Stmt (13)
176
                                     ΙF
177
                                     LP
178
                                     Exp (13)
179
                                       Exp (13)
180
                                          ID: value
181
                                       RELOP
182
                                       Exp (13)
183
                                          ID: intset_get
184
                                          LP
185
                                          Args (13)
186
                                            Exp (13)
187
```

| 1 | |
|-----|------------------|
| 188 | ID: is |
| 189 | COMMA |
| 190 | Args (13) |
| 191 | Exp (13) |
| 192 | ID: max |
| 193 | RP |
| 194 | RP |
| 195 | Stmt (13) |
| 196 | CompSt (13) |
| 197 | LC |
| 198 | StmtList (14) |
| 199 | Stmt (14) |
| 200 | IF |
| 201 | LP |
| 202 | Exp (14) |
| 203 | ID: pos |
| 204 | RP |
| 205 | Stmt (14) |
| 206 | Exp (14) |
| 207 | Exp (14) |
| 208 | ID: pos |
| 209 | ASSIGNOP |
| 210 | Exp (14) |
| 211 | ID: intrev32ifbe |
| 212 | LP |
| 213 | Args (14) |
| 214 | Exp (14) |
| 215 | Exp (14) |
| 216 | ID: is |
| 217 | DOT |
| 218 | ID: length |
| 219 | RP |

| 220 | SEMI |
|-----|----------------|
| 221 | StmtList (15) |
| 222 | Stmt (15) |
| 223 | RETURN |
| 224 | Exp (15) |
| 225 | INT: 0 |
| 226 | SEMI |
| 227 | RC |
| 228 | ELSE |
| 229 | Stmt (16) |
| 230 | IF |
| 231 | LP |
| 232 | Exp (16) |
| 233 | Exp (16) |
| 234 | ID: value |
| 235 | RELOP |
| 236 | Exp (16) |
| 237 | ID: intset_get |
| 238 | LP |
| 239 | Args (16) |
| 240 | Exp (16) |
| 241 | ID: is |
| 242 | COMMA |
| 243 | Args (16) |
| 244 | Exp (16) |
| 245 | INT: 0 |
| 246 | RP |
| 247 | RP |
| 248 | Stmt (16) |
| 249 | CompSt (16) |
| 250 | LC |
| 251 | StmtList (17) |

| 252 | Stmt (17) |
|-----|---------------|
| 253 | IF |
| 254 | LP |
| 255 | Exp (17) |
| 256 | ID: pos |
| 257 | RP |
| 258 | Stmt (17) |
| 259 | Exp (17) |
| 260 | Exp (17) |
| 261 | ID: pos |
| 262 | ASSIGNOP |
| 263 | Exp (17) |
| 264 | INT: 0 |
| 265 | SEMI |
| 266 | StmtList (18) |
| 267 | Stmt (18) |
| 268 | RETURN |
| 269 | Exp (18) |
| 270 | INT: 0 |
| 271 | SEMI |
| 272 | RC |
| 273 | RC |
| 274 | StmtList (22) |
| 275 | Stmt (22) |
| 276 | WHILE |
| 277 | LP |
| 278 | Exp (22) |
| 279 | Exp (22) |
| 280 | ID: max |
| 281 | RELOP |
| 282 | Exp (22) |
| 283 | ID: min |

| 284 | RP |
|-----|---------------|
| 285 | Stmt (22) |
| 286 | CompSt (22) |
| 287 | LC |
| 288 | StmtList (23) |
| 289 | Stmt (23) |
| 290 | Exp (23) |
| 291 | Exp (23) |
| 292 | ID: mid |
| 293 | ASSIGNOP |
| 294 | Exp (23) |
| 295 | ID: shr |
| 296 | LP |
| 297 | Args (23) |
| 298 | Exp (23) |
| 299 | LP |
| 300 | Exp (23) |
| 301 | Exp (23) |
| 302 | ID: min |
| 303 | PLUS |
| 304 | Exp (23) |
| 305 | ID: max |
| 306 | RP |
| 307 | COMMA |
| 308 | Args (23) |
| 309 | Exp (23) |
| 310 | INT: 1 |
| 311 | RP |
| 312 | SEMI |
| 313 | StmtList (24) |
| 314 | Stmt (24) |
| 315 | Exp (24) |

| 316 | Exp (24) |
|-----|----------------|
| 317 | ID: cur |
| 318 | ASSIGNOP |
| 319 | Exp (24) |
| 320 | ID: intset_get |
| 321 | LP |
| 322 | Args (24) |
| 323 | Exp (24) |
| 324 | ID: is |
| 325 | COMMA |
| 326 | Args (24) |
| 327 | Exp (24) |
| 328 | ID: mid |
| 329 | RP |
| 330 | SEMI |
| 331 | StmtList (25) |
| 332 | Stmt (25) |
| 333 | IF |
| 334 | LP |
| 335 | Exp (25) |
| 336 | Exp (25) |
| 337 | ID: value |
| 338 | RELOP |
| 339 | Exp (25) |
| 340 | ID: cur |
| 341 | RP |
| 342 | Stmt (25) |
| 343 | CompSt (25) |
| 344 | LC |
| 345 | StmtList (26) |
| 346 | Stmt (26) |
| 347 | Exp (26) |

| 348 | Exp (26) |
|-----|---------------|
| 349 | ID: min |
| 350 | ASSIGNOP |
| 351 | Exp (26) |
| 352 | Exp (26) |
| 353 | ID: mid |
| 354 | PLUS |
| 355 | Exp (26) |
| 356 | INT: 1 |
| 357 | SEMI |
| 358 | RC |
| 359 | ELSE |
| 360 | Stmt (27) |
| 361 | IF |
| 362 | LP |
| 363 | Exp (27) |
| 364 | Exp (27) |
| 365 | ID: value |
| 366 | RELOP |
| 367 | Exp (27) |
| 368 | ID: cur |
| 369 | RP |
| 370 | Stmt (27) |
| 371 | CompSt (27) |
| 372 | LC |
| 373 | StmtList (28) |
| 374 | Stmt (28) |
| 375 | Exp (28) |
| 376 | Exp (28) |
| 377 | ID: max |
| 378 | ASSIGNOP |
| 379 | Exp (28) |

| 380 | Exp (28) |
|-----|---------------|
| 381 | ID: mid |
| 382 | MINUS |
| 383 | Exp (28) |
| 384 | INT: 1 |
| 385 | SEMI |
| 386 | RC |
| 387 | ELSE |
| 388 | Stmt (29) |
| 389 | CompSt (29) |
| 390 | LC |
| 391 | StmtList (30) |
| 392 | Stmt (30) |
| 393 | Exp (30) |
| 394 | ID: break |
| 395 | SEMI |
| 396 | RC |
| 397 | RC |
| 398 | StmtList (34) |
| 399 | Stmt (34) |
| 400 | IF |
| 401 | LP |
| 402 | Exp (34) |
| 403 | Exp (34) |
| 404 | ID: value |
| 405 | RELOP |
| 406 | Exp (34) |
| 407 | ID: cur |
| 408 | RP |
| 409 | Stmt (34) |
| 410 | CompSt (34) |
| 411 | LC |

| 412 | StmtList (35) |
|-----|---------------|
| 413 | Stmt (35) |
| 414 | IF |
| 415 | LP |
| 416 | Exp (35) |
| 417 | ID: pos |
| 418 | RP |
| 419 | Stmt (35) |
| 420 | Exp (35) |
| 421 | Exp (35) |
| 422 | ID: pos |
| 423 | ASSIGNOP |
| 424 | Exp (35) |
| 425 | ID: mid |
| 426 | SEMI |
| 427 | StmtList (36) |
| 428 | Stmt (36) |
| 429 | RETURN |
| 430 | Exp (36) |
| 431 | INT: 1 |
| 432 | SEMI |
| 433 | RC |
| 434 | ELSE |
| 435 | Stmt (37) |
| 436 | CompSt (37) |
| 437 | LC |
| 438 | StmtList (38) |
| 439 | Stmt (38) |
| 440 | IF |
| 441 | LP |
| 442 | Exp (38) |
| 443 | ID: pos |

```
RP
444
                                           Stmt (38)
445
                                              Exp (38)
446
                                                Exp (38)
447
                                                   ID: pos
448
                                                ASSIGNOP
449
                                                Exp (38)
450
                                                   ID: min
451
                                              SEMI
452
                                        StmtList (39)
453
                                           Stmt (39)
454
                                              RETURN
455
                                              Exp (39)
456
                                                INT: 0
457
                                              SEMI
458
                                      RC
459
                      RC
460
```

4 D 组测试用例

本组测试用例共 3 个,针对不同分组进行测试。对应分组的同学需要输出语法树,提示错误则不得分;其他分组的同学只需要在对应位置提示错误即可,如果打印了语法树,则将视为违规,将会<mark>倒扣分</mark>。

4.1 D-1

输入

```
int main() {
  int x1 = 0703;
  int x2 = 0642;
  int x3 = 0x00ABC20f - 0x08048000 * 0XfFffFE - 000256;
}
```

```
Program (1)
     ExtDefList (1)
2
       ExtDef (1)
3
          Specifier (1)
            TYPE: int
5
          FunDec (1)
            ID: main
            LΡ
            RP
         CompSt (1)
10
            LC
11
            DefList (2)
              Def (2)
13
                 Specifier (2)
14
                   TYPE: int
15
                 DecList (2)
                   Dec (2)
17
                     VarDec (2)
18
                        ID: x1
19
                     ASSIGNOP
20
                     Exp (2)
21
                        INT: 451
22
                 SEMI
23
              DefList (3)
24
                 Def (3)
25
                   Specifier (3)
26
                     TYPE: int
27
                   DecList (3)
28
                     Dec (3)
                       VarDec (3)
30
                          ID: x2
31
```

```
ASSIGNOP
32
                        Exp (3)
33
                          INT: 418
34
                   SEMI
35
                 DefList (4)
36
                   Def (4)
                      Specifier (4)
38
                        TYPE: int
39
                      DecList (4)
40
                        Dec (4)
41
                          VarDec (4)
                             ID: x3
43
                          ASSIGNOP
44
                          Exp (4)
45
                             Exp (4)
46
                               Exp (4)
47
                                  INT: 11256335
48
                               MINUS
49
                               Exp (4)
                                  Exp (4)
51
                                    INT: 134512640
52
                                  STAR
53
                                  Exp (4)
54
                                    INT: 16777214
55
                             MINUS
56
                             Exp (4)
57
                               INT: 174
                      SEMI
59
            RC
60
```

说明: 1.1 分组的同学需要输出该语法树, 8 进制和 16 进制数必须正确转换; 其他分组的同学只要提示相应的错误(不输出语法树即)可。

4.2 D-2

输入

```
int main() {
  float e0 = 0.43244e5;
  float e1 = .4e-1;
  float e2 = 88.E002;
  float e3 = e1 + x2 - x4 * .3e+4;
}
```

```
Program (1)
     ExtDefList (1)
       ExtDef (1)
3
         Specifier (1)
           TYPE: int
         FunDec (1)
           ID: main
7
           LΡ
8
           RP
         CompSt (1)
10
           LC
11
           DefList (2)
12
             Def (2)
13
                Specifier (2)
                  TYPE: float
15
                DecList (2)
16
                  Dec (2)
17
                    VarDec (2)
                       ID: e0
19
                    ASSIGNOP
20
                    Exp (2)
21
                      FLOAT: 43244.000000
```

```
SEMI
23
              DefList (3)
24
                Def (3)
25
                   Specifier (3)
26
                     TYPE: float
27
                   DecList (3)
                     Dec (3)
                       VarDec (3)
30
                         ID: e1
31
                      ASSIGNOP
32
                      Exp (3)
33
                        FLOAT: 0.040000
34
                   SEMI
35
                DefList (4)
36
                   Def (4)
37
                     Specifier (4)
38
                       TYPE: float
39
                     DecList (4)
40
                       Dec (4)
                         VarDec (4)
42
                           ID: e2
43
                         ASSIGNOP
44
                         Exp (4)
45
                           FLOAT: 8800.000000
46
                     SEMI
47
                   DefList (5)
48
                     Def (5)
49
                       Specifier (5)
                          TYPE: float
51
                       DecList (5)
52
                          Dec (5)
53
                           VarDec (5)
```

```
ID: e3
55
                              ASSIGNOP
57
                              Exp (5)
                                Exp(5)
58
                                   Exp (5)
59
                                     ID: e1
                                   PLUS
61
                                   Exp (5)
62
                                     ID: x2
63
                                MINUS
                                Exp(5)
                                   Exp (5)
66
                                     ID: x4
67
                                   STAR
68
                                   Exp (5)
69
                                     FLOAT: 3000.000000
70
                         SEMI
71
             RC
72
```

说明: 1.2 分组的同学需要输出语法树,注意科学计数法浮点数的正确转换。其它分组同学 只需要提示相应错误(不输出语法树)即可。

4.3 D-3

输入

```
* required to implement a specific PRNG generating the same sequence
   * in different systems if seeded with the same integer).
   * The original code appears to be under the public domain.
10
   * I modified it removing the non needed functions and all the
11
   * 1960-style C coding stuff...
12
14
15
   * Copyright (c) 2010-2012, Salvatore Sanfilippo <antirez at gmail
16
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19
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32
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      OTHERWISE)
   * ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF
40
      ADVISED OF THE
   * POSSIBILITY OF SUCH DAMAGE.
41
   */
42
43
  struct long;
44
  struct unsigned;
  struct int32 t;
  struct uint32 t;
47
48
  struct uint32 t x[3], a[3], c;
```

```
int N, MASK;
  int X0, X1, X2, A0, A1, A2, C;
  struct long HI BIT;
54
  struct uint32 t LOW(struct uint32 t x) { return and(x, MASK); }
55
  struct uint32 t HIGH(struct uint32 t x) { return LOW(shr((x), N)); }
  int MUL(struct uint32 t x, struct uint32 t y, struct uint32 t z) {
57
      struct uint32 t l = x * y;
58
      (z)[0] = LOW(1);
59
       (z)[1] = HIGH(1);
      return 0;
62
63
  int CARRY(struct uint32 t x, struct long y) { return x + y > MASK; }
  int ADDEQU(struct uint32 t x, struct uint32 t y) { int z = CARRY(x, y
     ); x = LOW(x + y); return z; }
  int SET3(struct uint32_t x[3], struct uint32_t x0, struct uint32_t x1
      , struct uint32_t x2) { (x)[0] = (x0); (x)[1] = (x1); (x)[2] = (x2)
     ); }
  int SEED(struct uint32 t x0, struct uint32 t x1, struct uint32 t x2)
      { SET3(x, x0, x1, x2); SET3(a, A0, A1, A2); c = C; }
  int REST(int tv) {
68
      int i;
      while (i < 3) {
70
           xsubi[i] = x[i]; x[i] = temp[i];
           i = i + 1;
72
      return (v);
75
76
  int init() {
           = 16;
      Ν
```

```
MASK = (shl(1, (N - 1)) + shl(1, (N - 1)) - 1);
79
80
       X0 = 33;
81
       X1 = 34;
82
       X2 = 234;
83
       A0 = 66;
       A1 = 4;
85
       A2 = 5;
86
       C = 8;
87
       HI BIT = shl(1, (2 * N - 1));
90
       x[0] = X0;
91
       x[1] = X1;
92
       x[2] = X2;
       a[1] = A0;
94
       a[2] = A1;
95
       a[3] = A2;
       C = C;
97
   }
98
99
   struct int32 t redisLrand48() {
100
       struct int32 t ret;
101
       next();
102
       ret = (shl(x[2], (N - 1)) + shr(x[1], 1));
103
       return ret;
104
   }
105
106
   int redisSrand48(struct int32 t seedval) {
107
       SEED(X0, LOW(seedval), HIGH(seedval));
108
       return 0;
109
110 }
```

```
111
   int next() {
112
       struct uint32 t p[2], q[2], r[2], carry0, carry1;
113
114
       MUL(a[0], x[0], p);
115
       ADDEQU(p[0], c, carry0);
116
       ADDEQU(p[1], carry0, carry1);
117
       MUL(a[0], x[1], q);
118
       ADDEQU(p[1], q[0], carry0);
119
       MUL(a[1], x[0], r);
120
       x[2] = LOW(carry0 + carry1 + CARRY(p[1], r[0]) + q[1] + r[1] +
121
                a[0] * x[2] + a[1] * x[1] + a[2] * x[0]);
122
       x[1] = LOW(p[1] + r[0]);
123
       x[0] = LOW(p[0]);
124
125
       return 0;
126
127
128
129
    * Common block comment
130
131
   int /** break definition **/ myFunc() {
132
     int abc = 123; // commom line comment
133
     int abc = 123 // break line
134
135
     int abc = 3 /** break line too
136
     */, def = 4;
137
     int def = /** wierd block comment /**** *?
138
       // recurrsive comments
139
       int main() {
140
         int abc = 123; // commom line comment
141
         int abc = 123 // break line
```

```
143
         int abc = 3 /** break line too
144
         *x/, def = 4;
145
         int def = /** wierd block comment /**** *?
146
          // should recusive, stop it
147
         ****x/ 8865;
148
149
     */ 8865;
150
     struct Def def = //\\*//\\*//\\*//\\
151
152
     abc;
```

```
1 // 太长, 暂不放在该文件中。请在群文件里下载。
```

说明: 1.3 分组的同学需要输出语法树,不能提示有语法错误;其他分组同学只需要提示相应错误(不输出语法树)即可。

5 E 组测试用例

本组测试用例共6个,针对不同分组进行测试。

5.1 E1.1

这组测试用例针对 1.1 分组的同学。

输入(E1-1)

```
int main() {
  int x1 = 00703;
  int x2 = 0604209;
  float e3 = e1 + x2 - x4;
  return 0000;
}
```

```
Error type A at Line 3: Illegal oct INT number: "0604209"
```

说明: 仅 1.1 分组的同学需要测试这个用例,针对错误的 8 进制数 0604209,识别成错误类型 B 也可以。

输入(E1-2)

```
struct charstar;
  struct uint16_t crc16tab[256];
3
  struct uint16 t crc16(struct charstar buf, int len) {
      int counter;
5
      struct uint16_t crc;
      crc16tab = (
          0x0000 && 0x1021 && 0x2042 && 0x3063 && 0x4084 && 0x50a5 && 0
             x60c6 && 0x70e7 &&
          0x8108 && 0x9129 && 0xa14a && 0xb16b && 0xc18c && 0xd1ad && 0
10
             xelce && Oxflef &&
          0x1231 && 0x0210 && 0x3273 && 0x2252 && 0x52b5 && 0x4294 && 0
11
             x72f7 && 0x62d6 &&
          0x9339 && 0x8318 && 0xb37b && 0xa35a && 0xd3bd && 0xc39c && 0
12
             xf3ff && 0xe3de &&
          0x2462 && 0x3443 && 0x0420 && 0x1401 && 0x64e6 && 0x74c7 && 0
13
             x44a4 && 0x5485 &&
          0xa56a && 0xb54b && 0x8528 && 0x9509 && 0xe5ee && 0xf5cf && 0
14
             xc5ac && 0xd58d &&
          0x3653 && 0x2672 && 0x1611 && 0x0630 && 0x76d7 && 0x66f6 && 0
15
             x5695 && 0x46b4 &&
          0xb75b && 0xa77a && 0x9719 && 0x8738 && 0xf7df && 0xe7fe && 0
16
             xd79d && 0xc7bc &&
          0x48c4 && 0x58e5 && 0x6886 && 0x78a7 && 0x0840 && 0x1861 && 0
17
             x2802 && 0x3823 &&
          0xc9cc && 0xd9ed && 0xe98e && 0xf9af && 0x8948 && 0x9969 && 0
18
```

| | xa90a && 0xb92b && |
|----|---|
| 19 | 0x5af5 && 0x4ad4 && 0x7ab7 && 0x6u96 && 0x1a71 && 0x0a50 && 0 |
| | x3a33 && 0x2a12 && |
| 20 | 0xdbfd && 0xcbdc && 0xfbbf && 0xeb9e && 0x9b79 && 0x8b58 && 0 |
| | xbb3b && 0xab1a && |
| 21 | 0x6ca6 && 0x7c87 && 0x4ce4 && 0x5cc5 && 0x2a22 && 0x3c03 && 0 |
| | x0c60 && 0x1c41 && |
| 22 | 0xedae && 0xfd8f && 0xcdec && 0xddcd && 0xad2a && 0xbd0b && 0 |
| | x8d68 && 0x9d49 && |
| 23 | 0x7e97 && 0x6eb6 && 0x5ed5 && 0x4ef4 && 0x3e13 && 0x2e32 && 0 |
| | x1e51 && 0x0e70 && |
| 24 | 0xff9f && 0xefbe && 0xdfdd && 0xcffc && 0xbf1b && 0xaf3a && 0 |
| | x9f59 && 0x8f78 && |
| 25 | 0x9188 && 0x81a9 && 0xb1ca && 0xaleb && 0xd10c && 0xc12d && 0 |
| | xf14e && 0xe16f && |
| 26 | 0x1080 && 0x00a1 && 0x30c2 && 0x20e3 && 0x5u04 && 0x4025 && 0 |
| | x7046 && 0x6067 && |
| 27 | 0x83b9 && 0x9398 && 0xa3fb && 0xb3da && 0xc33d && 0xd31c && 0 |
| | xe37f && 0xf35e && |
| 28 | 0x02b1 && 0x1290 && 0x22f3 && 0x32d2 && 0x4235 && 0x5214 && 0 |
| | x6277 && 0x7256 && |
| 29 | 0xb5ea && 0xa5cb && 0x95a8 && 0x8589 && 0xf56e && 0xe54f && 0 |
| | xd52c && 0xc50d && |
| 30 | 0x34e2 && 0x24c3 && 0x14a0 && 0x0481 && 0x7466 && 0x6447 && 0 |
| | x5424 && 0x4405 && |
| 31 | 0xa7db && 0xb7fa && 0x8799 && 0x97b8 && 0xe75f && 0xf77e && 0 |
| | xc71d && 0xd73c && |
| 32 | 0x26d3 && 0x36f2 && 0x0691 && 0x16b0 && 0x6657 && 0x7676 && 0 |
| | x4615 && 0x5634 && |
| 33 | 0xd94c && 0xc96d && 0xo90e && 0xe92f && 0x99c8 && 0x89e9 && 0 |
| | xb98a && 0xa9ab && |
| 34 | 0x5844 && 0x4865 && 0x7806 && 0x6827 && 0x18c0 && 0x08e1 && 0 |

```
x3882 && 0x28a3 &&
           0xcb7d && 0xdb5c && 0xeb3f && 0xfb1e && 0x8bf9 && 0x9bd8 && 0
35
              xabbb && 0xbb9a &&
           0x4a75 && 0x5a54 && 0x6a37 && 0x7a16 && 0x0af1 && 0x1ad0 && 0
36
              x2ab3 && 0x3a92 &&
           0xfd2e && 0xed0f && 0xdd6c && 0xcd4d && 0xbdaa && 0xad8b && 0
              x9de8 && 0x8dc9 &&
           0x7c26 && 0x6c07 && 0x5c64 && 0x4c45 && 0x3ca2 && 0x2c83 && 0
38
              x1ce0 && 0x0cc1 &&
           0xef1f && 0xff3e && 0xcf5d && 0xdf7c && 0xaf9b && 0xbfba && 0
39
              x8fd9 && 0x9ff8 &&
           0x6e17 && 0x7e36 && 0x4e55 && 0x5e74 && 0x2e93 && 0x3eb2 && 0
40
              x0ed1 && 0x1ef0
       );
41
       crc = 0;
42
       counter = 0;
43
      while (counter < len) {</pre>
45
           crc = xor(shl(crc, 8), crc16tab[and(xor(shr(crc, 8), (star(
              buf) + 1)), 0 \times 0.0 FF)]);
           counter = counter + 1;
47
48
       return crc;
50
51
```

```
Error type A at Line 19: Illegal hex INT number: "0x6u96"

Error type A at Line 26: Illegal hex INT number: "0x5u04"

Error type A at Line 33: Illegal hex INT number: "0xo90e"
```

说明: 仅 1.1 分组的同学需要测试这个用例,针对错误的 16 进制数 0x6u96 与 0x5u04 与 0xo90e,识别成错误类型 B 也可以。

5.2 E1.2

这组测试用例针对 1.2 分组的同学。

输入(E2-1)

```
float m() {
    struct d3_t d3;
    float a = 1.e01;
    float b = .1E1;
    float c = .1e.1;
    float d = 0.1e*3;
    d3.e5 = 3;
    return a * b.e + .1e1 * a1.e1;
}
```

输出

```
Error type A at Line 5: Illegal FLOAT number: ".1e.1"

Error type A at Line 6: Illegal FLOAT number: "0.1e"
```

说明: 仅 1.2 分组的同学需要测试这个用例,针对错误浮点数.1e.1 和 0.1e,识别成错误类型 B 也可以。

输入(E2-2)

```
float m() {
   float c = elel;
   float f = .lel;
   float d = e.el;
   return el.el;
}
```

```
Program (1)
ExtDefList (1)
ExtDef (1)
Specifier (1)
TYPE: float
```

```
FunDec (1)
            ID: m
            LP
8
            RP
         CompSt (1)
10
            LC
11
            DefList (2)
12
              Def (2)
13
                Specifier (2)
14
                   TYPE: float
15
                DecList (2)
16
                   Dec (2)
17
                     VarDec (2)
18
                      ID: c
19
                     ASSIGNOP
20
                     Exp (2)
21
                      ID: e1e1
22
                SEMI
23
              DefList (3)
                Def (3)
25
                   Specifier (3)
26
                     TYPE: float
27
                   DecList (3)
28
                     Dec (3)
29
                       VarDec (3)
30
                          ID: f
31
                       ASSIGNOP
32
                       Exp (3)
33
                         FLOAT: 1.000000
34
                   SEMI
35
                DefList (4)
                   Def (4)
```

```
Specifier (4)
38
                         TYPE: float
39
                      DecList (4)
40
                         Dec (4)
41
                           VarDec (4)
42
                              ID: d
                           ASSIGNOP
                           Exp (4)
45
                             Exp (4)
46
                                ID: e
47
                              DOT
                              ID: e1
49
                      SEMI
50
            StmtList (5)
51
               Stmt (5)
52
                 RETURN
53
                 Exp (5)
54
                   Exp(5)
55
                      ID: e1
                    DOT
57
                    ID: e1
58
                 SEMI
59
            RC
```

说明: 仅1.2分组的同学需要测试这个用例。

5.3 E1.3

这组测试用例针对 1.3 分组的同学。

输入(E3-1)

```
/* addist.c - A generic doubly linked list implementation

*
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dot com>
```

```
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```
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27
      ADVISED OF THE
   * POSSIBILITY OF SUCH DAMAGE.
28
30
  /* Add a new node to the list, to tail, containing the specified '
31
     value'
   * pointer as value.
33
   * On error, NULL is returned and no operation is performed (i.e. the
34
   * list remains unaltered).
35
   * On success the 'list' pointer you pass to the function is returned
  struct liststar listAddNodeTail(struct liststart list, struct
     voidstart value)
38
  {
      struct listNodestart node;
39
40
      if ((node = zmalloc(sizeof(star(node)))) == NULL)
41
          return
          /*\}*\/*
43
          } * /
44
          NULL;
45
      node./**?*/value = value;
46
      if (list.len == 0) {
```

```
list.head = list.tail = node//;
48
            node.prev = node.next = NULL;
49
50
       } else {
            node.prev = list.tail;
51
           node.next = NULL;
52
            list.tail.next = node;
            list.tail = node;
55
       list.len = list.len + 1;
56
       return list;
57
59
       unclosed block comment
60
61
62
63
64
```

```
Error type B at Line 48: Missing ";"

Error type A at Line 60: Unclosed comment
```

说明: 仅 1.3 分组的同学需要测试这个用例。第 48 行也可以报在 49 行; 第 60 行也可以报在 61、62、63 或者 64 行,也可以报成语法错误。

输入(E3-2)

are met: * Redistributions of source code must retain the above copyright notice, this list of conditions and the following disclaimer. 10 * Redistributions in binary form must reproduce the above 11 copyright notice, this list of conditions and the following disclaimer 12 in the documentation and/or other materials provided with the 13 distribution. * Neither the name of Redis nor the names of its contributors 14 may be used to endorse or promote products derived from this software 15 without specific prior written permission. 16 17 * THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND 18 CONTRIBUTORS "AS IS" * AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED * IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR 20 PURPOSE * ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT OWNER OR 21 * LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, 22 OR * CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF * SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR 24 * INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY,

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26
       OTHERWISE)
   * ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF
27
      ADVISED OF THE
   * POSSIBILITY OF SUCH DAMAGE.
   * /
30
  /* Duplicate the whole list. On out of memory NULL is returned.
31
   * On success a copy of the original list is returned.
32
   * The 'Dup' method set with listSetDupMethod() function is used
34
    ^{\star} to copy the node value. Otherwise the same pointer value of
35
    * the original node is used as value of the copied node.
36
   * The original list both on success or error is never modified. */
38
  struct liststar listDup(struct liststar orig)
39
40
       struct liststar copy;
       struct listIter iter;
42
       struct listNodestar node;
43
44
       if ((copy = listCreate()) == NULL)
45
           return NULL;
46
       copy.dup = orig.dup;
47
       copy.free = orig.free;
48
       copy.match = orig.match;
49
       listRewind(orig, ref(iter));
50
       while((node = listNext(ref(iter))) != NULL) {
51
           struct voidstart value;
52
53
           if (copy.dup) {
```

```
value = dup(copy,
55
                /*This is a comment.\
                /\//\/*****\/\/\//
57
                if (value == NULL) {
58
                    listRelease(copy);
59
                   return NULL;
61
62
                node.value);
63
                if (value == NULL) {
                    listRelease(copy);
                    return NULL;
66
                }
67
           } else
68
                value = node.value;
69
           if (listAddNodeTail(copy, value) == NULL) {
70
                listRelease(copy);
71
               return NULL;
72
           }
73
74
       return copy;
75
76
```

```
Program (39)

ExtDefList (39)

ExtDef (39)

Specifier (39)

StructSpecifier (39)

STRUCT

Tag (39)

ID: liststar

FunDec (39)
```

```
ID: listDup
10
            LΡ
11
            VarList (39)
12
              ParamDec (39)
13
                 Specifier (39)
14
                   StructSpecifier (39)
                     STRUCT
16
                     Tag (39)
17
                        ID: liststar
18
                 VarDec (39)
19
                   ID: orig
20
            RP
21
         CompSt (40)
22
            LC
23
            DefList (41)
24
               Def (41)
25
                 Specifier (41)
26
                   StructSpecifier (41)
27
                     STRUCT
28
                     Tag (41)
29
                        ID: liststar
30
                 DecList (41)
31
                   Dec (41)
32
                     VarDec (41)
33
                        ID: copy
34
                 SEMI
35
               DefList (42)
                 Def (42)
37
                   Specifier (42)
38
                     StructSpecifier (42)
39
                        STRUCT
40
                        Tag (42)
```

```
ID: listIter
42
                   DecList (42)
43
                     Dec (42)
44
                       VarDec (42)
45
                          ID: iter
46
                   SEMI
47
                DefList (43)
48
                   Def (43)
49
                     Specifier (43)
50
                       StructSpecifier (43)
51
                          STRUCT
                          Tag (43)
53
                            ID: listNodestar
54
                     DecList (43)
55
                        Dec (43)
                          VarDec (43)
57
                           ID: node
58
                     SEMI
59
            StmtList (45)
              Stmt (45)
61
                ΙF
62
                LΡ
63
                Exp (45)
                   Exp (45)
65
                     LΡ
66
                     Exp (45)
67
                       Exp (45)
                          ID: copy
69
                       ASSIGNOP
70
                       Exp (45)
71
                          ID: listCreate
72
                          LΡ
```

```
RP
74
                     RP
75
                   RELOP
76
                   Exp (45)
77
                    ID: NULL
78
                 RP
79
                 Stmt (46)
80
                   RETURN
81
                   Exp (46)
82
                    ID: NULL
83
                   SEMI
84
               StmtList (47)
85
                 Stmt (47)
86
                   Exp (47)
87
                     Exp (47)
88
                       Exp (47)
89
                         ID: copy
90
                       DOT
91
                       ID: dup
                      ASSIGNOP
93
                     Exp (47)
94
                       Exp (47)
95
                         ID: orig
                        DOT
97
                       ID: dup
98
                   SEMI
99
                 StmtList (48)
100
                   Stmt (48)
101
                     Exp (48)
102
                       Exp (48)
103
                          Exp (48)
                            ID: copy
105
```

| 106 | DOT |
|-----|----------------|
| 107 | ID: free |
| 108 | ASSIGNOP |
| 109 | Exp (48) |
| 110 | Exp (48) |
| 111 | ID: orig |
| 112 | DOT |
| 113 | ID: free |
| 114 | SEMI |
| 115 | StmtList (49) |
| 116 | Stmt (49) |
| 117 | Exp (49) |
| 118 | Exp (49) |
| 119 | Exp (49) |
| 120 | ID: copy |
| 121 | DOT |
| 122 | ID: match |
| 123 | ASSIGNOP |
| 124 | Exp (49) |
| 125 | Exp (49) |
| 126 | ID: orig |
| 127 | DOT |
| 128 | ID: match |
| 129 | SEMI |
| 130 | StmtList (50) |
| 131 | Stmt (50) |
| 132 | Exp (50) |
| 133 | ID: listRewind |
| 134 | LP |
| 135 | Args (50) |
| 136 | Exp (50) |
| 137 | ID: orig |

| 138 | COMMA |
|-----|---------------|
| 139 | Args (50) |
| 140 | Exp (50) |
| 141 | ID: ref |
| 142 | LP |
| 143 | Args (50) |
| 144 | Exp (50) |
| 145 | ID: iter |
| 146 | RP |
| 147 | RP |
| 148 | SEMI |
| 149 | StmtList (51) |
| 150 | Stmt (51) |
| 151 | WHILE |
| 152 | LP |
| 153 | Exp (51) |
| 154 | Exp (51) |
| 155 | LP |
| 156 | Exp (51) |
| 157 | Exp (51) |
| 158 | ID: node |
| 159 | ASSIGNOP |
| 160 | Exp (51) |
| 161 | ID: listNext |
| 162 | LP |
| 163 | Args (51) |
| 164 | Exp (51) |
| 165 | ID: ref |
| 166 | LP |
| 167 | Args (51) |
| 168 | Exp (51) |
| 169 | ID: iter |

| 170 | RP | |
|-----|----------------------|--|
| 171 | RP | |
| 172 | RP | |
| 173 | RELOP | |
| 174 | Exp (51) | |
| 175 | ID: NULL | |
| 176 | RP | |
| 177 | Stmt (51) | |
| 178 | CompSt (51) | |
| 179 | LC | |
| 180 | DefList (52) | |
| 181 | Def (52) | |
| 182 | Specifier (52) | |
| 183 | StructSpecifier (52) | |
| 184 | STRUCT | |
| 185 | Tag (52) | |
| 186 | ID: voidstart | |
| 187 | DecList (52) | |
| 188 | Dec (52) | |
| 189 | VarDec (52) | |
| 190 | ID: value | |
| 191 | SEMI | |
| 192 | StmtList (54) | |
| 193 | Stmt (54) | |
| 194 | IF | |
| 195 | LP | |
| 196 | Exp (54) | |
| 197 | Exp (54) | |
| 198 | ID: copy | |
| 199 | DOT | |
| 200 | ID: dup | |
| 201 | RP | |

| ı | |
|-----|---------------|
| 202 | Stmt (54) |
| 203 | CompSt (54) |
| 204 | LC |
| 205 | StmtList (55) |
| 206 | Stmt (55) |
| 207 | Exp (55) |
| 208 | Exp (55) |
| 209 | ID: value |
| 210 | ASSIGNOP |
| 211 | Exp (55) |
| 212 | ID: dup |
| 213 | LP |
| 214 | Args (55) |
| 215 | Exp (55) |
| 216 | ID: copy |
| 217 | COMMA |
| 218 | Args (63) |
| 219 | Exp (63) |
| 220 | Exp (63) |
| 221 | ID: node |
| 222 | DOT |
| 223 | ID: value |
| 224 | RP |
| 225 | SEMI |
| 226 | StmtList (64) |
| 227 | Stmt (64) |
| 228 | IF |
| 229 | LP |
| 230 | Exp (64) |
| 231 | Exp (64) |
| 232 | ID: value |
| 233 | RELOP |

| 234 | Exp (64) |
|-----|-----------------|
| 235 | ID: NULL |
| 236 | RP |
| 237 | Stmt (64) |
| 238 | CompSt (64) |
| 239 | LC |
| 240 | StmtList (65) |
| 241 | Stmt (65) |
| 242 | Exp (65) |
| 243 | ID: listRelease |
| 244 | LP |
| 245 | Args (65) |
| 246 | Exp (65) |
| 247 | ID: copy |
| 248 | RP |
| 249 | SEMI |
| 250 | StmtList (66) |
| 251 | Stmt (66) |
| 252 | RETURN |
| 253 | Exp (66) |
| 254 | ID: NULL |
| 255 | SEMI |
| 256 | RC |
| 257 | RC |
| 258 | ELSE |
| 259 | Stmt (69) |
| 260 | Exp (69) |
| 261 | Exp (69) |
| 262 | ID: value |
| 263 | ASSIGNOP |
| 264 | Exp (69) |
| 265 | Exp (69) |

| 266 | ID: node |
|-----|---------------------|
| 267 | DOT |
| 268 | ID: value |
| 269 | SEMI |
| 270 | StmtList (70) |
| 271 | Stmt (70) |
| 272 | IF |
| 273 | LP |
| 274 | Exp (70) |
| 275 | Exp (70) |
| 276 | ID: listAddNodeTail |
| 277 | LP |
| 278 | Args (70) |
| 279 | Exp (70) |
| 280 | ID: copy |
| 281 | COMMA |
| 282 | Args (70) |
| 283 | Exp (70) |
| 284 | ID: value |
| 285 | RP |
| 286 | RELOP |
| 287 | Exp (70) |
| 288 | ID: NULL |
| 289 | RP |
| 290 | Stmt (70) |
| 291 | CompSt (70) |
| 292 | LC |
| 293 | StmtList (71) |
| 294 | Stmt (71) |
| 295 | Exp (71) |
| 296 | ID: listRelease |
| 297 | LP |
| | |

```
Args (71)
298
                                                             Exp (71)
299
                                                                ID: copy
300
                                                           RP
301
                                                        SEMI
302
                                                     StmtList (72)
303
                                                        Stmt (72)
304
                                                           RETURN
305
                                                           Exp (72)
306
                                                             ID: NULL
307
                                                           SEMI
308
                                                   RC
309
                                     RC
310
                              StmtList (75)
311
                                Stmt (75)
312
                                   RETURN
313
                                   Exp (75)
314
                                     ID: copy
315
                                   SEMI
316
              RC
317
```

说明: 仅 1.3 分组的同学需要测试这个用例,需要输出正确的语法树。

6 结束语

如果对本测试用例有任何疑议,可以写邮件与<mark>屈道涵</mark>助教或者<mark>李聪</mark>助教联系,注意同时抄送给<mark>许老师</mark>。