

Lab 10 - NPN Common-emitter Amplifier

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Introduction

In this lab we are going to practice designing a common-emitter amplifier by biasing in the active region targetting a specific gain.

We want to design the circuit such that the gain is at least $A_V = -200\text{V/V}$, and $I_C = 1\text{ mA}$.

We are going to use $V_+ = -V_- = 15\text{ V}$, $R_L = 10\text{ k}\Omega$, $R_B = 10\text{ k}\Omega$. We can assume that $\beta = 160$ for this transistor.

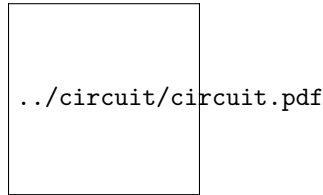


Figure 1: The circuit we use in the lab.

DC Analysis

For the DC analysis, we can assume that the capacitors act as open circuits, and so we can remove everything but the transistor, the sources, and the resistors R_C , R_E , and R_B .

Knowing that $I_B = \frac{I_C}{160}$, we can find that $I_B = 6.25\text{ }\mu\text{A}$, and as such $I_E = 1.00625\text{ mA}$. With $R_B = 10\text{ k}\Omega$, we find that $V_B = I_B \cdot R_B = 0.0625\text{ V}$.

$V_E = V_B - 0.7\text{ V}$ using the constant drop model, so $V_E = -0.6375\text{ V}$. This leads us to our R_E which we can find with $R_E = \frac{V_E - V_{EE}}{I_E} = 14.273\text{ k}\Omega$.

This concludes our DC analysis, we need to do the AC analysis to determine a value for R_C .

AC Analysis

For AC analysis we can replace the capacitors with short circuits, and assume that both of the sources are grounded.

We can find the value for g_m with $g_m = \frac{I_C}{V_T} = 0.0386\text{ A/V}$.

We can now also find R_π . $R_\pi = \frac{\beta}{g_m} = \frac{160}{g_m} = 4.144\text{ k}\Omega$.

The V_{sig} to V_i ratio is shown by

The expression for $A_v = \frac{V_o}{V_i}$ can be shown by $A_v = -g_m(R_C \parallel R_L)$.

With all of those values known, besides R_C , we can find that $R_C \parallel R_L = 5181\text{ }\Omega$, and accordingly $R_C = 10.753\text{ k}\Omega$.

This leads us to find that the value for V_C is 4.247 V , which lines suggests that we are in the active region.

The output resistance R_o that is seen by the load is just $R_o = R_C$.

As always we needed to combine resistors in separate networks to find the resistor values. They are shown in Table 3.

Simulation Results

We built the circuit in multisim and found these values for the simulation.

Simulation	
V_{BE}	0.72 V
V_{CE}	5.10 V
I_C	0.992 mA
I_B	7.11 μ A
I_E	0.994 mA

Table 1: Simulation results.

We found an amplification of $A_v = -176.9$ V/V.

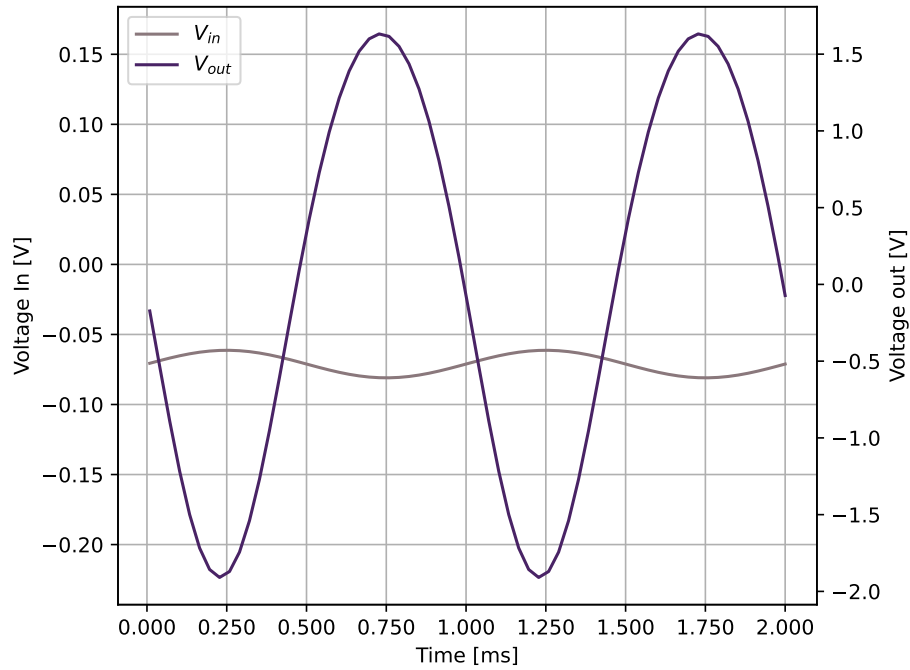


Figure 2: Simulation Results

Measurement Results

Active Mode	
V_{BE}	0.78 V
V_{CE}	5.08 V
I_C	1.01 mA
I_B	4.10 μ A
I_E	1.016 mA

Table 2: Measurement results.

Calculated Resistor	Equivalent Resistor	Measured Resistor	
10 k Ω	10 k Ω	9.90 k Ω	R_B
10.752 k Ω	10.680 k Ω	10.640 k Ω	R_C
	10 k Ω	14.82 k Ω	
	680 Ω	669 Ω	
14.273 k Ω	14.347 k Ω	14.441 k Ω	R_E
	15 k Ω	14.77 k Ω	
	330 k Ω	327 k Ω	
10 k Ω	10 k Ω	9.85 k Ω	R_L

Table 3: Resistors.

Post-Measurement Exercise

We found that $R_L = 1 \text{ M}\Omega$ we got 7.52 V_{pp} , and with $R_L = 6.8 \text{ k}\Omega$ we got 3.3 V_{pp} .