

Lab 9 Report

Ty Davis

November 28, 2023

1 Introduction

In this lab we are studying the DC biasing of an NPN transistor.

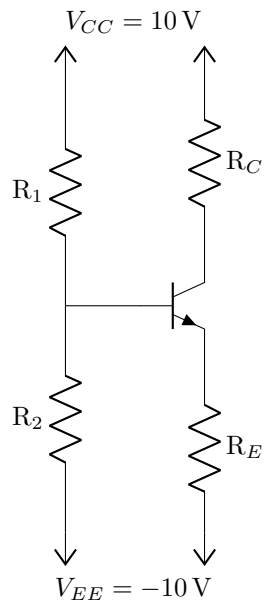


Figure 1: NPN Transistor Circuit