# LAB REPORT: EMBEDDED COMPUTING

LAB II: STEPPER MOTOR CONTROL

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## 1 Objective

The objective of this lab is to adapt the timer frequency to control a stepper motor speed. The timer is used in synchronization mode.

## 2 Control of a stepper motor

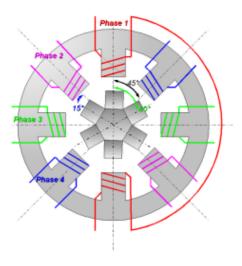


Figure 1: Operating principle of the stepper motor.

The ULN2003 chip is a buffer (Darlington transistor array) that is in charge of the power conversion.

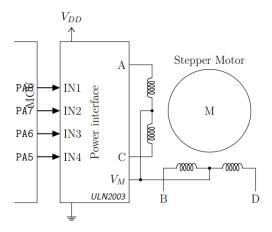


Figure 2: Electronic control interface.

### 3 Control of a stepper motor

To rotate the stepper motor counter-clockwise, we use a table that stores the entire sequence. The table is defined as follows:

```
const unsigned char seq []={8 ,0 xc ,4 ,6 ,2 ,3 ,1 ,9};
```

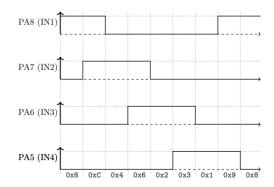


Figure 3: Time sequence for a counter-clockwise rotation.

**Question 1** *Write the setup() function to configure outputs.* 

The code below shows pin configuration by using the pinMode function.

```
"stm32f3xx.h"
   #include
   #include "pinAccess.h"
2
   #include "adc.h"
   void setup() {
5
       // OUTPUTs
6
       for(int i=5; i<9; i++) // PA5 ~ PA8
7
            pinMode(GPIOA,i,OUTPUT);
9
       }
10
   }
11
```

We use in the next two question a direct access to the GPIO, using ODR.

**Question 2** Write the stepCCW() function that updates outputs so that the motor make a single step each time the function is called. Note: a digitalWrite(...) is not appropriate, as the 4 outputs should be updated at the same time. Validate the code with a step by step execution.

The code below shows the setting of the Output Date Register(ODR). The output is updated everytime the StepCCW function is called.

```
"stm32f3xx.h"
   #include
   #include "pinAccess.h"
   #include "adc.h"
   const unsigned char seq[] = {0x8, 0xc, 0x4, 0x6, 0x2, 0x3, 0x1, 0x9};
5
6
   // Counter-Clockwise Rotation using ODR.
   // the 4 outputs are updated at the same time.
8
   void StepCCW(){
9
       static int num = 0;
10
       GPIOA->ODR = seq[num] << 5; // we use PA5 ~ PA8 in this lab.
11
12
       num = num % 8; // num goes up from 0 to 7 in a cycle.
13
   }
14
```

**Question 3** Use a timer so that the stepCCW() function is called @10Hz. The synchronization phase should not use a blocking function (active wait for a flag). There prefered way is a function that just look at the timer status (overflow).

Since the StepCCW function is called at 10Hz, this means that the function is called every 100ms. Therefore, with the internal clock frequency 64 MHz, the prescarler register is set at 64000-1 so the timer have 1ms each tick. Then the ARR register is set at 100-1, meaning that the timer counts 100 ticks so the period is 100ms. The code below give the configuration of the timer and the main function.

```
void setup() {
       // OUTPUTs
2
       for(int i=5; i<9; i++) // PA5 ~ PA8
3
       {
            pinMode(GPIOA,i,OUTPUT);
5
       }
       // TIM6 - input clock = 64MHz.
       RCC->APB1ENR |= RCC_APB1ENR_TIM6EN;
       __asm("nop");
       // reset peripheral (mandatory!)
10
       RCC->APB1RSTR |= RCC_APB1RSTR_TIM6RST;
11
       RCC->APB1RSTR &= ~RCC_APB1RSTR_TIM6RST;
12
       __asm("nop");
13
14
       TIM6->PSC = 64000 - 1; // the input frequency for the counter (1kHz)
15
       TIM6->CR1 |= TIM_CR1_CEN; // enable counter
16
       TIM6->ARR = 100 -1; // stepCCW() is called @10Hz.
17
       // setup Button D6/PB1 (see Question 6)
18
       pinMode(GPIOB, 1, INPUT);
       // setup ADC (see Question 7)
20
       ADCInit();
21
   }
22
23
   int main(void) {
24
       setup();
25
       // Infinite loop
26
       while (1) {
27
            TIM6->SR &= ~TIM_SR_UIF; // reinit overflow flag
28
            StepCCW(); // established in Q2
29
            while(! (TIM6->SR & TIM_SR_UIF)); // synchro
30
       }
31
   }
32
```

**Question 4** update the code to use the BSRR register of the GPIO. Validate the new solution.

According to the lecture slides, BSRR register allows to update a single bit. The 0-15 bits of this register is used to bit setting while the 15-31 bits of the register are used to bit resetting. The code below indicates the usage of the BSRR register.

```
// Counter-Clockwise Rotation using BSRR. BSRR access is atomic.
// the 4 outputs are updated at the same time.
StepCCW(){
```

```
static int num = 0;
// BS (Bit Set)
const unsigned int mask = (seq[num] << 5); // we use PA5 ~ PA8 in this lab.
// BR (Bit Resit)
PORTB->BSRR = mask | (~mask & OxFF) << 16; // BSRR has 32 bits.
num ++;
num = num % 8; // num goes up from 0 to 7 in a cycle.</pre>
```

#### 4 Control of a stepper motor

**Question 5** Write the stepCW() function that works as stepCCW, but in the other direction. The function uses the BSRR GPIO register directly.

```
// Clockwise Rotation using BSRR.
   void StepCW(){
2
       static int num = 0;
3
       // BS (Bit Set)
4
       const unsigned int mask = (seq[num] << 5); // we use PA5 ~ PA8 in this lab.
5
       // BR (Bit Resit)
       PORTB->BSRR = mask | (~mask & 0xFF) << 16; // BSRR has 32 bits.
       num = num + 7;
8
       num = num % 8; // num goes down from 7 to 0 in a cycle.
9
   }
10
```

We now want to mix the two directions.

**Question 6** The stepper motor should turn clockwise when the button (D6) is pressed, and in the other direction when it is released.

```
void Step(){
       static int num = 0;
2
       // BS (Bit Set)
3
       const unsigned int mask = (seq[num] << 5); // we use PA5 ~ PA8 in this lab.
       // BR (Bit Resit)
5
       PORTB->BSRR = mask | (~mask & OxFF) << 16; // BSRR has 32 bits.
       // read from D6/PB1.
7
       int button = digitalRead(GPIOB, 1);
10
       if (button)
           num = (num+1) % 8; // Counter Clockwise
11
       else
12
           num = (num+7) % 8; // Clockwise
13
   }
14
```

#### 5 Rotation Speed

The rotation speed is directly associated to the frequency of the timer. We use here the potentiometer that is associated to the ADC (ADC1, channel 4 on pin PA3). To use the ADC, a set of files adc.c/h are provided.

**Question 7** *Update the timer configuration so that the stepper functions are called from 10Hz (potentiometer set to 0) to 500Hz (potentiometer set to max).* 

The potentiometer now acts as a control input to adjust the calling frequency of the StepCCW function. Thus, to manufacture this feature, the prescaler frequency remains and each tick is 1ms. We change the calling frequency by tuning the ARR register value. And the ARR register value is related to the reading of the potentiometer. The corresponding code is given below.

```
int main(void) {
1
       setup();
2
       // Infinite loop
3
       while (1) {
4
           TIM6->SR &= ~TIM_SR_UIF; // reinit overflow flag
5
           Step(); // established in Q6
           int reading = ADCRead();
7
           TIM6->ARR = (2-100) *reading /4096 + 100 - 1;
8
           while(! (TIM6->SR & TIM_SR_UIF)); // synchro
9
       }
10
   }
11
```

**Question 8** *Test your solution: the stepper motor should change its direction of rotation each time button (D6) is pressed (Finite State Machine associated to the button state).* 

In order to achieve the feature, a finite state machine is used here. There are four states in the finite state machine: RELEASED, PUSHING, PUSHED, RELEASED. If the button is pushed, the input register is connected to the ground and thus the corresponding bit value will be set to 0. And it enters the state machine with the state becomes pushing. Everytime the button D6 is pressed, it enters the state machine and the direction is toggled by using a steptoggle function.

```
// FSM associated to the button state.
   enum PBState {RELEASED, PUSHING, PUSHED, RELEASING};
2
   enum PBState managePB1(){
3
       static enum PBState state = RELEASED;
4
       switch(state) {
5
            case RELEASED: if((GPIOB->IDR & 0x2) == 0) state = PUSHING; break;
            case PUSHING: state = PUSHED; break;
7
           case PUSHED: if(GPIOB->IDR & 0x2) state = RELEASING; break;
            case RELEASING: state = RELEASED; break;
       }
10
       return state;
11
   }
12
   // the stepper motor changes its direction of rotation each time button (D6) is pressed.
13
   void Step_toggle(enum PBState state){
14
       static int num = 0;
15
       // BS (Bit Set)
16
       const unsigned int mask = (seq[num] << 5); // we use PA5 ~ PA8 in this lab.
17
       // BR (Bit Resit)
18
       PORTB->BSRR = mask | (~mask & OxFF) << 16; // BSRR has 32 bits.
19
       // set a flag of clock-wise.
20
       static _Bool CW = 1;
21
       if(state = PUSHING)
22
           CW = !CW;
23
24
       if(!CW)
25
```

```
num = (num+1) \% 8; // CCW
26
        else
27
            num = (num+7) \% 8; // CW
28
   }
29
   // main function
30
   int main(void){
31
       setup();
        enum PBState state;
33
        // Infinite loop
34
       while (1) {
35
            TIM6->SR &= ~TIM_SR_UIF; // reinit overflow flag
            state = managePB1();
37
            Step_toggle(state);
            int reading = ADCRead();
39
            TIM6->ARR = (2-100) *reading /4096 + 100 - 1;
40
            while(! (TIM6->SR & TIM_SR_UIF)); // synchro
41
       }
42
   }
43
```

#### 6 Just a round

There are 64 steps for each round, but there is also a reduction factor of 64. We now want to do just one round. Each time the button D6 is pushed, the stepper performs one round, toggling the direction of rotation.

**Question 9** *Update your application to add this constraint.* 

In this case, we apply an interrupt to the Timer2. The configuration of Timer2 and interrupt handler are shown below.

```
void servoInit(){
       // Step1: Pin configuration
2
       pinAlt(GPIOA, 3, 1); // AF1 for PA3
3
       pinMode(GPIOB, 1, INPUT); // setup Button D6/PB1
4
       // Step2: Timer2 configuration
       RCC->APB1ENR |= RCC_APB1ENR_TIM2EN;
6
       __asm("nop");
       RCC->APB1RSTR |= RCC_APB1RSTR_TIM2RST;
8
       RCC->APB1RSTR &= ~RCC_APB1RSTR_TIM2RST;
       __asm("nop");
10
11
       TIM2->PSC = 64 - 1; //(1MHz)
12
       TIM2->ARR = 20000 -1; //(period: 20ms)
13
       // Step3: PWM configuration
14
       TIM2->CCMR2 &= ~TIM_CCMR2_CC4S_Msk; // channel 4 as output
15
       TIM2->CCMR2 &= ~TIM_CCMR2_OC4M_Msk;
16
       TIM2->CCMR2 |= 6 << TIM_CCMR2_OC4M_Pos; // output PWM mode 1
17
       TIM2->CCMR2 |= TIM_CCMR2_OC4PE; // pre-load register TIM2_CCR4;
18
19
       TIM2->CR1 &= ~TIM_CR1_CMS_Msk; // mode 1 // edge aligned mode
20
       TIM2->CCER |= TIM_CCER_CC4E; // enable
21
       TIM2->CR1 |= TIM_CR1_CEN; // config reg: enable
22
23
       TIM2->CCR4 = 1500 - 1; // 90 degrees
24
```

```
}
25
26
   void setup(){
27
        servoInit();
28
        // init scanning timer
29
        RCC->APB1ENR |= RCC_APB1ENR_TIM6EN;
30
        __asm("nop");
        RCC->APB1RSTR |= RCC_APB1RSTR_TIM6RST;
32
        RCC->APB1RSTR &= ~RCC_APB1RSTR_TIM6RST;
33
34
        TIM6->PSC = 64000 - 1;
        TIM6->CR1 |= TIM_CR1_CEN;
36
        TIM6->ARR = 5-1; // 5ms
37
38
        // Enable interrupt for TIM6
        TIM6->DIER |= TIM_DIER_UIE;
40
        NVIC_EnableIRQ(TIM6_DAC1_IRQn);
41
   }
42
43
   void servoSet(int setpoint){
44
        if(setpoint > 180)
45
            setpoint = 180;
46
        else if(setpoint < 0)</pre>
47
            setpoint = 0;
48
        TIM2->CCR4 = 1000*setpoint/180 + 1000 - 1;
49
   }
50
51
   void TIM6_DAC1_IRQHandler(){
52
        static int sense = 1;
53
        static int setpoint = 0;
        int button = digitalRead(GPIOB, 1);
55
        if(sense){
56
            setpoint++;
57
            if(setpoint > 180) setpoint = 180;
58
            if(setpoint == 180 && button == 0) sense=0;
59
        }
60
        else{
            setpoint --;
62
            if(setpoint < 0) setpoint = 0;</pre>
63
            if(setpoint == 0 && button == 0) sense=1;
64
        }
65
        servoSet(setpoint);
66
        //acknowledge
67
        TIM6->SR &= ~TIM_SR_UIF;
68
   }
69
70
   int main(void){
71
        setup();
72
        // Infinite loop
73
        while(1) {
74
        // empty
75
        }
76
   }
77
```