Tyler Bovenzi

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EXPERIENCE

COMPUTER ENGINEERING INTERN
NEXSTREAM TECHNICAL EDUCATION
San Diego, CA   June 2021 - Jan. 2022
- Optimized legacy code for server use
- Microcontroller to FPGA com. Protocol
- Automated Virtual Machine Containers
- Java, Bash, Python, Verilog

STUDENT ASSISTANT • CAL POLY ITS
San Luis Obispo, CA   Oct. 2019 - Mar. 2020
- Offered customer service to university staff
- Diagnosed software/hardware problems
- Documented solutions

INTERNSHIP PROJECT MANAGER
DRS Daylight Solutions
San Diego, CA   Sep. 2018 – Feb. 2019
- Led a team of engineering interns
- Designed a temperature and air flow rate monitoring embedded system.
- Communicated with clients and members
- Developed software (C++) and PCBs
- Completed calibration testing

JUNIOR ELECTRICAL AND SOFTWARE ENGINEER
Hibotics Incorporated
San Diego, CA   Sep. 2017 – Jun. 2019
- Front and Backend Python Development
- Completed circuit layout and PCB design
- Produced and assembled control board.

AFFILIATIONS

- Eta Kappa Nu - Honor Society   Oct. 2020
- IEEE Member   Sep. 2019
- Cal Poly Medical Design Club   Dec. 2021
- FIRST Robotics Student/Mentor   2013-2019

EDUCATION

BS in COMPUTER ENGINEERING • 2023
California Polytechnic University, San Luis Obispo

SKILLS

- Java, Python, Eagle, CAD, C/C++, Verilog, HTML, GLSL, Linux, ARM assem.
- Electrical Schematic and PCB design
- Electronic Test Equipment
- Soldering and Reflow, PCB Manufacturing
- Milling and Mechanical manufacturing
- Computer Diagnostics and board repair
- Network Administration and IT

PROJECTS

SOFTWARE
- Ray Marched Terrain Generation
- TCP text based communication server/client
- Network Attached Storage and Server
- Personal Website and Portfolio
- Server Hosting, Network Administration

MECHANICAL
- Long Range Pneumatic Cannon
- Computer controlled milling machine- CNC
- Fused Deposition Modeling 3D printer
- Aluminum Mecanum Drive Robot Chassis

ELECTRICAL
- Hexacopter UAV
- Laptop and Desktop repair and assembly
- **Custom SMD circuit boards** for motor/servo
- Real-time GPS tracking device
- Functional 32-bit RISC V MCU on FPGA

WEBSITE: tylerbovenzi.com